

Development of an Improved Simulator for Wireless Data Acquisition in Gas Turbine Engine Testing (WIDAGATE)

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ABSTRACT Due to its cable-free deployment, wireless sensor networks (WSNs) have drawn great attention as a new technique for industrial data acquisition. However, the suitability of wireless technologies for data acquisition in harsh industrial environment (e.g. aero engine test-beds) is still an open question. A definitive study of the impacts of harsh environments on the WSNs is currently lacking, which puts a barrier on the WSN's applicability to safety-critical industrial instrumentation and automation. In this paper, we report the test results of applying WSNs to data acquisition in gas turbine engine testing and the development of a realistic software simulator with the purpose of de-risking the wireless data transmission technology in a project called WIDAGATE. This paper provides an overview of the simulation platform developed and investigates how small-scale tests of a WSN deployed on a real engine were used to validate and improve the simulator platform. This work proposes the realistic modelling of the physical layer (radio channel) when subject to harsh industrial interference during aero engine testing. Based on the validated, realistic physical layer model, different MAC protocols are simulated to demonstrate how this improved simulator can be used to select an appropriate protocol.

1. INTRODUCTION

With the rapid development of wireless techniques, wireless data transmission using sensors to monitor aero engine systems is increasingly being viewed as a means of reducing time to market in engine testing [1]. Current wired communication systems for engine data acquisition involve extensive wiring harnesses, which leads to long, complicated and expensive setup procedures. Using wireless links instead of fixed copper wires has obvious advantages. Wireless sensor networks (WSNs) offer the data collection system flexibility with the potential to save setup costs, reduce deployment time and avoid failure in communication cables by reducing the complexity of the wiring required, which is particularly important for engine testing and instrumentation applications.

However, to ensure reliability it is of critical importance to understand the suitability of the proposed WSN techniques by estimating their performance in such harsh electromagnetic environments. However, the inherent stochastic nature of wireless

communication (i.e. interference, channel fading and medium access control) present challenges for analytical studies wishing to capture the complexity of the whole communication network. Experimentation on large-scale hardware test-beds could be very time consuming and costly, while a small-scale test-bed may not reveal scalability problems.

Discrete event simulators (like NS2 [2], OMNeT++ [3], OPNET[4]) are a standard tool to study communication network protocols. From the viewpoint of discrete event simulation, their capabilities of capturing and repeating the behaviours of a network are equivalent. If same algorithm and mathematical model representing the network layers are used in these simulators, the simulation results will be consistent. This is particularly true to the simulation of MAC/Network/TCP/Application layers, as many good models of the higher layers have been implemented in these simulators and their performances have been well demonstrated.

Although discrete-event simulators have been widely accepted as a tool for studying wireless networks due to its scalability, flexibility and cost efficiency (compared to hardware tests), the main disadvantage of software simulation is its fidelity and reality. These simulators provide a powerful simulation platform, but they mainly use a packet level abstract model (e.g. a Gaussian random generator to determine if a packet is received successfully) representing the complicated physical layer processes. There has been a lack of direct support for highly accurate modelling of complex wireless environments and the bit stream processing in physical layer. In contrast to the wired channel, the wireless channel has a complex influence on the protocol performance and requires in-depth investigation to understand the performance of wireless techniques in an industrial process scenario. In WIDAGATE, a realistic radio channel models is essential to successfully estimate what performance the wireless network can achieve in the harsh environment experienced.

One way of overcoming these problems of scalability of hardware tests and the reality of software simulation is the combination of software simulation with small-scale hardware tests in realistic environments. More specifically, the software simulator can be improved by developing a detailed and realistic physical layer and make use of the small-scale hardware test to validate the model's fidelity. By doing so, a good trade-off between time, cost and accuracy can be achieved.

Another issue pertinent to the WIDAGATE project is the lack of 3D support. In WIDAGATE, most of the sensors are deployed in the limited space around or within the engine, which is regarded as a steel cylinder. As the deployment of the sensor nodes is around an engine in 3 dimensions, a 3D location and radio and interference model is essential to achieve a comprehensive and accurate simulation. In order to achieve a high accuracy of the physical model in engine testing environment, a resolution of centimetre level is desired for both the radio channel model and 3D location of sensor nodes.

In this paper we describe the design and validation of a 3D physical model for the development of a realistic wireless network simulator. Although the final objective of the WIDAGATE simulator is to determine the performance (throughput, latency and fairness) of various MAC protocols in an aerospace environment, the main objective of this paper is to study the integration and validation of a channel model into a discrete simulation environment. The detailed simulation of different MAC protocols and various techniques is beyond the scope of this paper and interested reader are referred to [5]. For illustration purposes, we present the simulation results of CSMA/CA and polling protocols to compare the performances of contention-based and collision-free protocols to demonstrate the use of the simulator for evaluating and selecting suitable MAC protocols in terms of overall network throughput.

The rest of this paper is structured as follows: section 2 gives a brief description of related work. Section 3 presents a system view describing the problem and requirements of WIDAGATE. The node structure of the developed simulator is presented in section 4 followed by sections 5 and 6 which describe the development of the physical model and MAC modules respectively. Hardware tests and simulation validations are presented in section 7. The simulation results of the improved simulator and an application example of the developed simulator are given in section 8.

2. RELATED WORK

OMNeT++ is a modular, discrete-event simulator implemented in C++ and is becoming a popular tool for developing various network simulators. Powered by the OMNeT++ simulation engine [3][6], many extension packages have been developed for WSN. Some examples are Mobility Framework (MF) [7], Castalia [8]. MF focuses on mobile wireless networks with an emphasis on localization and MAC protocols while Castalia is for general purpose simulation of WSN. As a general wireless and mobile networks simulation framework, MiXiM (MiXed siMulator) [9] [10] has been recently developed by integrating several OMNeT++ simulator packages and features such as improved radio channel simulation. Since MiXiM provides a good framework supporting detailed simulation of physical layers allowing users to develop their own radio model, the physical model was built within the framework of MiXiM. Although developed for OMNeT++ the radio channel model could be easily implement in other event-driven platforms such as NS2 or OPNET.

Due to the open source nature of these OMNeT-based WSN simulators, they can be extended to cover almost all possible implementations and features. However, to the best of our knowledge, they have not previously been modified to investigate the impact of electromagnetic interference in harsh industrial environments. In addition, most existing simulation platforms assume a 2D deployment of the sensor nodes and adopt an open-area line-of-sight channel fading model. However, in the engine testing scenario considered in this project, sensor nodes are deployed along the surface of the engine modelled as a

steel cylinder which has significant impact on the wireless channel. As the size of aero engines typically in the range of a few meters with many hundreds of sensors, the node density could be very high.

The WIDAGATE simulation platform has been designed to investigate the suitability of wireless techniques for industrial data acquisition and de-risk the investment in the development of wireless data transmission technologies. The results of such a simulator and the quality of the performance evaluation depends heavily on the accuracy of the simulation environment, e.g., environment interference, network topology and traffic patterns. If the simulator does not reproduce the characteristics of real-world environment, the developed product may fail after deployment in real networks in spite of promising results achieved in simulation. Based on the framework provided by OMNeT++ and MiXiM, we develop the WIDAGATE simulator incorporating realistic radio channel modelling, validation by hardware test-bed and the evaluation of various MAC protocols in a 3D engine environment.

3. SYSTEM OVERVIEW

The simulation platform is intended to simulate an industrial wireless communication network for data acquisition during engine development and testing. The requirements of engine testing lead to the following main design considerations:

- **Simulation environment:** The wireless communication network may have hundreds to thousands of nodes resulting in a high node density which must be supported by the simulator. Therefore simulation models must be hierarchical and components should be modular and reusable.
- **Physical Models:** The simulation platform will need to accurately represent the complex RF (Radio Frequency) and physical conditions encountered in an active aero-engine.
- **Traffic pattern:** In the engine testing, the data traffic is generated by a sampling process at a constant but adjustable sampling frequency. In contrast to many simulators in which the package arrival is assumed to be Poisson distributed, the periodic sensor measurements in our data acquisition system tend to generate a periodic data flow. For the purpose of engine development and condition monitoring, the amount of data generated is huge, which creates a high traffic load in the communication network. Some of the periodically generated sensor data requires real-time processing and fast delivery to the data collectors to ensure safety within the condition monitoring system.
- **Network Topology:** In this application the network dynamics are limited as the sensor nodes are stationary, deployed in a grid-like topology along the surface of the engine and split into spatial sub-groups. Furthermore, considering the requirement of real-time transmission for high priority data, it is essential to improve spectral efficiency and reduce the communication hops. Therefore, an appropriate topology is the cluster tree network [11], as illustrated in Figure

1, where sensor nodes are hierarchically organised into *clusters* consisting of a cluster head (CH) and a set of child sensor nodes (SN). Within a cluster, the CH communicates with its associated child sensors in a one-hop fashion. These CHs are connected in the form of a tree. Once received from the child sensors, the CHs in turn forward the data to the *data collector*, either directly or via a multi-hop path through other intermediate CHs.

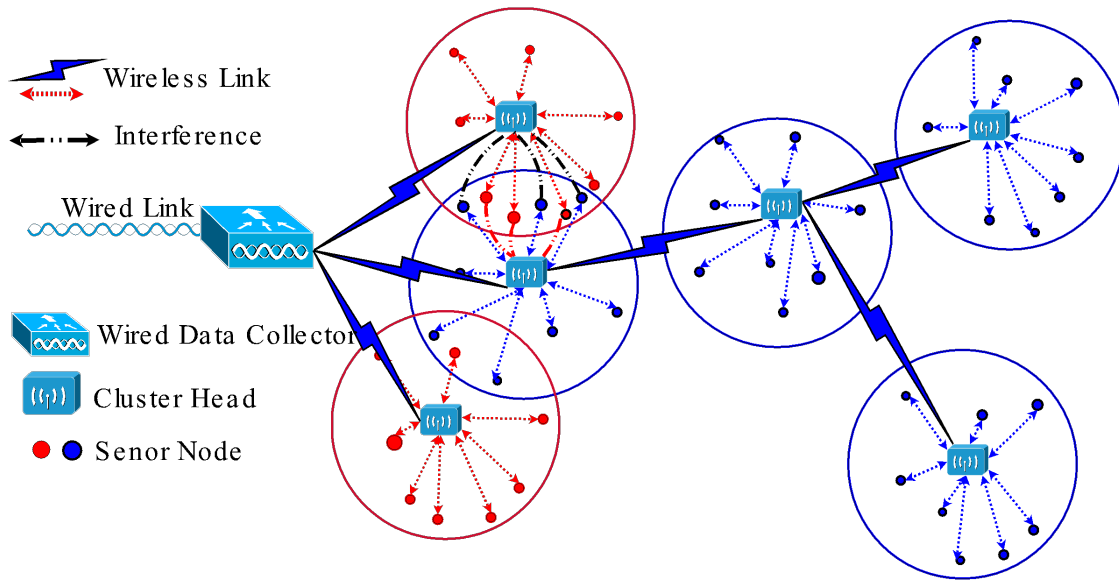


Figure 1 Cluster tree topology

- Protocols and high spectral efficiency:** During engine development testing, many hundreds to thousands of sensors (thermocouples, pneumatic lines and accelerometers) are required to measure and record the temperature, pressure, and vibration. The periodically generated data and high density of sensors result in a huge amount of data to be transmitted across the network. Thus, a high data rate and high spectral efficiency are essential to achieve low latency operation. Considering the relatively short duration of the engine test (usually a few hours), energy consumption is given lower priority in WIDAGATE. Therefore, the IEEE 802.11 protocols of higher data rate are preferred than the low-power-consumption but low data rate IEEE 802.15.4 protocols, although the later may be used for low-sampling-rate sensors (i.e. thermocouples). The overall network throughput is of more interest than the allocation of bandwidth to different traffic categories. Hence, we do not consider different data type and services, as they are related to the high level above the PHY layer and have limited impact on the overall throughput.

In the WIDAGATE cluster network, the communication system is divided into two parts; Single hop Sensor-to-Cluster-head (SN-to-CH) communication within a cluster; Multi-hop Clusterhead-to-Clusterhead (CH-to-CH) relay among cluster heads. In order to improve the spectral efficiency by reducing the channel interference, the CHs are equipped with a double-radio module working at two non-overlapped channels. One is for the SN-to-CH

communication and another one for the CH-to-CH communication. Furthermore, the simulation platform must support various MAC protocols including CSMA/CA, TDMA, polling, etc. in order for studies to be conducted to optimize the network throughput and transmission latency. CSMA is a contention-based, distributed, random channel access control protocol typically used for distributed networks without time synchronization, while TDMA is a collision-free deterministic channel access control protocol that offers high channel utilization but requires good timing and central coordination.

4. NODE MODULES

The simulator consists of a number of different kinds of nodes, such as child sensor nodes, cluster heads, data collectors and gateway node etc. A typical cluster in the network consists of twenty sensor nodes deployed around the surface of one engine module (e.g. the compressor) and two cluster heads (one is the working cluster head and the other one is a backup) are located at the bottom of the engine module. The active CH receives packet from sensor nodes and replies with ACK and commands (e.g. adjust the sampling rate). The backup CH works as a ‘sniffer’ node monitoring the traffic in the air. If the active node does not behave as expected (e.g. no ACK for a series of packets in a row), the backup CH will invoke communication with the active CH through the CH-to-CH channel and decide if the backup CH will replace the active CH. The clusters are pre-determined based on the sensor structure of the aero-engine under test. A wireless node (either sensor node or cluster head) in our simulator includes several modules: the application layer, the IP network layer and a compound NIC (Network Interface Card) module containing the MAC layer and the physical layer. It is worth noting that an additional management sub-layer is inserted between the MAC layer and the network layer to simulate the wireless management, such as authorizing, association and channel assignment. The main task of the application layer is to generate the measurement data periodically according to the sampling rate. The network layer encapsulates the data from the application layer by adding an IP header. In our simulation platform, various NIC modules (e.g. [Nic80211](#) for CSMA/CA in Ad Hoc mode, [NicMacPollSN](#) for sensor node in polling scheme, [NicTDMACN](#) for cluster head in TDMA scheduling, etc.) have been developed to support various MAC protocols.

Each node also contains two support modules. The *mobility* module represents the location and allows for the location of the node to be controlled by end users via a special node gateway. To simplify information sharing among modules in a node, the *nodeinfo* module is introduced to store common and public information, e.g. sampling rate, clustering information and statistics for result analysis.

5. PHYSICAL LAYER MODULE

A realistic channel model specifically for engine testing was built from a series of transmission gain measurements carried out at Rolls-Royce's engine testing site with and without an engine cowling [12]. The developed channel model is comprised of three parts: (a) the transmission gain model (TGM), (b) the interference model and (c) the noise model. Based on the measurements, a specific TGM has been derived for both 2.4GHz ISM band and 5GHz (although only the 2.4GHz band is validated here). The whole transmission gain model $G_T(s, k, f)$ consists of two sub-models [12]

$$G_T(s, k, f) = \bar{G}_T(s, k, f) + \varepsilon(s, k, f) \quad (1)$$

where $\bar{G}_T(s, k, f)$ is the **deterministic model** in dB representing path loss and $\varepsilon(s, k, f)$ is the **Gaussian random model** in dB representing the channel fading. The input variables of the model are path length s , path curvature k and frequency range f . Note that $\varepsilon(s, k, f)$ is a Gaussian process with mean μ and standard deviation σ . Since the values of μ and σ depend on s , k and f , this channel fading model is written in an explicit form. The parameters of the path loss model $\bar{G}_T(s, k, f)$ are identified by using the best fit first degree polynomial to approximate the measurements. The mean and deviation of the channel fading model $\varepsilon(s, k, f)$ are derived from the errors between the measured data and the path loss model $\bar{G}_T(s, k, f)$.

From the viewpoint of a simulator, the radio model's input is the signal sent into the channel by the transmitter. The radio model first adjusts the input signal according to the TGM, and adds noise and interference to the signal. The receiver front-end noise is modeled as additive white Gaussian noise (AWGN). The radio channel model built from the measurements is a waveform model developed in MATLAB/SIMULINK [12], where the transmission signal, interference signal and thermal noise are expressed as their electric fields. However, the waveform model typically uses many samples per bit with a sampling time of 1ns. This model provides greatest accuracy, but requires many samples per bit (for example, a 128Byte packet at 2Mbps data rate at a single wireless link is represented by a $128 \times 8 \times 500 = 500k$ samples per packet). It is therefore computationally prohibitive for simulations of networks comprising many links and large data streams. Therefore, the waveform model is converted into an abstract model (discrete event model), that is suitable for simulating a wireless network with many wireless links.

The interference model has been derived from Environmental Conditions and Test Procedures for Airborne Equipment [13]. There are three kinds of interference waveforms: unmodulated continuous wave (CW), square wave (SW) modulation and pulse modulation (PM). The electric field strength of the interference (V/m) is classified into 17 equipment categories although endusers can also set the interference level manually. The E_{field} is specified by the standard [13], and various values of E_{field} are stored in a look-up table for various classes of equipment as a means to find the right value of E_{field} quickly

without requiring a complex calculation. When generating interference, a value of E_{field} is first retrieved from the look-up table according to the equipment category, and the interference power (in Watts) is calculated as

$$P_{EI}(t) = AEA \times E_{field} \quad (2)$$

where AEA is the Antenna Effective Area given by

$$AEA = \frac{9 \times 10^{16}}{4 f^2 \pi} \quad (3)$$

where f is the carrier frequency. The noise model used in the simulator is given by

$$P_N = k[T_a + (10^{(F/10)} - 1)290]B_N \quad (4)$$

where $k = 1.381 \times 10^{-23}$ J/K is the Boltzman's constant, T_a is antenna effective temperature in K, F is sensor node's noise figure in dB and B_N is the receiver noise bandwidth. Given the transmission power $P_{TX}(t)$ in dB, the receiving power $P_{RX}(t)$ is

$$P_{RX}(t) = P_{TX}(t) + \bar{G}_T(s, k, f) + \varepsilon(\mu, \sigma, f) \quad (5)$$

and the Signal to Interference Noise Ratio (SINR) is given by

$$SINR(t) = \frac{P_{RX}(t)}{P_{EI}(t) + P_{MAI}(t) + N} \quad (6)$$

where $P_{MAI}(t)$ is the multiple access interference (MAI) caused by collisions. $P_{MAI}(t)$ is the sum of all signals that arrive at the receiver at the same time period.

$$P_{MAI}(t) = \sum_{i \neq TX} P_i(t) \quad (7)$$

The bit error ratio (BER) is the ratio of the number of bits incorrectly received to the total number of bits sent during a specified time interval. The relationship between the BER and the SINR depends on the modulation scheme. Under the assumption of AWGN, the BER can be calculated from the SNIR via signal-noise-ratio per bit. The signal-noise-ratio per bit is calculated as

$$\frac{E_b}{N_0} = \frac{P_{RX}/R_b}{N/W} = SINR \cdot \frac{W}{R_b} \quad (8)$$

where W is the bandwidth depending on the PHY layer and R_b is the bit rate depending on the modulation scheme. Given a specific digital modulation scheme, the BER is calculated by

$$P_b = \frac{1}{2} e^{-E_b/N_0} = \frac{1}{2} e^{\left(-SINR \frac{W}{R_b}\right)} \quad (9)$$

for DBPSK/DQPSK, and

$$P_b = \frac{2}{m} \left(1 - \frac{1}{\sqrt{M}} \right) \cdot \text{erfc} \left(\text{SINR} \cdot \frac{W}{R_b} \right) \quad (10)$$

for M -ary QAM (Quadrature Amplitude Modulation) without CCK, and

$$P_b = \frac{2}{m} \left(1 - \frac{1}{\sqrt{M}} \right) \cdot \text{erfc} \left(2 \times \text{SINR} \cdot \frac{W}{R_b} \right) \quad (11)$$

for M -ary QAM (Quadrature Amplitude Modulation) with CCK. Thus, the possibility of correctly receiving a packet is

$$P_{\text{correct}} = (1 - P_b)^l \quad (12)$$

where l is the packet length in bits.

6. MAC LAYER MODULE

The main job of the MAC protocol is to regulate the usage of the medium through a channel access mechanism which allocates the radio channel among competing nodes. Keeping in mind the periodic traffic pattern in engine testing, the latency and throughput requirement, high spectral utilisation is critical to the WIDAGATE application and various MAC protocols have been evaluated to find an appropriate one for this application. Three main MAC protocols, namely, (a) CSMA/CA random access, (b) polling and (c) TDMA scheduling, have been developed for the WIDAGATE wireless network. The standard IEEE 802.11b MAC protocol is implemented by a **Mac80211AdHoc** module for ad hoc mode and a **Mac80211Infr** module for infrastructure mode, and can be used for both SN-to-CH and CH-to-CH communications.

In the polling scheme, which is only used for SN-to-CH communication, the cluster head maintains a polling queue (**qPoll**) storing the addresses of its child sensor nodes (SNs) and polls its SNs in turn as specified by the polling queue. The SN sends data out only when it is polled. A polling process is initiated by the CH when a polling timer is fired. For the purpose of real-time transmission, the polling interval is equal to the sampling interval. CH reads the first element (a SN's address) from **qPoll** and broadcasts a data request packet **POLL**. Only the SN matching the destination address replies with its **DATA** packet, while all other nodes keep silence. Once the required data package is received successfully, the CH replies with an **ACK** before polling the next SN. If the CH fails to receive the packet, re-polling by retransmission is carried out [5]. Hence, the medium access is fully controlled by the CH and collisions are avoided. It is worth noting that no time synchronisation is required here to avoid collisions. The polling sequence can be configured as a fixed sequence according to the SN's priority, or a random sequence implying that all SNs have the equal priority. The channel access is fully controlled by the cluster head which determines the polling sequence and therefore the order of the sensor accessing the channel. The sequence may be pre-determined or random. Two variations of polling mechanisms have been implemented: CSMA/CA-based polling at the application layer (**SNodeAppPoll**) and polling at the MAC layer (**PollMacSN**).

A TDMA scheduling was also implemented for the CH-to-CH communication. The CH nodes have three states, namely idle (**I**), transmit (**T**) and receive (**R**). When a CH node transmits a data packet to its neighbour CH towards the data collector, it may cause interference to other nodes in the receive state. Therefore, a minimum h -hop separation is required between the transmitting CH and other nodes in **R** state. Once a route is selected, a CH is aware of its hop-count and the interference range h hops through an initialization phase and system updates. Therefore, the proposed TDMA scheduling is a combination of TDMA with spacial reuse and the objective is to increase the spectral efficiency by maximizing the number of CH nodes that can send data in the same time-slot. Three TDMA protocols have been implemented. They are parallel line scheduling (PLS), serial line scheduling (SLS) and hybrid line scheduling (HLS). For more technique details of these three scheduling protocols, please refer to [14].

Our modifications of the MAC protocols concern the adaptation of polling and TDMA to meet the spectral efficiency requirement. Although the collision-free TDMA will provide a better performance in terms of spectral efficiency and fixed latency, it is expected that the adoption of TDMA into WSN will face an obstacle of time synchronization. On the other hand, polling does not require time synchronization to avoid collision.

7. Hardware Tests and Simulator Validation

This section provides details of how the developed software simulator is validated against the results of a small-scale engine test, carried at the Rolls-Royce's gas turbine engine testing site. In the validation procedure, a hardware test-bed consisting of a single cluster of seven IEEE 802.11b nodes (SELEX CISP nodes) was first set up and tested on a Rolls-Royce's Gnome engine. The system was configured as a series of star network configurations with the number of sensor nodes (SNs) enabled varying from 1 to 6, all communicating to a single CH. The PER and bit error rate (BER) are estimated from the engine test results and then incorporated into the simulator to improve the physical module in the simulator. Figure 2 shows the deployment of six sensor nodes (C0, C1...C5) mounted onto the engine frame surrounding the surface of the Gnome engine. In all tests, the packet payload length at the IP network layer is 1446 Bytes and, in every second, about 144 data packets in total are generated to simulate the sampling process in engine testing. Each test lasted 15 minutes and is compared with the average of 10 simulations runs.

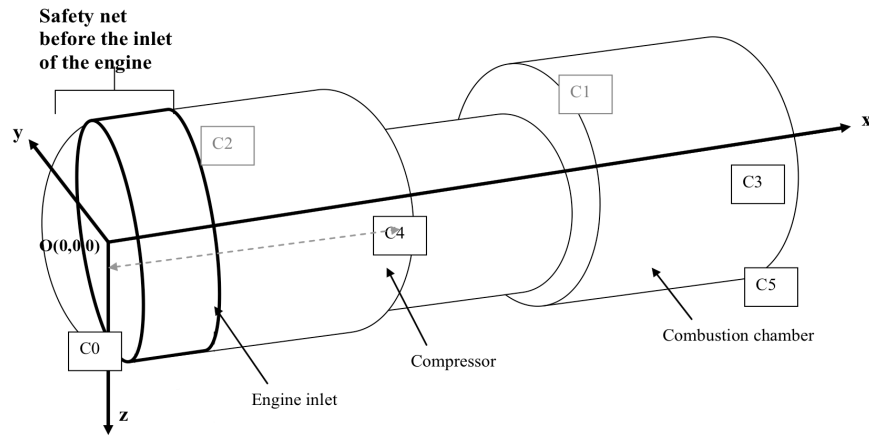


Figure 2 Deployment of the SELEX CISP nodes on the Gnome engine

The results of the single sensor node test work as training data, with the other test results used for validation. In the single sensor node tests, two IEEE 802.11b compatible SELEX CISP nodes are used as the sensor node (transmitter, C2 in Figure 2) and the cluster head (receivers, not shown in Figure 2), respectively to perform a baseline measurement of the PHY layer performance without multiple access interference from other nodes. Furthermore, in our test-bed, the IP/TCP and application layers do not have a re-transmission scheme or an error correction scheme. The maximum number of transmissions is set to 1 to avoid the impacts of re-transmission, as the main focus of our validation is the PHY layer. More parameters of the test-bed are listed in Table I.

Table I Common parameters of the engine tests

Parameters	Values
Physical data rate	2 Mbps
SNR threshold	3dB
Transmission Power	+17dBm
Antenna gain	2dB

Figure 3 shows the data rate (left y-axis) and packet loss (right y-axis) in the single sensor node engine test. These result are also summarized in the third column of Table II which lists the measured performance metrics of the single node engine test in terms of average values of data rate (Mbps) and packet loss (packet per second).

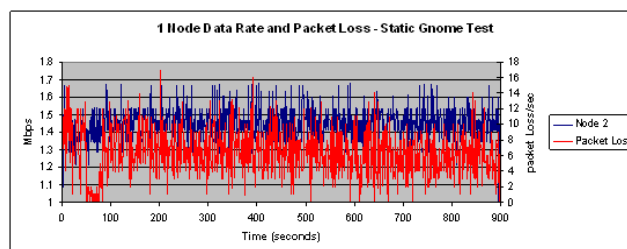


Figure 3 Data rate and packet loss of the single sensor node engine test

For the purpose of bit error rate estimation, in the following analysis, a term ‘traffic load’, measured in ‘packet per second (pkt/s)’, is defined as the number of packets generated and fed into the network in one second. The traffic load is estimated as

$$\text{Traffic load (pkt/s)} = \frac{\text{Data Rate} \times 10^6}{\text{PacketPayload} \times 8} + \text{Packet loss} \quad (13)$$

The packet loss rate β is the ratio between packet loss and traffic load. It is calculated as

$$\beta = \frac{\text{Packet Loss}}{\text{Traffic load}} \quad (14)$$

The packet loss rate β calculated from engine tests (the third row in Table II) is about 4.8%. However, the preliminary simulation results using the parameters in Table I predict that there will be no packet loss in such a single sensor node configuration. This simulation result is not unexpected because, given the transmission power of +17dBm and the relatively small path loss due to the small size of the engine (in a few meters), the receiving signal power is approximately -23dBm and the resulting SNR is over +70dB which is much higher than the required SNR threshold. Therefore, the packet loss due to the noise is nearly zero and no packet loss is observed in simulation. This mismatch between hardware tests and simulation suggested that additional hardware validation was needed to improve the software simulator.

Table II Measured and estimated parameters at engine tests and anechoic chamber tests

	Parameters	Engine Tests	Chamber
Measured parameters	Number of sensor nodes	1	1
	Data rate (Mbps)	1.45	1.500
	Packet loss (Pkt/s)	6.33	1.9
	Packet payload (Byte)	1446	1446
	Maximum MAC retransmission m	1	1
Estimated parameters	Valid traffic (Pkt/s)	125.35	129.67
	Estimated Traffic load (Pkt/s)	131.68	131.57
	Packet loss ratio	4.81%	1.44%
	PER estimate ($m=1$)	0.0481	0.0144
	BER estimate	4.26×10^{-6}	1.26×10^{-6}

As the CISP has a large buffer such that no packet is dropped due to buffer overflow and the higher layers (TCP/IP and above) have no retransmission or error correct scheme, thus, in the single node tests, no packets are lost by the higher layer protocols (i.e. above the PHY layer). Therefore, the packet loss witnessed in these tests must be caused by two factors: (1) Multiple access interference in collision due to MAC protocol; (2) noise and interference at the PHY layer (e.g. thermal noise, environment RF interference, inter-symbol interference (ISI) by multi-path or non-ideal filtering, etc.). However, in our single sensor node configuration, no multiple access interference is present. Furthermore, as analysed in the preceding paragraph, thermal noise would not explain the 4.8% packet loss in the hardware tests. This leaves only the environment RF interference and inter-symbol interference (ISI) by multi-path or non-ideal filtering to explain the packet loss.

In order to further identify the behind reasons for the packet loss in single sensor node, a series of tests were carried out in an anechoic chamber, where the sensor nodes are isolated from environmental RF interference. The anechoic chamber test results of a single sensor node are listed in the last column of Table II, where an average packet loss of about 2 Pkt/s can be seen. This suggests that a packet loss rate $\beta=1.4\%$ exists even in an ideal, RF interference-free, environment.

Comparing the difference of the packet loss ratio between engine tests and the anechoic chamber tests, two conclusions can be made: (1) The packet loss caused by the ISI due to multi-path and/or non-ideal filtering is about 1.4%; (2) Both environmental RF interference and ISI due to multi-path contribute to the packet loss in engine tests. It is reasonable to assume that both engine tests and anechoic chamber tests suffers the same ISI induced packet loss, thus the packet loss due to environment RF interference in engine tests is about $4.8\% - 1.4\% = 3.4\%$.

8. Simulator Improvement

In order to improve the simulator to reproduce the results of the real test-bed, an empirical but realistic packet error rate (PER) and bit error rate (BER) are estimated from the hardware test results and incorporated into the simulator. Since the data packets are 72 times longer ($1446 \times 8 = 11568$ bits) than the 20 Bytes RTS/CTS/ACK control packets, the packet loss due to failed RTS/CTS/ACK packets is negligible in comparison. For example, given a moderate bit error rate of 1×10^{-4} , the possibility of RTS/CTS/ACK having a bit error is $\frac{1-(1-BER)^{1440 \times 8}}{1-(1-BER)^{20 \times 8}} = 43$ times less than the possibility of a corrupted DATA packet. It is reasonable to assume an error-free RTS/CTS/ACK transmission in our tests. Given the maximum number of re-transmissions at the MAC layer is m , the packet error rate (PER) can be estimated as

$$PER = m\sqrt{\beta} \quad (15)$$

Note that, this calculation is only correct when the transmission is collision-free (without multiple access interference), i.e. in single sensor node tests. Since the payload of a DATA packet is $1446 \times 8 = 11568$ bits, the bit error rate is approximately

$$BER = 1 - \sqrt[11568]{(1-PER)} \quad (16)$$

Substituting $m=1$ into above equations, the resulting BERs estimated from the engine tests and anechoic chamber tests are about 4.26×10^{-6} and 1.26×10^{-6} , respectively as detailed in Table II. As the packet error caused by ISI due to non-ideal filtering in the hardware and/or from multipath is irreducible, increasing transmitted power or improving the receiver noise figure will not reduce the packet loss. Hence, it is reasonable and straightforward to incorporate an additional bit error rate in the simulator to account for these hardware limitation and environmental impacts. Furthermore, from the viewpoint of software simulation, only the total BER is needed in order to approximate experimental results and it is not necessary for the simulator to distinguish between the effect of the ISI and environmental RF interference. This can be easily verified by

studying equations (9)-(11). In summary, the simulator is updated by adding an extra BER item α into equations (9)-(11) as follows

$$P_b^{(r)} = P_b + \alpha \quad (17)$$

where $P_b^{(r)}$ is the new bit error rate to be used for simulation, P_b is given by equations (9)-(11) depending on the modulation scheme where α is the BER estimated from the hardware tests.

In order to improve the simulator, a new physical layer parameter called `** .phy .BER` is introduced into the OMNeT++ configuration file so that the BER of the simulator is adjustable to satisfy the engine tests. Note, that because of the introduction of the gateway module in our simulator, the user is also able to adjust the BER to an appropriate value during the simulation. In the simulator core, the SINR-BER model is improved by adding the user specified BER into the mathematically calculated BER. The simulation of single sensor node with an improved BER model gives the average data rate of 1.5Mbps and the packet loss rate of 1.9 packet per second (pps) in the chamber environment and 1.45Mbps data rate and 6.4pps packet loss for the engine test. Comparing this to the results of chamber and engine tests in Table II, it can be seen that a good match between the simulation results and engine test results is achieved which validates the improved simulator.

Furthermore, the improved simulator is cross-validated by comparison with hardware tests with multiple sensor nodes. The comparison between the simulation results and the hardware tests are shown in Figure 4, where the number of sensor nodes increases from 2 to 6. Figure 4 shows good agreement between the simulation and hardware tests, with the maximum percentage error being less than 1.7%. For example, for four sensor nodes, both show the same data rate of about 1.4 Mbps distributed evenly over the four nodes. Similarly to the hardware test, these sensors are organised in a single cluster. A good consistency in terms of network throughput between the software simulation results and the hardware test-bed results is achieved which enhance the reality of the software simulator.

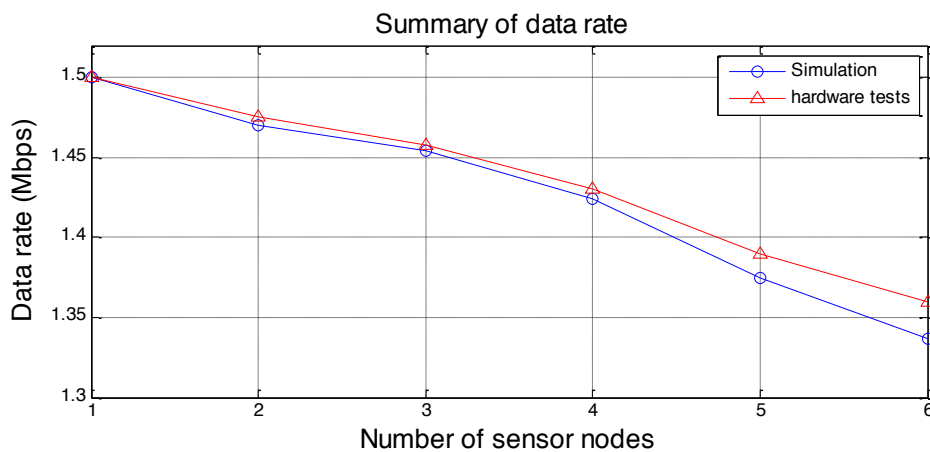


Figure 4 Summary of data rate in simulation and hardware tests

The validated simulator enables us to estimate the performance of the WIDAGATE wireless network with more confidence. In order to demonstrate how the developed realistic simulator aids the network developer and instrument engineer selecting an appropriate protocol for this application, this section presents an example of the simulation results, where the performance of three protocols (IEEE 802.11b CSMA/CA, the CSMA/CA-based polling and direct polling at the MAC layer) for SN-to-CH communication are compared.

Table III Key parameters of network configuration

Common parameters	
Cluster size	{5,6,...,40}
Sampling interval	0.03s
DATA packet Payload	40 Bytes
POLL/ACK packet length	38 Bytes
Parameters of PHY layer (802.11b DSSS, DQPSK, long preamble)	
Data rate at PHY layer	2 Mbps
Transmission power	20 dBm
Noise bandwidth	22 MHz
Parameters of Polling at MAC layer	
POLL-DATA timeout	1.5ms
Parameters of Polling at APP layer	
POLL-DATA timeout	10 ms

The key parameters of the network configuration are listed in Table III. The cluster size denotes the number of sensor nodes in a cluster. The network throughput of CSMA/CA and the proposed polling under various traffic load (measured in cluster sizes) are depicted in Figure 5. The throughput is shown in terms of both how many packets are received by the cluster head in one second and the equivalent payload data throughput (kbps). It can be seen that, at smaller cluster size, the throughput of all three schemes increases linearly with respect to the increasing traffic load but differ at the upper bounds. The CSMA/CA-based polling achieves the upper bound of 150kbps when cluster size is 14. The IEEE 802.11b CSMA/CA achieves the maximum throughput of 275kbps and then decreases when increasing the cluster size further. This shows that the CSMA/CA reaches a saturated condition at 275kbps, which is a drawback of CSMA/CA in high traffic networks. In contrast, the direct polling MAC is capable of 300 kbps (at cluster size of 28) and then stays constant even though the traffic load keeps increasing. This implies that MAC polling is able to double the throughput at moderate and large cluster size compared to the CSMA/CA-based polling.

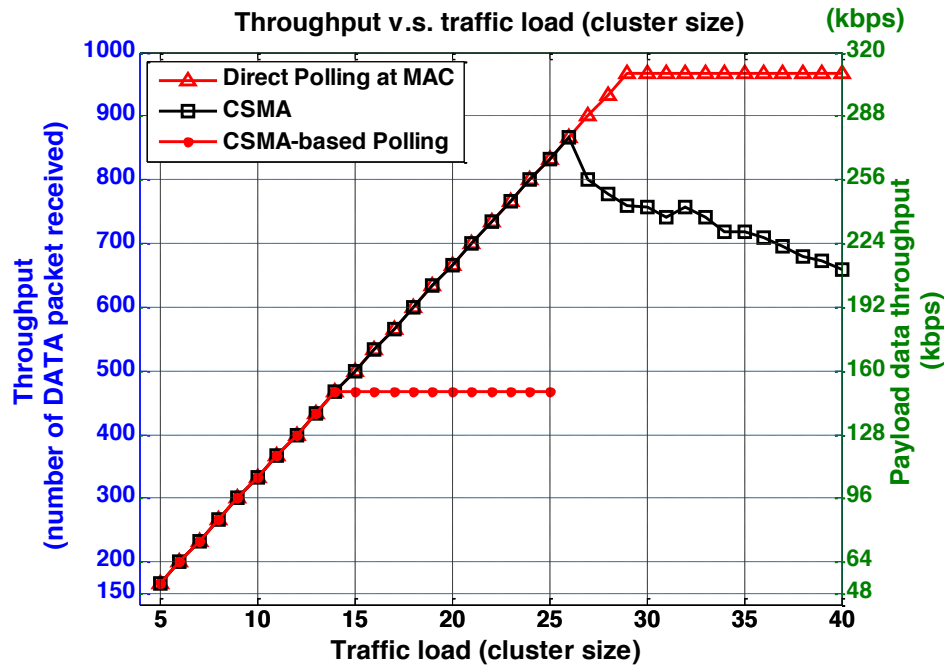


Figure 5 Throughput of CSMA/CA and polling

From the point of view of instrumentation engineers, the simulation results suggest that: (1) for a small network with low traffic load (cluster size less than 14), all these protocols have the same throughput performance. Under low traffic load, the first channel sense and first random back-off in CSMA/CA are able to avoid any collisions. Therefore, the performance of contention-based CSMA/CA is similar to the collision-free polling scheme. In terms of development costs and time-to-market, the IEEE 802.11b is the cheapest and fastest solution, MAC polling is costly and CSMA/CA-based polling could be the trade-off. (2) For a moderate network, the CSMA/CA-based polling fails to meet the throughput requirement. (3) For a large network with cluster size greater than 26, the IEEE 802.11b also fails and the direct polling at MAC is the only choice despite potentially higher product development costs and longer time-to-market. (4) The direct polling MAC also fails when cluster size is greater than 28 and some modifications have to be taken into account to improve the throughput performance. For example, dynamically adjusting the POLL-DATA timeout, or using a multi-polling/multi-ACK scheme (to reduce the overhead of POLL-ACK). Alternatively, some intelligent optimization method may be adopted to optimize the location and polling sequence to improve the throughput. Furthermore, as the polling has a flat throughput when saturated, the polling protocol is more robust than CSMA/CA in industrial applications.

9. CONCLUSIONS

In this paper, we present a realistic wireless sensor network simulation platform for industrial applications in harsh environments. In order to increase the accuracy of such a simulator, a realistic radio channel model has been developed from

a series of transmission gain measurements taken from an engine site and validated by a series of hardware tests. A good match between the simulation results and the hardware tests demonstrates the effectiveness of the proposed scheme and gives endusers confidence in the simulation results. Although the proposed simulation platform is developed for data acquisition during gas turbine engine testing, it is a generic modular framework that provides a solid base and framework for simulating wireless networks for industrial applications and can be easily modified for many industrial applications.

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