Very High Frequency Interleaved Self-Oscillating Resonant SEPIC Converter - DTU Orbit (09/11/2017)

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This paper describes analysis and design procedure of an interleaved, self-oscillating resonant SEPIC converter, suitable for operation at very high frequencies (VHF) ranging from 30 MHz to 300 MHz. The presented circuit consists of two resonant SEPIC DC-DC converters, and a capacitive interconnection

network between the switches which provides self-oscillating and interleaved operation. A design approach to ensure zero voltage switching (ZVS) condition of the MOSFET devices is provided. To verify the proposed method, an 11 W, 50 MHz prototype was built using low-cost VDMOS devices and experimental results are presented. Peak achieved efficiency was 87%.

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