CORE

# Rate of convergence of increasing path-vector routing protocols 

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#### Abstract

A good measure of the rate of convergence of path-vector protocols is the number of synchronous iterations required for convergence in the worst case. From an algebraic perspective, the rate of convergence depends on the expressive power of the routing algebra associated with the protocol. For example in a network of $n$ nodes, shortest-path protocols are guaranteed to converge in $O(n)$ iterations. In contrast the algebra underlying the Border Gateway Protocol (BGP) is in some sense too expressive and the protocol is not guaranteed to converge. There is significant interest in finding well-behaved algebras that still have enough expressive power to satisfy network operators.

Recent theoretical results have shown that by constraining routing algebras to those that are "strictly increasing" we can guarantee the convergence of path-vector protocols. Currently the best theoretical worst-case upper bound for the convergence of such algebras is $O(n!)$ iterations. However in practice it is difficult to find examples that do not converge in $n$ iterations. In this paper we close this gap. We first present a family of network configurations that converges in $\Theta\left(n^{2}\right)$ iterations, demonstrating that the worst case is $\Omega\left(n^{2}\right)$ iterations. We then prove that pathvector protocols with a strictly increasing algebra are guaranteed to converge in $O\left(n^{2}\right)$ iterations. Together these results establish a tight $\Theta\left(n^{2}\right)$ bound. This is another piece of the puzzle in showing that "strictly increasing" is, at least on a technical level, a reasonable constraint for practical policy-rich protocols.


In memory of Abha Ahuja.

## I. Introduction

Complex path-vector protocols such as the Border Gateway Protocol (BGP) [1] provide extremely powerful tools to fine-tune routing configurations. However the very power of the policy language gives rise to anomalies such as nonconvergence, non-deterministic convergence and other problems [2], [3], [4].

One of the more successful frameworks for reasoning about these problems is the algebraic approach [5], [6]. This framework untangles the routing problem being solved from the algorithm being used to solve it. A protocol can therefore be decomposed as protocol $=$ algebra + algorithm. For instance all distance and path-vector protocols use some variant of distributed version of the Bellman-Ford algorithm. However each protocol instantiates the algorithm with a different algebra. For instance RIP's algebra measures shortest paths, whereas BGP's algebra is far more complicated and includes complex conditional policy.

The main advantage of the algebraic approach is that, by considering an abstract algebra, the convergence of all
distance/path-vector protocols can be reasoned about within a single framework. Hence, although much of the more recent work in the area is motivated by BGP, it equally applies to many other path-vector protocols. The model does not yet include protocols that route around congestion or other transient traffic metrics. However protocols such as BGP do not route around congestion.

Recent work has demonstrated that if the underlying algebra is strictly increasing then distance/path-vector protocols are guaranteed to converge in a well-behaved manner [5], [7].

In this paper we address the open question of how quickly convergence occurs for path-vectors protocols with strictly increasing algebras. Existing results [5], [7], [8] have focused on convergence rather than the rate of convergence. When examined closely the convergence proofs suggest a worstcase bound of $O(n!)$ synchronous iterations. Clearly there is potentially a problem here. If increasing algebras can take an exponential number of iterations to converge, then for all practical purposes they might as well not converge at all for networks of any reasonable size. However in practice it appears non-trivial to construct examples that take more than $n$ iterations to converge.

We entirely close this gap by demonstrating an example of a strictly increasing path-vector algebra and a family of graphs $Q_{n}$ that require $\Theta\left(n^{2}\right)$ synchronous iterations to converge. We then prove that in the worst case strictly increasing path algebras converge in $O\left(n^{2}\right)$ iterations. Together these results show that the worst-case is $\Theta\left(n^{2}\right)$.

The $O\left(n^{2}\right)$ proof of convergence has been verified in the theorem prover Agda [9]. Our library, available online [10], also contains the formalised results of [7]. We hope others will be able to use and extend this library to reason about specific routing protocols.

## II. Previous work on convergence

## A. Algebraic work on convergence

The origins of the algebraic approach stems from early work on semiring best-path problems [6]. This approach successfully modelled many simple algebras such as hopcount and shortest-path. However the proofs of correctness of the best-path algorithms all relied on the fact that these algebras were distributive. However work investigating the non-convergence of BGP showed that BGP's algebra violated
this "distributivity" property [11]. Furthermore as distributivity equates to all participants in the network sharing a common preference order over routes, it is unlikely that BGP's algebra could ever be made distributive.

An alternative algebraic property "strict increasingness"1 was proposed by Sobrinho [5]. BGP does not obey such a property, but it is much more natural to suppose that it could as the property only says that the extension of a route must be strictly less preferred than the route itself.

In the original paper Sobrinho showed that if the algebra is strictly increasing then path-vector protocols are guaranteed to converge and reconverge even after arbitrary changes to the network. A more recent paper by Daggitt et al. [7] built upon this and showed strictly increasing algebras are guaranteed to converge to a unique solution even under considerably weaker asynchronous assumptions.

The strictly increasing property therefore looks to be a promising algebraic theory on which to build the next generation of safe-by-design routing protocols. However the rate of convergence of these algebras is still an open question.

## B. Rate of convergence

Measuring the rate of convergence of asynchronous processes is inherently tricky. One obvious approach might be to measure the number of "events" that occur (messages sent/received, table entries updated etc.), however this runs into several problems. Firstly, many of these events happen in parallel and therefore it is not clear that this is an accurate proxy for the actual time required. Secondly, it has been shown that even a simple shortest-paths path-vector protocol may require an exponential number of events to occur in the worst case [12].

Instead we will measure the number of iterations required for the convergence of a synchronous iterative model of pathvector protocols (see Section III-E). This assumes that all nodes propagate their changes instantaneously to each other at the same time. Furthermore given upper bounds on the number of synchronous iterations, the link latency and the loss rate, an upper bound for the time taken for asynchronous convergence can be recovered using the notion of pseudoperiods from [13].

Note that when we talk about the convergence time with respect to the number of synchronous iterations, we are ignoring the $O\left(n^{3}\right)$ work required to compute each iteration. However this work is in some sense uninteresting as it is constant with respect to the algebra and is the portion of work parallelised in the asynchronous version of the protocol. In contrast the iterative component of the work cannot be parallelised and indeed may be adversely affected by the parallelisation.
Labovitz et al. [14] demonstrated through a combination of theory and experimentation that is possible in real life to get BGP to explore all $n$ ! paths. We should note that BGP does not use an increasing algebra, and therefore this does not affect the theoretical bounds we seek.

The worst-case convergence time for semiring algebras with paths over networks with $n$ nodes has long been known to

[^0]be $\Theta(n)$ iterations [15]. As mentioned in Section I, current proofs [5], [8], [7] only suggest a worst-case upper bound of $O(n!)$ for the convergence of strictly increasing algebras with paths. These bounds are derived from the number of simple paths in the network.

## III. An ALGEBRAIC MODEL OF ROUTING

In this section we present our abstract algebraic model of distance-vector routing. The model we describe is taken from [7], which in turn is a refinement of the model proposed by [5]. Section IV presents concrete examples to help the reader to understand the scope and flexibility of this model.

## A. Routing algebras

Definition 1. A routing algebra is a tuple $(S, \oplus, F, \overline{0}, \bar{\infty})$ where:

- $S$ is the set of weights with $\overline{0} \in S$ and $\bar{\infty} \in S$,
- $\oplus: S \times S \rightarrow S$ is the choice operator,
- $F$ is a set of functions from $S \rightarrow S$.
such that:
R1) $\oplus$ is associative $-\forall x, y, z: x \oplus(y \oplus z)=(x \oplus y) \oplus z$.
R2) $\oplus$ is commutative $-\forall x, y: x \oplus y=y \oplus x$.
R3) $\oplus$ is selective $-\forall x, y: x \oplus y \in\{x, y\}$.
R4) $\overline{0}$ is an annihilator for $\oplus-\forall x: x \oplus \overline{0}=\overline{0} \oplus x=\overline{0}$.
R5) $\bar{\infty}$ is an identity for $\oplus-\forall x: x \oplus \bar{\infty}=\bar{\infty} \oplus x=x$.
R6) $\bar{\infty}$ is a fixed point for all $f-\forall f \in F: f(\bar{\infty})=\bar{\infty}$.
The operator $\oplus$ is the decision procedure used by the protocol to choose between two possible weights. Selectivity implies $\oplus$ always returns one of its two arguments. Hence it is possible to define a total order, $\leq$, over the weights as follows:

$$
x \leq y \triangleq x \oplus y=x \quad x<y \triangleq x \leq y \wedge x \neq y
$$

i.e. $x \leq y$ if and only if $x$ is chosen over $y$.

The weights $\overline{0}$ and $\bar{\infty}$ represent the weight of the empty path and the weight of an invalid path respectively. Assumptions R4) and R5) imply that $\forall x \in S: \overline{0} \leq x \leq \bar{\infty}$.

Each function in $F$ represents a procedure to transform a route's weight when that route is extended along an edge. Hence elements of $F$ will be used to label edges in the network (see the following section). Readers may be more familiar with arc weights rather than arc functions. However functions are a strictly more expressive model and can capture conditional routing policies such as BGP route maps. Such complex policies are not easily expressible as edge weights. See Section IV for concrete examples of $F$.

We additionally define the three additional properties. The algebra is distributive if:

$$
\begin{equation*}
\forall f, x, y: f(x \oplus y)=f(x) \oplus f(y) \tag{1}
\end{equation*}
$$

and is increasing if:

$$
\begin{equation*}
\forall f, x: x \leq f(x) \tag{2}
\end{equation*}
$$

and is strictly increasing if:

$$
\begin{equation*}
\forall f, x: x \neq \bar{\infty} \Rightarrow x<f(x) \tag{3}
\end{equation*}
$$

These properties are optional and may or may not be satisfied by a particular routing algebra.

## B. Network configuration, paths, and routes

The network is modelled as a directed graph $G=(V, E)$, where $V$ is a set of $n$ nodes $V=\{0,1, \cdots, n-1\}$ and $E$ is a set of arcs. A configuration of $G$ with respect to a routing algebra $(S, \oplus, F, \overline{0}, \bar{\infty})$ is a mapping from $E$ to $F$. Such mappings will be represented by an $n \times n$ adjacency matrix $\mathbf{A}$ where $\mathbf{A}_{i j} \in F$. We assume there exists the constant function $f_{\bar{\infty}} \in F$ that always returns the invalid weight (i.e. $\left.\forall x \in S: f_{\bar{\infty}}(x)=\bar{\infty}\right)$. This function can be used to represent missing edges.

After changes to the network topology (see Section III-E), nodes may be exchanging routes that contain paths that no longer exist in the current configuration. Therefore our definition of a path must be independent of the current graph $G$ and hence is somewhat non-standard.

A path $p=\left[\left(v_{1}, v_{1}^{\prime}\right), \cdots,\left(v_{m}, v_{m}^{\prime}\right)\right]$ is a (possibly empty) sequence of arcs such that $v_{i}^{\prime}=v_{i+1}$ for all $0 \leq i<m$. However, for the reasons outlined above, the arcs are not members of $E$ but are instead arbitrary members of $\mathbb{N} \times \mathbb{N}$.

For notational convenience we will write $p$ as [] when $m=0$ and as $\left(v_{1}, v_{1}^{\prime}\right):: q$ when $0 \leq i<m$ where $q=\left[\left(v_{2}, v_{2}^{\prime}\right), \cdots,\left(v_{k}, v_{k}^{\prime}\right)\right]$. A path is simple if it never visits a vertex more than once. We also consider a special additional path $\perp$ which represents the invalid path. We will refer to the set of paths as $\mathcal{P}$ and the length of a path $p$ as $|p|$.

## C. Path algebras

Our definition of a routing algebra so far models general distance-vector protocols. However we are interested in the strict subset of path-vector protocols, which track the path that routes are generated along and remove any looping paths.

Given an routing algebra $\mathcal{A}=(S, \oplus, F, \overline{0}, \bar{\infty})$ one way to add paths to $\mathcal{A}$ is as follows:
$\mathbb{P} \mathbb{A}(\mathcal{A})=\left(((S-\{\infty\}) \times \mathcal{P}) \cup\left\{\infty^{\prime}\right\}, \oplus^{\prime}, F^{\prime},(\overline{0},[]), \infty^{\prime}\right)$.
In the augmented algebra, $\mathbb{P A}(\mathcal{A})$, weights are of the form $\infty^{\prime}$ or $(s, p)$ where $s \in S-\{\infty\}$ and $p$ is a path. For all $(s, p)$ we have $(s, p) \oplus^{\prime} \infty^{\prime}=\infty^{\prime} \oplus^{\prime}(s, p)=(s, p)$. Non- $\infty^{\prime}$ routes are compared lexicographically:
$\left(s_{1}, p_{1}\right) \oplus\left(s_{2} p_{2}\right) \triangleq \begin{cases}\left(s_{1}, p_{1}\right) & \text { if } s_{1}=\left(s_{1} \oplus s_{2}\right) \neq s_{2} \\ \left(s_{2}, p_{2}\right) & \text { if } s_{1} \neq\left(s_{1} \oplus s_{2}\right)=s_{2} \\ \left(s_{1}, p_{1} \hat{\oplus} p_{2}\right) & \text { if } s_{1}=s_{2}\end{cases}$ where

$$
p_{1} \hat{\oplus} p_{2} \triangleq \begin{cases}p_{1} & \text { if }\left|p_{1}\right|<\left|p_{2}\right| \\ p_{2} & \text { if }\left|p_{2}\right|<\left|p_{1}\right| \\ \operatorname{dict}\left(p_{1}, p_{2}\right) & \text { otherwise }\end{cases}
$$

where $\operatorname{dict}\left(p_{1}, p_{2}\right)$ returns the smallest path in dictionary order.
We define the concatenation operator $\hat{:}$ that takes an arc and a path. The result of $(i, j) \hat{:} p$ is the invalid path $\perp$ if $i$ is already in the path or if the first node of $p$ is not $j$, otherwise it returns $(i, j):: p$. For example:

$$
\begin{aligned}
(5,3) \hat{:}[(3,4),(4,5)] & =\perp \\
(2,1) \hat{:}[(3,4),(4,5)] & =\perp \\
(2,3) \hat{:}[(3,4),(4,5)] & =[(2,3),(3,4),(4,5)]
\end{aligned}
$$

The $F^{\prime}$ for the path-algebra is the collection of the policy functions $g_{f, u, v}$ which are defined as:

$$
\begin{aligned}
& g_{f, u, v}\left(\infty^{\prime}\right)=\infty^{\prime} \\
& g_{f, u, v}(s, p) \triangleq \begin{cases}\infty^{\prime} & \text { if } f(s)=\infty \\
\infty^{\prime} & \text { if }(u, v) \hat{:} p=\perp \\
(f(s),(u, v) \hat{:} p) & \text { otherwise }\end{cases}
\end{aligned}
$$

However $\mathbb{P A}$ is just one of many possible methods of adding paths to a routing algebra. For example in BGP's algebra, the path is not the last attribute inspected in a lengthy lexicographic best route selection process. Therefore in order to reason about all path-vector protocols, we need to abstract away the exact method of tracking and removing paths.
Definition 2. A path algebra is a tuple $(S, \oplus, F, \overline{0}, \bar{\infty}$, path $)$ where:

- $(S, \oplus, F, \overline{0}, \bar{\infty})$ is a routing algebra
- path : S $\rightarrow \mathcal{P}$ is a function that returns the path the weight was generated along.
such that:
P1) $\operatorname{path}(x)=\perp \Leftrightarrow x=\bar{\infty}$
P2) $\operatorname{path}(x)=[] \Leftarrow x=\overline{0}$
P3) $\operatorname{path}\left(\mathbf{A}_{i j}(x)\right)= \begin{cases}\perp & \text { if } i \in \operatorname{path}(x) \\ \perp & \text { if } j \neq \operatorname{src}(\operatorname{path}(x)) \\ (i, j):: \operatorname{path}(x) & \text { otherwise }\end{cases}$
Note that P3) implies that if a path algebra is increasing it is automatically strictly increasing. As expected $\mathbb{P} \mathbb{A}(\mathcal{A})$ is a path algebra for any $\mathcal{A}$. Its path function is defined as path $\left(\infty^{\prime}\right)=$ $\perp$ and $\operatorname{path}((s, p))=p$.


## D. What problem are we solving?

Let $\mathbb{M}_{n}(S)$ be the set of $n \times n$ matrices over $S$. Each matrix $\mathbf{X} \in \mathbb{M}_{n}(S)$ represents one possible global state of the routing protocol. The row $\mathbf{X}_{i}$ represents the current routing table of node $i$ and $\mathbf{X}_{i j}$ is node $i$ 's current route to node $j$.

Going forwards we will often refer to $\mathbf{X}_{i j}$ as a route from $i$ to $j$. This is a minor abuse of terminology, as $\mathbf{X}_{i j}$ is a weight in $S$ rather than a path in $G$. The intuition behind this terminology is that every valid route $\mathbf{X}_{i j}$ in a path algebra will contain the path it was generated along.

The aim of distance-vector routing protocols is to find an assignment of routes such that each node's route is the best possible extension of the routes offered to it by each of its neighbours. Concretely, in our algebraic model, we are looking for a state $\mathbf{X}$ such that:

$$
\forall i, j: \mathbf{X}_{i j}= \begin{cases}\overline{0} & \text { if } i=j  \tag{4}\\ \bigoplus_{k} \mathbf{A}_{i k}\left(\mathbf{X}_{k j}\right) & \text { otherwise }\end{cases}
$$

We will now represent this series of equations as a single matrix equation by defining some analogues to traditional matrix addition and multiplication in the standard way [11].

If $\mathbf{X}, \mathbf{Y} \in \mathbb{M}_{n}(S)$ we define their sum as

$$
(\mathbf{X} \oplus \mathbf{Y})_{i j} \triangleq \mathbf{X}_{i j} \oplus \mathbf{Y}_{i j}
$$

If $\mathbf{A}$ is an adjacency matrix and $\mathbf{X} \in \mathbb{M}_{n}(S)$ we define the application of $\mathbf{A}$ to $\mathbf{X}$ as:

$$
\mathbf{A}(\mathbf{X})_{i j} \triangleq \bigoplus_{0 \leq k<n} \mathbf{A}_{i k}\left(\mathbf{X}_{k j}\right)
$$

and the identity matrix $I$ as:

$$
\mathbf{I}_{i j}= \begin{cases}\overline{0} & \text { if } i=j \\ \bar{\infty} & \text { otherwise }\end{cases}
$$

Using this notation it is easy to verify that:

$$
\mathbf{X}=\mathbf{A}(\mathbf{X}) \oplus \mathbf{I}
$$

is equivalent to Equation 4.

## E. An iterative solution to the routing problem

The operation of distance-vector and path-vector protocols is closely modelled by the right-hand side of Equation 4. Each node chooses the best route broadcast from routes to all of its neighbours. We therefore define the matrix function $\sigma$ as:

$$
\sigma(\mathbf{X}) \triangleq \mathbf{A}(\mathbf{X}) \oplus \mathbf{I}
$$

The result of $\sigma(\mathbf{X})$ is the state resulting from every node synchronously choosing the best extension of its neighbours' routes in state $\mathbf{X}$. A synchronous version of distance-vector protocols can therefore be modelled as repeatedly applying $\sigma$ to the current state. We use $\sigma^{t}(\mathbf{X})$ to represent $\sigma$ applied $t$ times to $\mathbf{X}$. That is, $\sigma^{0}(\mathbf{X})=\mathbf{X}$ and $\sigma^{t+1}(\mathbf{X})=\sigma\left(\sigma^{t}(\mathbf{X})\right)$. The state $\sigma^{t}(\mathbf{X})$ is therefore the state at time $t$.

See [7] for an asynchronous version of $\sigma$ as well as a proof that if the algebra is strictly increasing, then the synchronous and asynchronous version of $\sigma$ compute the same unique routing solution.

We say $\sigma$ converges from $\mathbf{X}$ if there exists a $k$ such that:

$$
\begin{equation*}
\sigma^{k}(\mathbf{X})=\sigma^{k+1}(\mathbf{X}) \tag{5}
\end{equation*}
$$

and we say $\sigma$ converges if it converges from every $\mathbf{X}$. If Equation 5 holds, then $\sigma^{k}(\mathbf{X})$ is a routing solution since:

$$
\sigma^{k}(\mathbf{X})=\sigma^{k+1}(\mathbf{X})=\sigma\left(\sigma^{k}(\mathbf{X})\right)=\mathbf{A}\left(\sigma^{k}(\mathbf{X})\right) \oplus \mathbf{I}
$$

Why does our model allow arbitrary starting states? The network topology, as described by $\mathbf{A}$, is fixed in the model just described. However in real networks A changes over time as new nodes/links are added, old nodes/links are removed, and policies on links are changed. Obviously if such events continue to occur, then talking about convergence is meaningless. The protocol will only converge if there is a sufficiently long period with no changes to the network. Whenever there is a change to the network, our theory will treat that as an entirely new instance of the routing problem. However old information from the previous configuration is still present in the network and therefore we need to talk about convergence from arbitrary states rather than the initial state, the identity matrix I. These arbitrary states may contain routes that are inconsistent with the new network topology.

## IV. Routing algebra examples

We now present some example routing algebras to demonstrate the flexibility of the model and for use in Section V.

## A. Shortest-path algebras

The most familiar examples can be constructed around shortest-path algebras of the form

$$
(\mathbb{N} \cup\{\infty\}, \min , F, \infty, 0)
$$

where $F$ is a collection of additive functions. We will use the notation $f_{n}$ to denote the function $f_{n}(x)=x+n$. Both min and + are extended in the natural way handle $\infty$, so for example $f_{\infty}(x)=x+\infty=\infty$. Here are few sets of functions that result in useful routing algebras:

$$
\begin{array}{lll}
F_{1} \triangleq\left\{f_{n} \mid n \in \mathbb{N} \cup\{\infty\}\right\} & \text { (increasing) } \\
F_{2} \triangleq\left\{f_{n} \mid n \in \mathbb{N} \cup\{\infty\}, 0<n\right\} & \text { (strictly increasing) } \\
F_{3} \triangleq\left\{f_{1}, f_{\infty}\right\} & \text { (hop-count) }
\end{array}
$$

Suppose that $P$ is some predicate over the weights in $\mathbb{N} \cup\{\infty\}$. Then $g_{n}$ will denote the function $g_{n}(x)=$ if $P(x)$ then $f_{n}(x)$ else $\infty$.

$$
F_{4} \triangleq\left\{g_{1}, f_{\infty}\right\} \quad \text { (hop-count with filtering) }
$$

Such conditional policies violate distributivity.
Any of these algebras could be transformed into a pathalgebra using the $\mathbb{P A}$ construction.

## B. Shortest-widest path algebra

Conditional policies (such as $F_{4}$ above) are not the only way to violate distributivity. This is illustrated by the shortestwidest paths algebra, $\mathbb{S W P}$.

The weights of $\mathbb{S W P P}$ are of the form $(b, d)$ where $b$ is bandwidth and $d$ is length. Thus, $\overline{0} \triangleq(\infty, 0)$ is the best possible route, while $\bar{\infty} \triangleq(0, \infty)$ is the worst possible route (i.e. the weight of the non-existent paths). Weights are compared lexicographically:

$$
(a, b) \oplus(c, d) \triangleq \begin{cases}(a, b) & \text { if } a=\max (a, c) \neq c \\ (c, d) & \text { if } a \neq \max (a, c)=c \\ (a, \min (b, d)) & \text { else if } a=c\end{cases}
$$

That is, routes with higher bandwidth are preferred. If two routes have equal bandwidth, then ties are broken by distance.

Policy functions on arcs, $f_{c, w}$, are defined as

$$
f_{c, w}(b, d)=(\min (c, b), w+d)
$$

Here $c$ can be interpreted as the capacity of an arc and $w$ its length. Note that the first component $\min (c, b)$ represents the bandwidth of the bottleneck link.

To see that this algebra is not distributive, suppose $1<k$. Note that $f_{1,1}((1,1) \oplus(2, k)) \neq f_{1,1}(1,1) \oplus f_{1,1}(2, k)$ since

$$
\begin{aligned}
f_{1,1}((1,1) \oplus(2, k)) & =f_{1,1}(2, k)=(1, k+1) \\
f_{1,1}(1,1) \oplus f_{1,1}(2, k) & =(1,2) \oplus(1, k+1)=(1,2)
\end{aligned}
$$

## C. Stratified shortest paths

We now describe a variant of the Stratified Shortest Path (SSP) algebra (see [16]). We start with the increasing routing algebra:

$$
\mathbb{I N} \mathbb{C} \triangleq(\mathbb{N} \cup\{\infty\}, \min , \quad\{f \mid \forall x: x \leq f(x)\}, 0, \infty)
$$

The weights of $\mathbb{N} \mathbb{C}$ are referred to as levels and policy functions by definition cannot decrease the level.

In this paper we will only use policy functions that can be represented as vectors over $\mathbb{N} \cup\{\infty\}$. The application of the function $f=\left\langle s_{0}, s_{1}, \ldots, s_{k}\right\rangle$ is defined as:

$$
f(i) \triangleq \begin{cases}s_{i} & \text { if } i \leq k \\ \infty & \text { if } i>k \\ \infty & \text { if } i=\infty\end{cases}
$$

For example:

$$
\langle 0, \infty, 17,3\rangle(i)= \begin{cases}0 & \text { if } i=0 \\ \infty & \text { if } i=1 \\ 17 & \text { if } i=2 \\ 3 & \text { if } i=3 \\ \infty & \text { if } i>3 \\ \infty & \text { if } i=\infty\end{cases}
$$

This algebra can implement the customer/provider/peer policies described in [17] \& [18]. Imagine that the level 0 represents routes from customers, 1 represents routes from peers, and 2 represents routes from providers. Then [5] shows that the standard customer/provider/peer policy functions can be represented as:

$$
\begin{array}{lll}
\langle 0, \infty, \infty\rangle & \text { (towards customers) } \\
\langle 1, \infty, \infty\rangle & \text { (towards peers) } \\
\langle 2, \quad 2, & 2\rangle & \text { (towards providers) }
\end{array}
$$

We can then use $\mathbb{P} \mathbb{A}$ to add paths to $\mathbb{N} \mathbb{C}$ :

$$
\mathbb{S P P} \triangleq \mathbb{P A}(\mathbb{I N} \mathbb{C})
$$

We will use $\mathbb{S P P}$ to construct an example of quadratic convergence time in Section V-C.

## V. A FAMILY OF NON-LINEAR CONFIGURATIONS

We will now explore a strictly increasing path algebra and a family of network configurations $Q_{n}$ that take $\Theta\left(n^{2}\right)$ iterations to converge. This demonstrates that the worst-case is $\Omega\left(n^{2}\right)$. In this section we only consider routing towards a fixed destination, node 0 , and therefore ignore all other parts of the global routing state. Also, for notational brevity, we represent paths as sequences of nodes, rather the sequences of edges used elsewhere in the paper.

## A. Review: the classic count-to-convergence problem

The count-to-convergence/infinity problem is a well-known problem that afflicts distance-vector routing protocols [19]. It arises when the weight on a link increases or the link fails. Consider the example in Figure 1 where nodes 1 and 2 seek to find a shortest path route to node 0 . Functions $f_{n}$ are defined as $f_{n}(x)=n+x$. In the top part of the figure the network first converges, then after link $(1,0)$ goes down nodes 1 and 2 still have what is now a "junk route" to 0 and they continually swap this route between them. If the domain is finite (RIP [19] defines $\infty$ as 16 ), then this example will converge.


Fig. 1. Count-to-convergence with the shortest-paths algebra from an arbitrary state. Green cells indicate that the route has improved. Red cells indicate that the route has got worse. Bold indicates that the node has converged.

## B. Count-to-convergence induced by non-distributivity

The underlying cause of the count-to-convergence is that nodes are exchanging "junk routes" that are not consistent with the current network configuration. Using the nondistributive shortest-widest paths algebra ( $\mathbb{S W P}$ ) described in Section IV-B, we now show that junk routes can also arise simply from violations of distributivity (Equation 1).
Figure 2 illustrates how the violation of distributivity described in Section IV-B can generate junk routes and hence count-to-convergence, even in the absence of changes to the network topology. We assume that $k$ is some large even number. Nodes 1 and 2 form what we call a latch. At time 1 node 1 opens the latch by adopting the route generated along the path $[1,0]$. At the same time node 2 loads the route generated along path $[2,0]$. Then at time 2 node 2 closes the latch by adopting the higher bandwidth route along path

|  |  |  |  |  |  |
| :---: | :---: | :---: | :---: | :---: | :---: |
| Time | 0 | 1 | 2 | 3 | 4 |
| 0 | $(\infty, \mathbf{0})$ | $(0, \infty)$ | $(0, \infty)$ | $(0, \infty)$ | $(0, \infty)$ |
| 1 | - | $(\mathbf{2 , k})$ | $(1,1)$ | $(0, \infty)$ | $(0, \infty)$ |
| 2 | - | - | $(\mathbf{2 , k + 1})$ | $(1,2)$ | $(0, \infty)$ |
| 3 | - | - | - | $(1, k+2)$ | $(1,3)$ |
| 4 | - | - | - | $(1,4)$ | $(1, k+3)$ |
| 5 | - | - | - | $(1, k+2)$ | $(1,5)$ |
| 6 | - | - | - | $(1,6)$ | $(1, k+3)$ |
| $\vdots$ | $\vdots$ | $\vdots$ | $\vdots$ | $\vdots$ | $\vdots$ |
| $k$ | - | - | - | $(1, k)$ | $(1, k+3)$ |
| $k+1$ | - | - | - | $(\mathbf{1 , k}, \mathbf{k}+\mathbf{2})$ | $(1, k+1)$ |
| $k+2$ | - | - | - | $(1, k+2)$ | $(\mathbf{1}, \mathbf{k}+\mathbf{3})$ |

Fig. 2. Count-to-convergence with the shortest-widest-paths algebra from the initial state, using a distributivity violation. Red cells indicate that the route has got worse. Bold indicates that the node has converged.
$[2,1,0]$. But at the same time node 3 adopts the junk route through $[3,2,0]$ that it prefers due to the previously described distributivity violation. After time 2 the junk route (and its extensions) are exchanged back-and-forth between nodes 3 and 4. Finally, convergence is reached at time $k+2$.

Path-vector protocols are designed to solve the count-toconvergence problem by prohibiting nodes from adopting routes with a path containing themselves. For example at time 4 the algebra $\mathbb{P} \mathbb{A}(\mathbb{S W P})$ would prevent node 3 from extending node 4's junk route as the resulting path $[3,4,3,2,1,0]$ would have a loop in it. Therefore whereas $\mathbb{S W P}$ requires $k+2$ iterations to converge in this example, $\mathbb{P} \mathbb{A}(\mathbb{S W P})$ would require only 4 iterations.

## C. An example of $O\left(n^{2}\right)$ convergence

We now describe a family of network configurations $Q_{n}$ for $n \geq 3$ designed specifically to repeatedly generate and circulate junk routes as long as possible. $Q_{n}$ has $n$ nodes, labelled 0 through $n-1$. The network $Q_{n}$ has $m$ latches and $m-1$ loops, where $m=\left\lfloor\frac{n-1}{2}\right\rfloor$. The $i^{t h}$ latch contains the nodes $\{2 i-1,2 i\}$ and the $i^{\text {th }}$ loop contains the nodes $\{2 i+$ $1, \cdots, n-1\}$. Figure 3 illustrates the $i^{\text {th }}$ gadget comprised of latch $i$ and loop $i$.

Why does $Q_{n}$ require $\Theta\left(n^{2}\right)$ iterations to converge? The distributivity violation of latch $i$ generates junk routes which propagate around loop $i$ for $O(n)$ iterations. When these junk routes are finally flushed from loop $i$, they trigger latch $i+1$. As there are $O(n)$ gadgets we then obtain the required bounds.

We will now describe the operation of gadget $i$ in detail. Let time $t$ be the activation time of gadget $i$. At this time all nodes in latches 1 through $i-1$ have converged and latch $i$ has just been opened and loaded. That is node $2 i-1$ has opened latch $i$ by accepting the route generated along the path


Fig. 3. The $i^{t h}$ gadget in $Q_{n}$. Junk routes are generated by the latch and then continue to circulate for $n-2 i-1$ iterations in the loop.
[2i-1, 0] and node $2 i$ has loaded the latch by accepting the route generated along the path $[2 i, 0]$. Epoch $i$ then continues:

- At time $t+1$ node $2 i$ closes the latch by adopting the path $[2 i, 2 i-1,0]$. At the same time all the nodes in loop $i$ extend node $2 i$ 's route with the path $[2 i, 0]$.
- At time $t+2$ latch $i$ is closed. Node $2 i$ is offering the route generated along the path $[2 i, 2 i-1,0]$ but due to a distributivity violation, all the nodes in loop $i$ prefer the extensions of the junk route of the path $[2 i, 0]$ that was advertised at time $t+1$. Each node in loop $i$ will accept the extension of the junk route through its counterclockwise neighbour. For example, node $2 i+1$ takes the path $[2 i+1,2 i+2,2 i, 0]$, while node $n-1$ takes the path [ $n-1,2 i+1,2 k, 0]$.
- This process continues with each node in loop $i$ repeatedly accepting the junk routes offered by its counterclockwise neighbour.
- At time $t+n-2 i-1$, it is no longer possible for this to continue as the path of the junk routes will contain every node in loop $i$. For example, node $2 i+1$ takes the path $[2 i+1,2 i+2,2 i+3, \cdots, n-1,2 k, 0]$, while node $n-1$ takes the path $[n-1,2 i+1,2 i+2, \cdots n-2,2 k, 0]$.
- Therefore at time $t+n-2 i$, these junk routes will be discarded and $i+1^{t h}$ gadget will be initialised.
Let $T(n)$ be the convergence time of $Q_{n}$. Consider the general case for $Q_{n+2}$, and the time taken for the 1 st gadget to finish executing. It takes 1 iteration for the junk route to escape the 1st latch, and $n-1$ iterations for the junk route to then be flushed from the 1 st loop, giving a total of $n$ iterations. Nodes $1 \& 2$ will then have converged. If we take these nodes out of the graph, and relabel then we are left with exactly $Q_{n}$. Hence we can formulate a recurrence relation for $T(n)$ as

$$
T(3)=2, \quad T(4)=3, \quad T(n+2)=n+T(n)
$$

This is clearly quadratic by inspection. Due to parity issues,
solving it is messy, but it can be simplified to:

$$
T(n)=\frac{1}{4} n^{2}-\frac{1}{2} n+c
$$

where $c=1$ if $n$ is even and $c=\frac{5}{4}$ when $n$ is odd.

| $i$ | $j$ | Policy | Implements |
| :--- | :--- | :--- | :--- |
| 1 | 0 | $\langle 0\rangle$ | latch 1 open |
| 2 | 0 | $\langle 1\rangle$ | latch 1 load |
| 2 | 1 | $\langle 0\rangle$ | latch 1 close |
| 3 | 0 | $\langle 2\rangle$ | latch 2 open |
| 3 | 2 | $\langle\infty, 1\rangle$ | violation 1 |
| 3 | 4 | $\langle\infty, 1\rangle$ | loop 1 |
| 4 | 0 | $\langle 3\rangle$ | latch 2 load |
| 4 | 2 | $\langle\infty, 1\rangle$ | violation 1 |
| 4 | 3 | $\langle\infty, \infty, 2\rangle$ | latch 2 close |
| 4 | 5 | $\langle\infty, 1\rangle$ | loop 1 |
| 5 | 0 | $\langle 4\rangle$ | latch 3 open |
| 5 | 2 | $\langle\infty, 1\rangle$ | violation 1 |
| 5 | 4 | $\langle\infty, \infty, \infty, 3\rangle$ | violation 2 |
| 5 | 6 | $\langle\infty, 1, \infty, 3\rangle$ | loops 1, 2 |
| 6 | 0 | $\langle 5\rangle$ | latch 3 load |
| 6 | 2 | $\langle\infty, 1\rangle$ | violation 1 |
| 6 | 4 | $\langle\infty, \infty, \infty, 3\rangle$ | violation 2 |
| 6 | 5 | $\langle\infty, \infty, \infty, \infty, 4\rangle$ | latch 3 close |
| 6 | 7 | $\langle\infty, 1, \infty, 3\rangle$ | loops 1, 2 |
| 7 | 0 | $\langle 6\rangle$ | latch 4 open |
| 7 | 2 | $\langle\infty, 1\rangle$ | violation 1 |
| 7 | 4 | $\langle\infty, \infty, \infty, 3\rangle$ | violation 2 |
| 7 | 6 | $\langle\infty, \infty, \infty, \infty, \infty, 5\rangle$ | violation 3 |
| 7 | 8 | $\langle\infty, 1, \infty, 3, \infty, 5\rangle$ | loops 1,2,3 |
| 8 | 0 | $\langle 7\rangle$ | latch 4 load |
| 8 | 2 | $\langle\infty, 1\rangle$ | violation 1 |
| 8 | 3 | $\langle\infty, 1\rangle$ | loop 1 |
| 8 | 4 | $\langle\infty, \infty, \infty, 3\rangle$ | violation 2 |
| 8 | 5 | $\langle\infty, \infty, \infty, 3\rangle$ | loop 2 |
| 8 | 6 | $\langle\infty, \infty, \infty, \infty, \infty, 5\rangle$ | violation 3 |
| 8 | 7 | $\langle\infty, \infty, \infty, \infty, \infty, 5,6\rangle$ | loop 3 and latch 4 close |
|  |  |  |  |

Fig. 4. Constructing the adjacency matrix of $Q_{9}$. This lists the entries of $\mathbf{B}_{i j}$. If $\mathbf{B}_{i j}$ is not listed, then $\mathbf{A}_{i j}=f_{\infty^{\prime}}$. Otherwise, if $f=\mathbf{B}_{i j}$ then $\mathbf{A}_{i j}=g_{f, i, j}$ as defined in Section IV-C. The annotation "violation $i$ " means that the policy implements one of the distributivity violations in gadget $i$ and "loop $i$ " means that the policy implements the loop in gadget $i$.

To implement this scheme, we will use the strictly increasing path algebra $\mathbb{S S P}$ defined in Section IV-C.

We now describe the construction of an adjacency matrix that implements the execution described above. From Figure 4 we can construct the adjacency matrix $\mathbf{A}_{i j}$ of $Q_{9}$. Other instances of $Q_{n}$ are constructed in a similar way.

The figure lists the entries of a matrix $\mathbf{B}_{i j}$. If Figure 4 does not contain an entry for $\mathbf{B}_{i j}$, then $\mathbf{A}_{i j}=f_{\infty^{\prime}}$. Otherwise, if $f=\mathbf{B}_{i j}$ then $\mathbf{A}_{i j}=g_{f, i, j}$ as defined in Section IV-C. Note
that for all edges $(i, 0)$ the policy $\mathbf{B}_{i 0}=\langle i-1\rangle$ ensures that the latch of the next gadget cannot activate before the previous gadget has finished executing.

Figure 5 presents an execution trace of the 17 iterations required for the convergence of $Q_{9}$.

## VI. Tight upper bounds

In the previous section we explored an example of a strictly increasing path algebra which converged in $\Theta\left(n^{2}\right)$ iterations. Do there exist even more pathological algebras?

We will now answer in the negative by proving that every strictly increasing path algebra converges in at most $n^{2}$ iterations. The proof technique is as follows. Consider the nodes that have already converged. For any node with the best route into this set, we show that this node will itself converge after a further $O(n)$ iterations.

In most proofs of convergence for distributive algebras [20] this next node only takes a single additional iteration to converge. However in an increasing, non-distributive algebra this may take significantly longer due to junk routes generated by distributivity violations (as described in Figure 2). The key insight is that junk routes capable of interfering with the convergence of this next node can persist for at most $n$ iterations before necessarily containing a loop and hence being flushed.

## A. Some core definitions and lemmas

We now rigorously define some of the terms used informally in previous sections. Every claim in this section has been formalised in the theorem-proving language Agda [9], but we have not included some of the simpler proofs here due to space constraints. Interested readers may consult the Agda code [10].

Let $(S, \oplus, F, \overline{0}, \bar{\infty}$, path) be a strictly increasing path algebra and consider a network of $n$ nodes represented by an adjacency matrix $\mathbf{A}$. Let $\mathbf{X}$ be the initial state and without loss of generality let $j$ be the destination node.

Lemma 1. $\forall i: \sigma(\mathbf{X})_{i i}=\mathbf{I}_{i i}$. Omitted.
First we need a notion of an individual node converging. Our initial attempt is as follows:

Definition 3. A node $i$ is fixed at time $t$ iff

$$
\forall s: t \leq s \Rightarrow \sigma^{s}(\mathbf{X})_{i j}=\sigma^{t}(\mathbf{X})_{i j}
$$

with $\mathcal{F}_{t}$ being the set of fixed nodes at time $t$.
Lemma 2. $s \leq t \Rightarrow \mathcal{F}_{s} \subseteq \mathcal{F}_{t}$. Omitted.
Lemma 3. $j \in \mathcal{F}_{1}$. Omitted.
Lemma 4. If path $\left(\sigma^{t}(\mathbf{X})_{i j}\right)=(i, k):: p$ and $i \in \mathcal{F}_{t}$ then $\sigma^{t}(\mathbf{X})_{i j}=\mathbf{A}_{i k}\left(\left(\sigma^{t}(\mathbf{X})_{k j}\right)\right.$ and $p=\operatorname{path}\left(\sigma^{t}(\mathbf{X})_{k j}\right)$.
Proof. As $i \in \mathcal{F}_{t}$ we have that $\sigma^{t}(\mathbf{X})_{i j}=\sigma^{t+1}(\mathbf{X})_{i j}$ and so path $\left(\sigma^{t+1}(\mathbf{X})_{i j}\right)=(i, k):: p$. The path algebra assumption P3) therefore implies that $\sigma^{t+1}(\mathbf{X})_{i j}=\mathbf{A}_{i k}\left(\sigma^{t}(\mathbf{X})_{k j}\right)$ and that $p=\operatorname{path}\left(\sigma^{t}(\mathbf{X})_{k j}\right)$. Again as $i \in \mathcal{F}_{t}$ we have that $\sigma^{t}(\mathbf{X})_{i j}=\mathbf{A}_{i k}\left(\sigma^{t}(\mathbf{X})_{k j}\right)$.

| Time | Node | Level | Path | Event |
| :---: | :---: | :---: | :---: | :---: |
| 1 | 1 | 0 | 1, 0 | open latch 1 |
| 1 | 2 | 1 | 2, 0 | load latch 1 |
| 1 | 3 | 2 | 3, 0 |  |
| 1 | 4 | 3 | 4, 0 |  |
| 1 | 5 | 4 | 5, 0 |  |
| 1 | 6 | 5 | 6,0 |  |
| 1 | 7 | 6 | 7, 0 |  |
| 1 | 8 | 7 | 8, 0 |  |
| 2 | 2 | 0 | 2, 1, 0 | close latch 1 |
| 2 | 3 | 1 | 3, 2, 0 | violation 1 |
| 2 | 4 | 1 | 4, 2, 0 | violation 1 |
| 2 | 5 | 1 | 5, 2, 0 | violation 1 |
| 2 | 6 | 1 | 6, 2, 0 | violation 1 |
| 2 | 7 | 1 | 7,2,0 | violation 1 |
| 2 | 8 | 1 | 8, 2, 0 | violation 1 |
| 3 | 3 | 1 | $3,4,2,0$ |  |
| 3 | 4 | 1 | 4, 5, 2, 0 |  |
| 3 | 5 | 1 | 5, 6, 2, 0 | junk circulates |
| 3 | 6 | 1 | 6, 7, 2, 0 | in loop 1 |
| 3 | 7 | 1 | 7, $8,2,0$ |  |
| 3 | 8 | 1 | 8, $3,2,0$ |  |
| 4 | 3 | 1 | $3,4,5,2,0$ |  |
| 4 | 4 | 1 | 4, 5, 6, 2, 0 |  |
| 4 | 5 | 1 | 5, 6, 7, 2, 0 | junk circulates |
| 4 | 6 | 1 | 6, 7, 8, 2, 0 | in loop 1 |
| 4 | 7 | 1 | 7, $8,3,2,0$ |  |
| 4 | 8 | 1 | $8,3,4,2,0$ |  |
| 5 | 3 | 1 | $3,4,5,6,2,0$ |  |
| 5 | 4 | 1 | 4, 5, 6, 7, 2, 0 |  |
| 5 | 5 | 1 | 5, 6, 7, 8, 2, 0 | junk circulates |
| 5 | 6 | 1 | $6,7,8,3,2,0$ | in loop 1 |
| 5 | 7 | 1 | 7, $8,3,4,2,0$ |  |
| 5 | 8 | 1 | $8,3,4,5,2,0$ |  |
| 6 | 3 | 1 | $3,4,5,6,7,2,0$ |  |
| 6 | 4 | 1 | 4, 5, 6, 7, 8, 2, 0 |  |
| 6 | 5 | 1 | 5, 6, 7, 8, 3, 2, 0 | junk circulates |
| 6 | 6 | 1 | $6,7,8,3,4,2,0$ | in loop 1 |
| 6 | 7 | 1 | 7, 8, 3, 4, 5, 2, 0 |  |
| 6 | 8 | 1 | $8,3,4,5,6,2,0$ |  |


| Time | Node | Level | Path | Event |
| :---: | :---: | :---: | :---: | :---: |
| 7 | 3 | 1 | $3,4,5,6,7,8,2,0$ | junk circulates in loop 1 |
| 7 | 4 | 1 | 4, 5, 6, 7, 8, 3, 2, 0 |  |
| 7 | 5 | 1 | 5, 6, 7, 8, 3, 4, 2, 0 |  |
| 7 | 6 | 1 | $6,7,8,3,4,5,2,0$ |  |
| 7 | 7 | 1 | $7,8,3,4,5,6,2,0$ |  |
| 7 | 8 | 1 | 8, 3, 4, 5, 6, 7, 2, 0 |  |
| 8 | 3 | 2 | 3, 0 | open latch 2 load latch 2 |
| 8 | 4 | 3 | 4, 0 |  |
| 8 | 5 | 4 | 5,0 |  |
| 8 | 6 | 5 | 6, 0 |  |
| 8 | 7 | 6 | 7,0 |  |
| 8 | 8 | 7 | 8, 0 |  |
| 9 | 4 | 2 | 4, 3, 0 | close latch 2 |
| 9 | 5 | 3 | 5, 4, 0 | violation 2 |
| 9 | 6 | 3 | 6, 4, 0 | violation 2 |
| 9 | 7 | 3 | 7, 4, 0 | violation 2 |
| 9 | 8 | 3 | 8, 4, 0 | violation 2 |
| 10 | 5 | 3 | 5, 6, 4, 0 | junk circulates in loop 2 |
| 10 | 6 | 3 | 6, 7, 4, 0 |  |
| 10 | 7 | 3 | 7, 8, 4, 0 |  |
| 10 | 8 | 3 | 8, 5, 4, 0 |  |
| 11 | 5 | 3 | 5, 6, 7, 4, 0 | junk circulates in loop 2 |
| 11 | 6 | 3 | 6, 7, 8, 4, 0 |  |
| 11 | 7 | 3 | 7, 8, 5, 4, 0 |  |
| 11 | 8 | 3 | 8, 5, 6, 4, 0 |  |
| 12 | 5 | 3 | 5, 6, 7, 8, 4, 0 | junk circulates in loop 2 |
| 12 | 6 | 3 | 6, 7, 8, 5, 4, 0 |  |
| 12 | 7 | 3 | 7, $8,5,6,4,0$ |  |
| 12 | 8 | 3 | 8, 5, 6, 7, 4, 0 |  |
| 13 | 5 | 4 | 5,0 | open latch 3 load latch 3 |
| 13 | 6 | 5 | 6,0 |  |
| 13 | 7 | 6 | 7,0 |  |
| 13 | 8 | 7 | 8, 0 |  |
| 14 | 6 | 4 | 6,5,0 | close latch 3 |
| 14 | 7 | 5 | 7,6,0 | violation 3 |
| 14 | 8 | 5 | 8, 6,0 | violation 3 |
| 15 | 7 | 5 | 7, 8, 6, 0 | junk circulates |
| 15 | 8 | 5 | 8,7,6, 0 | in loop 3 |
| 16 | 7 | 6 | 7,0 | open latch 4 |
| 16 | 8 | 7 | 8, 0 | load latch 4 |
| 17 | 8 | 6 | 8,7,0 | close latch 4 |

Fig. 5. A trace for the iteration of $Q_{9}$. Green cells indicate that the route has improved. Red cells indicate the route has gotten worse. The step in which a node has converged is shown in bold, and after that the node's state is no longer listed. In the initial state at time 0 (not listed) only node 0 has converged and all other nodes have routes $\infty^{\prime}$. The notation "violation" stands for distributivity violation.

Perhaps counter-intuitively, if $i \in \mathcal{F}_{t}$ and $l \in \operatorname{path}\left(\sigma^{t}(\mathbf{X})_{i j}\right)$ then it is not guaranteed that $l \in \mathcal{F}_{t}$. This is a direct result of starting the algorithm from an arbitrary state $\mathbf{X}$ rather than from the initial state $\mathbf{I}$.


Fig. 6. Example of an unfixed node on the path of a fixed node for the shortest-widest-paths algebra. The weight of the $(1,0)$ arc has just changed from $f_{2,1}$ to $f_{1,1}$.

For example consider Fig. 6 where the bandwidth of arc $(1,0)$ has decreased from 2 to 1 . Node 1 initially contains an inconsistent route, but then adopts the new valid route at time 1 . Hence node 1 is not in $\mathcal{F}_{0}$. However the bandwidth of the arc $(2,1)$ acts as a bottleneck, and therefore node 2 never notices the change in the bandwidth of node 1's route. Hence node 2 is in $\mathcal{F}_{0}$ but node 1 is not.

Consequently the set $\mathcal{F}_{t}$ does not necessarily form a tree rooted at $j$ and therefore to make the proof to go through (in particular Lemma 10) we need a stronger notion of a node having converged.

Definition 4. A node $i$ has converged at time $t$ iff:

$$
\forall k \in \operatorname{path}\left(\sigma^{t}(\mathbf{X})_{i j}\right): k \in \mathcal{F}_{t}
$$

with $\mathcal{C}_{t}$ being the set of converged nodes at time $t$.
Lemma 5. $s \leq t \Rightarrow \mathcal{C}_{s} \subseteq \mathcal{C}_{t}$. Omitted.
Lemma 6. $j \in \mathcal{C}_{1}$. Omitted.
Lemma 7. If $i \in \mathcal{C}_{t}$ and $l \in \operatorname{path}\left(\sigma^{t} \mathbf{X}_{i j}\right)$ then $l \in \mathcal{C}_{t}$. That is, the set $\mathcal{C}_{t}$ forms a tree routed at $j$.

Proof. We prove this by induction over the path of $i$ 's route at time $t$.
Cases 1 \& 2: $\operatorname{path}\left(\sigma^{t}(\mathbf{X})_{i j}\right)=\perp$ or $\operatorname{path}\left(\sigma^{t}(\mathbf{X})_{i j}\right)=[]$ Then there cannot exist a node $l \in \operatorname{path}\left(\sigma^{t} \mathbf{X}_{i j}\right)$ and so the statement is vacuously true.
Case 3: $\operatorname{path}\left(\sigma^{t}(\mathbf{X})_{i j}\right)=(i, k):: p$
If $l \in \operatorname{path}\left(\sigma^{t}(\mathbf{X})_{i j}\right)$ then either $l=i$ or $l \in p$. In the former case we know $i \in \mathcal{C}_{t}$ and so $l \in \mathcal{C}_{t}$. In the latter case we use Lemma 4 to get that $\operatorname{path}\left(\sigma^{t}(\mathbf{X})_{k j}\right)=p$. Therefore as $i \in \mathcal{C}_{t}$ we have that all nodes in $p$ are in $\mathcal{F}_{t}$ and therefore we have $k \in \mathcal{C}_{t}$. Hence, as $l \in \operatorname{path}\left(\sigma^{t}(\mathbf{X})_{k j}\right)$, we can apply the induction hypothesis to prove $l \in \mathcal{C}_{t}$.

In Section V we talked informally about junk routes. These are routes that exist in the graph but conflict with the current routing choices made by the nodes along the route's path. Consider latch 1 in the trace of $Q_{9}$ in Figure 5. At time 2,
node 3 's route is junk because node 3 thinks that its message will travel along the path $(3,2,0)$. However this conflicts with the routing choice of node 2 and in reality next-hop forwarding will ensure that the message travels along the path $(3,2,1,0)$.

In practice it's easier to define when a route is not junk. Intuitively a route is real at time $t$ if all the routing decisions of nodes along its path are consistent with one another.
Definition 5. A node $i$ is real at time $t$ if

$$
\forall(k, l) \in \operatorname{path}\left(\sigma^{t}(\mathbf{X})_{i j}\right): \sigma^{t}(\mathbf{X})_{k j}=\mathbf{A}_{k l}\left(\sigma^{t}(\mathbf{X})_{l j}\right)
$$

with $\mathcal{R}_{t}$ being the set of real nodes at time $t$.
Lemma 8. $j \in \mathcal{R}_{1}$. Omitted.
Lemma 9. $i \in \mathcal{R}_{t}$ and $l \in \operatorname{path}\left(\sigma^{t}(\mathbf{X})_{i j}\right)$ implies $l \in \mathcal{R}_{t}$. Omitted.

Lemma 10. $\mathcal{C}_{t} \subseteq \mathcal{R}_{t}$.
Proof. Assume $i \in \mathcal{C}_{t}$, then prove $i \in \mathcal{R}_{t}$ by induction over the path of $\sigma^{t}(\mathbf{X})_{i j}$ (using Lemma 4).

If we started in the initial state $\mathbf{I}$ then we could prove a stronger version of this lemma: $\mathcal{F}_{t} \subseteq \mathcal{R}_{t}$. However this is not true when starting in arbitrary states. Figure 6 provides a counterexample as at time 2 , node 2 is fixed but it is not real.

Unlike $\mathcal{F}$ and $\mathcal{C}$, the family of sets $\mathcal{R}$ does not necessarily grow monotonically over time as distributivity violations can repeatedly generate fresh junk routes.

To assist with notation we define a time-indexed family of orderings over arcs as follows:

$$
(i, k) \preceq^{t}(l, m) \triangleq \mathbf{A}_{i k}\left(\sigma^{t}(\mathbf{X})_{k j}\right) \leq \mathbf{A}_{l m}\left(\sigma^{t}(\mathbf{X})_{m j}\right)
$$

Later on in Lemma 15 we will need to prove that some node $l$ permanently adopts the route through one of its neighbour $m$. Intuitively if $(i, k) \preceq^{t}(l, m)$ then the route $\mathbf{A}_{i k}\left(\sigma^{t} \mathbf{X}_{k j}\right)$ and some of its extensions may be capable of threatening $l$ 's adoption of the route through $m$ at some point in the future.

## B. The inductive step of the proof

The thrust of the proof is to keep track of $P(t)$, the set of nodes that we can prove have converged at time $t$. This is a (possibly strict) subset of $\mathcal{C}_{t}$ the nodes that have actually converged at time $t$. We will then show that while there exist nodes not in $P(t)$, then we can prove that at least one of those nodes will have converged by time $t+n$. This node can then be added to $P(t+n)$, the set of nodes proven to have converged at time $t+n$. The whole process can then be repeated and this forms the crux of the inductive argument for convergence.

Consider a time $\tau \geq 1$ and assume $j \in P(\tau)$. Denote $P(\tau)$ 's complement as $\overline{P(\tau)}$. Let $E(\tau)$ be the set of all arcs going from $\overline{P(\tau)}$ to $P(\tau)$ (i.e. the in-cutset of $P(\tau))$ and $e_{\min }(\tau)=$ $\left(i_{\min }(\tau), k_{\min }(\tau)\right)$ be a minimal arc in $E(\tau)$ with respect to $\preceq^{\tau}$ (see Figure 7). We will now prove that $i_{\min }(\tau) \in \mathcal{C}_{\tau+n}$ and therefore $i_{\min }(\tau)$ can added as a member of $P(\tau+n)$.


Fig. 7. Construction of the $\operatorname{arc} e_{\min }(\tau)=\left(i_{\min }, k_{\min }\right) . P(\tau)$ is the set of nodes we can prove have converged at time $\tau$. Dashed arcs are members of $E(\tau)$, the cutset of $P(\tau)$. The arc $e_{\min }$ is a minimal member of $E(\tau)$ with respect to $\preceq^{\tau}$.

In order to improve readability, we will now drop $e_{\min }(\tau)$, $i_{\min }(\tau)$ and $k_{\min }(\tau)$ 's dependencies on $\tau$ and just write $e_{\min }$, $i_{\text {min }}$ and $k_{\text {min }}$.

What can stop node $i_{\text {min }}$ from routing through $k_{\text {min }}$ ? In the distributive world nothing. The node $i_{\min }$ uses a route from either $P(\tau)$ or $\overline{P(\tau)}$. The route through $k_{\min }$ is by definition the best route from $P(\tau)$, and any route from $\overline{P(\tau)}$ must go through $P(\tau)$. Hence distributivity ensures that the route through $\overline{P(\tau)}$ cannot be better than the route through $k_{\text {min }}$.

However non-distributive algebras can generate junk routes before time $\tau$, that can continue to persist in $\overline{P(\tau)}$ after time $\tau$. These junk routes can prevent the adoption of $e_{\text {min }}$. We now prove that a real node in $\overline{P(\tau)}$ can never threaten $e_{\text {min }}$.
Lemma 11. For all $t \geq \tau$ if $k \in \mathcal{R}_{t}$ and $k \in \overline{P(\tau)}$ then for all nodes $i$ we have $e_{\min } \preceq^{t}(i, k)$.
Proof. We proceed by induction over the path of $\sigma^{t}(\mathbf{X})_{k j}$.
Case 1: $\operatorname{path}\left(\sigma^{t}(\mathbf{X})_{k j}\right)=[]$
If the path is empty then we must have that $k=j$, and so we have $j \notin P(\tau)$. This contradicts the assumption at the start of Section VI-B that $j \in P(\tau)$.

Case 2: $\operatorname{path}\left(\sigma^{t}(\mathbf{X})_{k j}\right)=\perp$
Then we have the required inequality as follows:

$$
\begin{array}{ll}
\mathbf{A}_{i_{\min } k_{\min }}\left(\sigma^{t}(\mathbf{X})_{k_{\min } j}\right) & \leq(\text { by R5 })) \\
\infty & =(\text { by R6 }) \\
\mathbf{A}_{i k}(\infty) & =\text { (by P1) and case assumption) } \\
\mathbf{A}_{i k}\left(\sigma^{t}(\mathbf{X})_{k j}\right) &
\end{array}
$$

Case 3: $\operatorname{path}\left(\sigma^{t}(\mathbf{X})_{k j}\right)=(k, l):: p$
If $l \in P(\tau)$ then $(k, l)$ is an element of the cutset $E$ and so we have that $e_{\min } \preceq^{t}(k, l)$ as $e_{\min }$ is a minimal member of $E$. If $l \notin P(\tau)$ then we have that $e_{\min } \preceq^{t}(k, l)$ by applying the inductive hypothesis as $l \in \mathcal{R}_{t}$ (from Lemma 9). Therefore either way $e_{\min } \preceq^{t}(k, l)$.

Using this we can then show that the required inequality holds as follows:

$$
\begin{array}{ll}
\mathbf{A}_{i_{\min } k_{\min }}\left(\sigma^{t}(\mathbf{X})_{k_{\min } j}\right) & \leq\left(\text { by } e_{\min } \preceq^{t}(k, l)\right) \\
\mathbf{A}_{k l}\left(\sigma^{t}(\mathbf{X})_{l j}\right) & \left.=\text { (by } k \in \mathcal{R}_{t}\right) \\
\sigma^{t}(\mathbf{X})_{k j} & \leq \text { (by } F \text { increasing) } \\
\mathbf{A}_{i k}\left(\sigma^{t}(\mathbf{X})_{k j}\right) &
\end{array}
$$

Therefore the nodes that are capable of blocking $i_{\text {min }}$ from routing through $e_{\min }$, are those that are not real and that have an extension that threatens $e_{\text {min }}$.

Definition 6. A node $k$ 's route is dangerous at time $t \geq \tau$ if:

$$
k \notin \mathcal{R}_{t} \wedge \exists i:(i, k) \prec^{t} e_{\min }
$$

where $\mathcal{D}_{t}^{\tau}$ is the set of dangerous nodes at time $t \geq \tau$.
It is important to note that routes are labelled as dangerous relative to the current minimal arc. We will now prove that after time $\tau$ all dangerous routes must be an extension of some other dangerous route. Hence new dangerous routes cannot be generated after time $\tau$.

Lemma 12. For all $t \geq \tau$ and $k \in \mathcal{D}_{t+1}^{\tau}$ then there exists $l$ such that $\sigma^{t+1}(\mathbf{X})_{k j}=\mathbf{A}_{k l}\left(\sigma^{t}(\mathbf{X})_{l j}\right)$ and $l \in \mathcal{D}_{t}^{\tau}$.

Proof. From the definition of $\mathcal{D}_{t+1}^{\tau}$ we know that $k \notin \mathcal{R}_{t+1}$. As the routes $\bar{\infty}$ and $\overline{0}$ are trivially real, $k$ 's route cannot be either $\bar{\infty}$ or $\overline{0}$. Therefore $\sigma^{t+1}(\mathbf{X})_{k j}=\mathbf{A}_{k l}\left(\sigma^{t}(\mathbf{X})_{l j}\right)$ for some $l$. We now need to show that $l \in \mathcal{D}_{t}^{\tau}$ by proving $(k, l) \prec^{t} e_{\text {min }}$ and $l \notin \mathcal{R}_{t}$.

As $k \in \mathcal{D}_{t+1}^{\tau}$ we know there exists a node $i$ such that $(i, k) \prec^{t+1} e_{\min }$. We therefore have the following chain of reasoning to prove that $(k, l) \prec^{t} e_{\min }$ :

$$
\begin{array}{ll}
\mathbf{A}_{k l}\left(\sigma^{t}(\mathbf{X})_{l j}\right) & =\text { (by prev. reasoning) } \\
\sigma^{t+1}(\mathbf{X})_{k j} & \leq \text { (by } F \text { increasing) } \\
\mathbf{A}_{i k}\left(\sigma^{t+1}(\mathbf{X})_{k j}\right) & \left.<\text { (by }(i, k) \prec^{t+1} e_{\min }\right) \\
\mathbf{A}_{i_{\min } k_{\min }}\left(\sigma^{t+1}(\mathbf{X})_{k_{\min } j}\right) & \left.=\text { (by } k_{\min } \in \mathcal{C}_{t}\right) \\
\mathbf{A}_{i_{\min } k_{\min }}\left(\sigma^{t}(\mathbf{X})_{k_{\min } j}\right) &
\end{array}
$$

It remains to show that $l \notin \mathcal{R}_{t}$. Clearly $k \notin P(\tau)$ as that would contradict $k \notin \mathcal{R}_{t+1}$ by Lemma 10 . If $l \in P(\tau)$ then $(k, l)$ is an element of the cutset $E$ and so we have that $e_{\min } \preceq^{t+1}$ $(k, l)$ as $e_{\min }$ is a minimal member of $E$. If $l \notin P(\tau)$ then we have that $e_{\min } \preceq^{t+1}(k, l)$ by applying Lemma 11. Therefore either way $e_{\min } \preceq^{t+1}(k, l)$.

Assume $l \in \mathcal{R}_{t}$. Then we have a contradiction as we have assumed that $(i, k) \prec^{t+1} e_{\text {min }}$ but we can show $e_{\text {min }} \preceq^{t+1}$ $(i, k)$ as follows:

$$
\begin{array}{ll}
\mathbf{A}_{i_{\min } k_{\min }}\left(\sigma^{t+1}(\mathbf{X})_{k_{\min } j}\right) & =\left(\text { by } k_{\min } \in \mathcal{C}_{t}\right) \\
\mathbf{A}_{i_{\min } k_{\min }}\left(\sigma^{t}(\mathbf{X})_{k_{\min } j}\right) & \leq \text { (by } e_{\min } \preceq^{t+1}(k, l) \text { ) } \\
\mathbf{A}_{k l}\left(\sigma^{t}(\mathbf{X})_{l j}\right) & =\text { (by prev. reasoning) } \\
\sigma^{t+1}(\mathbf{X})_{k j} & \leq \text { (by } F \text { increasing) } \\
\mathbf{A}_{i k}\left(\sigma^{t+1}(\mathbf{X})_{k j}\right) &
\end{array}
$$

Hence $l \notin \mathcal{R}_{t}$ and so $l \in \mathcal{D}_{t}^{\tau}$.
We now prove that all dangerous routes must have been flushed by time $t+n$.

Lemma 13. $\mathcal{D}_{\tau+n}^{\tau}=\{ \}$
Proof. By Lemma 12 we have that the length of the paths of the dangerous routes must increase by 1 each iteration. As routes formed along looping paths are removed, then after $n$ iterations all such dangerous routes must have been eliminated from the routing state. Hence there can no longer be any dangerous junk routes at time $\tau+n$.

We now prove that at any time after $\tau+n$ the route through $k_{\text {min }}$ is guaranteed to be the best route available to $i_{\text {min }}$.
Lemma 14. For every node $k$ and time $s \geq \tau+n$ then:

$$
\left(i_{\min }, k_{\min }\right) \preceq^{s}\left(i_{\min }, k\right)
$$

Proof. Consider an arbitrary $k$. If $k \in P(\tau)$ then we have the required inequality by the fact that $e_{\min }$ is a minimal arc into $P(\tau)$. If $k \notin \mathcal{R}_{s}$ then we must have the required inequality otherwise there would exist a dangerous route which would contradict Lemma 13.

Finally if $k \notin P(\tau)$ and $k \in \mathcal{R}_{s}$ then we have the required inequality by Lemma 11.

Hence we can now show that at any time $s \geq \tau+n$ then $i_{\text {min }}$ routes through $k_{\text {min }}$.
Lemma 15. For every time $s \geq \tau+n$ then :

$$
\sigma^{s}(\mathbf{X})_{i_{\min } j}=\mathbf{A}_{i_{\min } k_{\min }}\left(\sigma^{s-1}(\mathbf{X})_{k_{\min } j}\right)
$$

Proof. As $i_{\min } \notin P(\tau)$ and $j \in P(\tau)$ then clearly $i_{\min } \neq j$. Hence we have as follows:

$$
\begin{array}{lll}
\sigma^{s}(\mathbf{X})_{i_{\min } j} & = & \text { (by definition of } \sigma \text { ) } \\
\bigoplus_{k} \mathbf{A}_{i_{\min } k}\left(\sigma^{s-1}(\mathbf{X})_{k j}\right) \oplus \mathbf{I}_{i_{\min } j} & =\text { (by definition of } \mathbf{I}) \\
\bigoplus_{k} \mathbf{A}_{i_{\min } k}\left(\sigma^{s-1}(\mathbf{X})_{k j}\right) \oplus \bar{\infty} & =\text { (by R5)) } \\
\bigoplus_{k} \mathbf{A}_{i_{\min } k}\left(\sigma^{s-1}(\mathbf{X})_{k j}\right) & =\text { (by Lemma 14) } \\
\mathbf{A}_{i_{\min } k_{\min }}\left(\sigma^{s-1}(\mathbf{X})_{k_{\min } j}\right) & &
\end{array}
$$

Lemma 16. $i_{\text {min }} \in \mathcal{C}_{\tau+n}$
Proof. We first prove that $i_{\min } \in \mathcal{F}_{\tau+n}$. Consider an arbitrary $s \geq \tau+n$. then:

$$
\begin{array}{ll}
\sigma^{\tau+n}(\mathbf{X})_{i_{\min } j} & =\text { (by Lemma 15) } \\
\mathbf{A}_{i_{\min } k_{\min }}\left(\sigma^{\tau+n-1}(\mathbf{X})_{k_{\min } j}\right) & \left.=\text { (by } k_{\min } \in \mathcal{C}_{\tau}\right) \\
\mathbf{A}_{i_{\min } k_{\min }}\left(\sigma^{s-1}(\mathbf{X})_{k_{\min } j} j\right) & =\text { (by Lemma 15) } \\
\sigma^{s}(\mathbf{X})_{i_{\min } j} &
\end{array}
$$

For any $s \geq \tau+n$ we know that $i_{\text {min }}$ routes through $k_{\text {min }}$ which is in $C$. As $C \subseteq \mathcal{C}_{\tau} \subseteq \mathcal{C}_{\tau+n} \subseteq \mathcal{F}_{\tau+n}$, we therefore know that all nodes in the path of $k$ are in $\mathcal{F}_{\tau+n}$. Hence all nodes in the path of $i$ are in $\mathcal{F}_{\tau+n}$, and so $i \in \mathcal{C}_{\tau+n}$.

Theorem 1. For any strictly increasing path algebra $(S, \oplus, F, \overline{0}, \bar{\infty})$ and network of $n$ nodes then $\sigma$ converges in at most $n^{2}$ iterations from every starting state $\mathbf{X}$.
Proof. Let $P(t)$ be the set of nodes we can prove have converged at time $t$. Clearly $P(0)=\emptyset$. At time 1 we know by Lemma 6 that $j$ has converged and so let $P(1)=\{j\}$.

By fixing the current time as $\tau$, we can repeatedly generate a new node $i_{\min }(\tau)$ not yet in $P(\tau)$ via the process described in Section VI-B. Lemma 16 proves that $i_{\min }(\tau)$ will have converged at time $\tau+n$, thus $P(\tau+n)=P(\tau) \cup\left\{i_{\min }(\tau)\right\}$.

By repeating the process above $n-1$ times we get that after time then $P(n(n-1)+1)$ will contain every node and so the network is guaranteed to have converged.

## VII. Open QUestions

Not all strictly increasing path algebras converge in $\Theta\left(n^{2}\right)$. For example the shortest-widest paths algebra is not distributive, yet it appears to always to converge in $2 n-2$ iterations. It seems likely there are sub-families of strictly increasing path algebras that have distinct worst-case convergence bounds. In some cases it might be feasible to further constrain the policies in order to ensure faster convergence.

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[^0]:    ${ }^{1}$ Sobrinho refers to this property as "monotonicity".

