Author(S):	M. Panagopoulou <sup>a</sup> , C. Psychalinos <sup>a</sup> , F.A. Khanday <sup>b</sup> , N.A. Shah <sup>b</sup>
Title:	Sinh-Domain multiphase sinusoidal oscillator
Keywords:	Analog integrated circuits, Companding circuits, Sinh-Domain circuits, Sinusoidal oscillators, Multiphase oscillators
Year:	2013
Name of journal:	Microelectronics Journal
Volume & Issue	44(9)
Page No:	834-839
Institute:	<ul> <li><sup>a</sup> Department of Physics, Electronics Laboratory, University of Patras, Rice Patras, Greece.</li> <li><sup>b</sup> Department of Electronics and Instrumentation Technology, University of Kashmir, Srinagar, India.</li> </ul>

## Abstract

A Multiphase Sinusoidal Oscillator (MSO) configuration derived by employing appropriate nonlinear transconductors which implement lossy integrators in the Sinh-Domain is introduced in this paper. Owing to its companding nature, the oscillator offers the benefits of electronic tuning of the oscillation frequency and the capability for operating in a low-voltage environment. In addition, the condition of oscillation could be electronically adjusted without disturbing the oscillation frequency. This has been achieved by introducing a novel Sinh-Domain lossy integrator topology. The performance of the proposed oscillator has been evaluated through a design example, where a six-phase topology has been simulated by utilizing the Analog Design Environment of the Cadence software.

DOI: 10.1016/j.mejo.2013.06.017