

Eur. Phys. J. Special Topics **222**, 1217–1221 (2013)
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DOI: [10.1140/epjst/e2013-01916-x](https://doi.org/10.1140/epjst/e2013-01916-x)

THE EUROPEAN
PHYSICAL JOURNAL
SPECIAL TOPICS

Review

On field effect studies and superconductor-insulator transition in high- T_c cuprates

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Received 18 March 2013 / Received in final form 12 May 2013

Published online 15 July 2013

Abstract. We summarize previous field effect studies in high- T_c cuprates and then discuss our method to smoothly tune the carrier concentration of a cuprate film over a wide range using an applied electric field. We synthesized epitaxial one-unit-cell thick films of $\text{La}_{2-x}\text{Sr}_x\text{CuO}_4$ and from them fabricated electric double layer transistor devices utilizing various gate electrolytes. We were able to vary the carrier density by about 0.08 carriers per Cu atom, with the resulting change in T_c of 30 K. The superconductor-insulator transition occurred at the critical resistance very close to the quantum resistance for pairs, $R_Q = h/(2e)^2 = 6.5 \text{ k}\Omega$. This is suggestive of a quantum phase transition, possibly driven by quantum phase fluctuations, between a “Bose insulator” and a high- T_c superconductor state.

1 Introduction

The carrier density plays a large role in determining the physical properties of correlated electron materials. For example, cuprates can be antiferromagnetic insulators, high critical temperature (T_c) superconductors, or ordinary metals, all within a small window of carrier concentrations. The electric field effect is thus a potentially valuable tool for the study of such materials [1]. Indeed, already back in 1988 Brazovskii and Yakovenko suggested the possibility to induce high- T_c superconductivity in La_2CuO_4 by the field effect [2]. The technical difficulty in implementing this idea was the relatively large carrier density required, of the order of 10^{21} cm^{-3} , which corresponds to an electronic screening length of $\leq 1 \text{ nm}$. The latter in turn implies that one must secure an atomically smooth surface, which in the case of cuprates has been far from trivial. Nevertheless, the field effect has been demonstrated successfully in high- T_c cuprates, following various approaches.

The familiar field effect transistor geometry uses a metallic top gate separated from the active film (“channel”) by a gate insulator. The latter must withstand the maximal electric field to be applied, and the leakage currents must be small. The lack of an adequate gate insulator turned out to be the key limiting factor in early studies of the electric field effect in cuprates that employed such FET geometry [3]. Namely,

to be able to induce the large desired changes in the carrier density, of the order of 10^{21} cm^{-3} , the breakdown field of the insulator must be larger than $\sim 10^8 \text{ V/cm}$ and such an insulator is not available.

An approach that proved more successful utilized an inverted metal-insulator-superconductor geometry, whereby the gate electrode was placed at the bottom of a SrTiO_3 substrate, on top of which a high- T_c $\text{YBa}_2\text{Cu}_3\text{O}_{7-\delta}$ film was grown [4]. By applying a voltage to the back gate, the carrier density in the film was varied, producing T_c shifts of 10 K in $\text{Y}_{1-y}\text{Ca}_y\text{Ba}_2\text{Cu}_3\text{O}_{7-\delta}$ [5,6] and $\text{NdBa}_2\text{Cu}_3\text{O}_{7-\delta}$ [7] films. The same technique of back gating SrTiO_3 has also been used more recently to induce interface superconductivity, with $T_c = 310 \text{ mK}$, in the 2D electron gas at the $\text{LaAlO}_3\text{-SrTiO}_3$ interface [8].

The third approach is to use ferroelectric gating. In this arrangement, a layer of a ferroelectric material such as $\text{PbZr}_x\text{Ti}_{1-x}\text{O}_3$ is placed in between the superconducting film and the gate electrode, and an applied gate voltage is used to change the polarization of the ferroelectric. In turn, this polarization field modulates the carrier density, and thus the electronic properties, of the superconducting film. Shifts in T_c by several degrees Kelvin were induced in this way in $\text{GdBa}_2\text{Cu}_3\text{O}_{7-\delta}$ [9] and $\text{NdBa}_2\text{Cu}_3\text{O}_{7-\delta}$ [10].

Yet another design, the one that we adopted in our studies of the field-effect in cuprates, is the electric double layer transistor (EDLT). Here, voltage is applied between a gate electrode and the channel material through a solid (polymer) or liquid electrolyte. Ions of one polarity are attracted to the gate electrode whereas ions of the opposite polarity are drawn to the channel, where an electronic double layer is formed at the electrolyte-channel interface. Within the double layer, electric field is very high it can exceed 10^8 V/cm . Using polymer electrolytes (salts dissolved in a polymer such as polyethylene glycol), two-dimensional carrier density as high as 10^{14} cm^{-2} has been achieved. Even higher densities of $\sim 8 \times 10^{14} \text{ cm}^{-2}$ have been attained using ionic liquids (molten organic salts) as the electrolyte. This is comparable to the carrier density of high- T_c cuprates, and hence EDLT technique offers new opportunities for studying the field effect in these materials. Indeed, already from early nineties, McDevitt and collaborators applied EDLT technique to $\text{YBa}_2\text{Cu}_3\text{O}_{7-\delta}$, $\text{Tl}_2\text{Ba}_2\text{CaCu}_2\text{O}_8$ and $\text{Tl}_2\text{Ba}_2\text{Ca}_2\text{Cu}_3\text{O}_{10}$ and demonstrated spectacularly large shifts of T_c [11–15]. Using the same technique in recent years, Iwasa, Kawasaki, and collaborators were able to induce superconductivity by the field effect in an insulator (SrTiO_3) [16] as well as a semiconductor (ZrNCl) [17]. They have also demonstrated, for the first time, superconductivity in KTaO_3 [18]. While admittedly the T_c was quite low ($\sim 0.05 \text{ K}$), nevertheless this was a seminal result, since this was the first new superconductor discovered by the electric field effect techniques.

2 Experimental results

In the last several years, we have extensively used the EDLT technique to study the basic physics of high- T_c superconductivity (HTS) in cuprates. In particular, we were interested in the superconductor-insulator transition (SIT) that occurs in $\text{La}_{2-x}\text{Sr}_x\text{CuO}_4$ (LSCO) as the doping is tuned around the critical value of $x_c \approx 0.06$. Indeed, we were driven by the prospects to reversibly and at will display the wealth of cuprate physics in a single sample by applying an electric field. The traditional approach to studying a particular segment of the cuprate phase diagram is to synthesize many individual crystals or thin films with different levels of doping. This is generally not only very time and labor intensive, but also subject to large uncertainty in the actual stoichiometry and doping levels. Moreover, chemical doping also induces disorder due to random distribution of dopant atoms. In contrast, the principal effect of

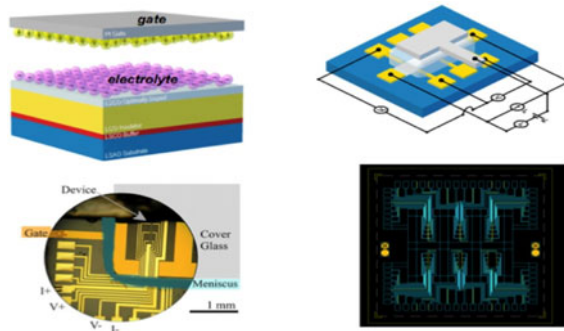


Fig. 1. Illustration of our electrolyte double-layer transistor and the Hall-Effect bar device, which is immersed in electrolyte. The gate voltage is applied by a Pt counter-electrode, or a coplanar Au electrode through the electrolyte [22].

the electrostatic technique described here is just to modify the carrier density in the films. Consequently, the EDLT technique not only allows one to study the intrinsic properties of cuprates, but also to determine the relative contributions of electronic doping and disorder to phenomena such as quantum phase transitions.

For EDLT studies, we synthesized ultrathin one- unit-cell (1 UC) or two-unit-cell (2 UC) thick LSCO films using atomic-layer-by-layer molecular beam epitaxy [19]. This technique allows reproducible fabrication of atomically smooth films of cuprates with control over the stoichiometry at the 1% level. We have demonstrated the capability to synthesize films in which HTS superfluid is confined to a single CuO_2 plane, and yet the critical temperature is comparable to that in optimally doped bulk LSCO single crystals [20–22]. The films thickness is controlled digitally, in real time, using reflection high-energy electron diffraction which shows pronounced oscillation with the period corresponding to assembly and completion of exactly one molecular layer on the surface.

Subsequently, we pattern the films using photolithography to form Hall-effect devices (see Fig. 1). We study them by sequences of automated resistance measurements while thermally cycling the samples from room temperature to 4 K. In one thermal cycle, at a chosen temperature we apply the desired gate voltage to our devices, charge them for a selected time, and subsequently measure the resistance as a function of temperature (at a constant cooling rate).

Initially, we have chosen to use the ionic liquid DEME-TFSI as a gate electrolyte, as it had been successfully used by other researchers in this field [11–18]. Other electrolytes we tested included the solution of NaF in polyethylene glycol, as well as the ionic liquid $\text{C}_6\text{MIm-BF}_4$. For each of these electrolytes, we studied in detail the charging characteristics of our devices at different temperatures.

Using this approach, we were able to apply very large electric fields on 1 UC or 2 UC thick LSCO films. We induced large changes in the surface carrier density, up to about 0.08 carriers per Cu atom, that caused T_c to shift by up to 30 K. Hundreds of resistance vs. temperature and carrier density curves were recorded by virtue of the automation described above. This allowed us to study the physics of the superconductor–insulator transition (SIT) in monolayer LSCO films (see Fig. 2) in great detail. We were able to determine the critical sheet resistance in several devices on different films, and it turned to be quite close to the quantum resistance for pairs, $R_Q = 6.5 \text{ k}\Omega$. The scaling analysis allowed us to determine the product of the critical exponents. The results were consistent with a 2D quantum phase transition driven by quantum phase fluctuations, between HTS state and a “Bose insulator” state with localized pairs [23–25]. Indeed, in a recent review [26], we have shown that in several

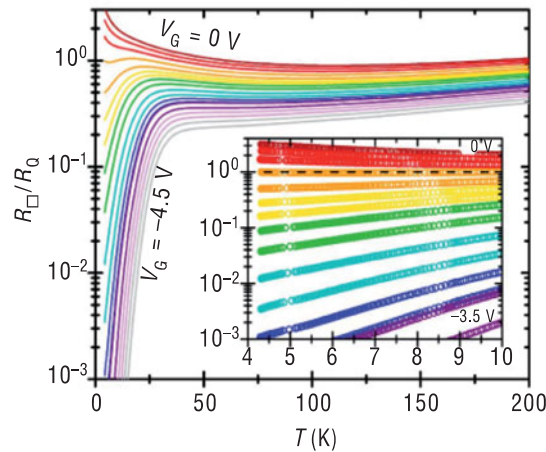


Fig. 2. Superconductor–insulator transition driven by electric field. Temperature dependence of normalized resistance of an initially heavily underdoped and insulating film. The device (see Fig. 1) employs a coplanar Au gate and DEME-TFSI ionic liquid. The carrier density, fixed for each curve, is tuned by varying the gate voltage from 0 V to -4.5 V in 0.25 V steps; an insulating film becomes superconducting via a quantum phase transition. The inset highlights the separatrix independent of temperature in the range from 4 K to 10 K. The open circles are the actual raw data points; the black dashed line is quantum resistance for pairs; the critical sheet resistance is equal to the quantum resistance of pairs, $R_Q = h/4e^2 = 6.5$ k Ω [15].

other p -type cuprates, SIT also occurs at the critical sheet resistance approximately equal to the pair quantum resistance.

More recently, the groups of Allen Goldman at University of Minnesota as well as the IWASA group reported several further studies of cuprates, both $\text{YBa}_2\text{Cu}_3\text{O}_{7-\delta}$ and La_2CuO_4 , by EDLT technique [27–30]. While most findings are broadly consistent with ours, clearly much more work needs to be done, ideally extended to additional quasi-2D materials and to much lower temperatures.

I.B. and A.T.B. were supported by the US Department of Energy, Basic Energy Sciences, Materials Sciences and Engineering Division. G.D. and D.P. were supported by the Laboratory for Physics of Complex Matter-EPFL, and the Swiss National Science Foundation.

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