

$\tilde{O}(n^{1/3})$ -Space Algorithm for the Grid Graph Reachability Problem

Ryo Ashida

Department of Mathematical and Computing Science, Tokyo Institute of Technology
(c/o Professor Osamu Watanabe)
Tokyo, Japan
ashida1@is.titech.ac.jp

Kotaro Nakagawa

JMA SYSTEMS Corporation
Tokyo, Japan
kootaroonakagawa@gmail.com

Abstract

The directed graph reachability problem takes as input an n -vertex directed graph $G = (V, E)$, and two distinguished vertices s and t . The problem is to determine whether there exists a path from s to t in G . This is a canonical complete problem for class NL. Asano et al. proposed an $\tilde{O}(\sqrt{n})$ space¹ and polynomial time algorithm for the directed grid and planar graph reachability problem. The main result of this paper is to show that the directed graph reachability problem restricted to grid graphs can be solved in polynomial time using only $\tilde{O}(n^{1/3})$ space.

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1 Introduction

The graph reachability problem, for a graph $G = (V, E)$ and two distinct vertices $s, t \in V$, is to determine whether there exists a path from s to t . This problem characterizes many important complexity classes. The *directed graph reachability problem* is a canonical complete problem for the nondeterministic log-space class, NL. Reingold showed that the *undirected graph reachability problem* characterizes the deterministic log-space class, L[8]. As with P vs. NP problem, whether L=NL or not is a major open problem. This problem is equivalent to whether the directed graph reachability problem is solvable in deterministic log-space. There exist two fundamental solutions for the directed graph reachability problem, breadth first search, denoted as BFS, and Savitch's algorithm. BFS runs in $O(n)$ space and $O(m)$ time, where n and m are the number of vertices and edges, respectively. For Savitch's algorithm, we use only $O(\log^2 n)$ space but require $\Theta(n^{\log n})$ time. BFS needs short time but large space. Savitch's algorithm uses small space but super polynomial time. A natural question is whether we can make an efficient deterministic algorithm in both space and time for the directed graph reachability problem. In particular, Wigderson proposed a problem that does

¹ In this paper " $\tilde{O}(s(n))$ space" means $O(s(n))$ words intuitively and precisely $O(s(n) \log n)$ space.



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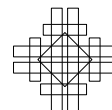
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there exist an algorithm for the directed graph reachability problem that uses polynomial time and $O(n^\varepsilon)$ space, for some $\varepsilon < 1$? [11], and this question is still open. The best known polynomial time algorithm, shown by Barns, Buss, Ruzzo and Schieber, uses $O(n/2\sqrt{\log n})$ space [4].

For some restricted graph classes, better results are known. Stolee and Vinodchandran showed that for any $0 < \varepsilon < 1$, the reachability problem for directed acyclic graph with $O(n^\varepsilon)$ sources and embedded on a surface with $O(n^\varepsilon)$ genus can be solved in polynomial time and $O(n^\varepsilon)$ space [9]. A natural and important restricted graph class is the class of planar graphs. The *planar graph reachability problem* is hard for L, and in the unambiguous log-space class, UL [5], which is a subclass of NL. Imai et al. gave an algorithm using $O(n^{1/2+\varepsilon})$ space and polynomial time for the planar graph reachability problem [2, 7]. Moreover Asano et al. devised a efficient way to control the recursion, and proposed a polynomial time and $\tilde{O}(\sqrt{n})$ space algorithm for the planar graph reachability problem [3]. In this paper, we focus on the *grid graph reachability problem*, where grid graphs are special cases of planar graphs. Allender et al. showed the planar graph reachability problem is log-space reducible to the grid graph reachability problem [1]. By using the algorithm of Asano et al., we can solve the grid graph reachability problem in $\tilde{O}(\sqrt{n})$ space and polynomial time. The main result of this paper is to show an $\tilde{O}(n^{1/3})$ space and polynomial time algorithm for the directed grid graph reachability problem.

► **Theorem 1** ([3]). *There exists an algorithm that decides directed planar graph reachability in polynomial time and $\tilde{O}(\sqrt{n})$ space. (We refer to this algorithm by PlanarReach in this paper.)*

2 Preliminaries and an outline of the algorithm

We will use the standard notions and notations for algorithms, complexity measures, and graphs without defining them. We consider mainly directed graphs, and a graph is assumed to be a directed graph unless it is specified as a undirected graph. Throughout this paper, for any set X , $|X|$ denotes the number of elements in X . We refer to the maximum and minimum elements of X as $\max X$ and $\min X$, respectively. Consider any directed graph $G = (V, E)$. For any $u, v \in V$, a directed edge e from u to v is denoted as $e = (u, v)$; on the other hand, the tail u and the head v of e are denoted as $t(e)$ and $h(e)$, respectively. For any $U \subseteq V$, let $G[U]$ denote the subgraph of G induced by U .

Recall that a grid graph is a graph whose vertices are located on grid points, and whose vertices are adjacent only to their immediate horizontal or vertical neighbors. We refer to a vertex on the boundary of a grid graph as a *rim vertex*. For any grid graph G , we denote the set of the rim vertices of G as R_G .

Computational model

For discussing sublinear-space algorithms formally, we use the standard multi-tape Turing machine model. A multi-tape Turing machine consists of a read-only input tape, a write-only output tape, and a constant number of work tapes. The space complexity of this Turing machine is measured by the total number of cells that can be used as its work tapes.

For the sake of explanation, we will follow a standard convention and give a sublinear-space algorithm by a sequence of constant number of sublinear-space subroutines A_1, \dots, A_k such that each A_i computes, from its given input, some output that is passed to A_{i+1} as an input. Note that some of these outputs cannot be stored in a sublinear-size work tape;

nevertheless, there is a standard way to design a sublinear-space algorithm based on these subroutines. The key idea is to compute intermediate inputs every time when they are necessary. For example, while computing A_i , when it is necessary to see the j th bit of the input to A_i , simply execute A_{i-1} (from the beginning) until it yields the desired j th bit on its work tape, and then resume the computation of A_i using this obtained bit. It is easy to see that this computation can be executed in sublinear-space. Furthermore, while a large amount of extra computation time is needed, we can show that the total running time can be polynomially bounded if all subroutines run in polynomial-time.

Outline of the algorithm

We show the outline of our algorithm. Our algorithm uses the algorithm `PlanarReach` for the planar graph reachability. We assume both \sqrt{n} and $n^{1/3}$ are integers for simplicity. Let G be an input $\sqrt{n} \times \sqrt{n}$ grid graph with n vertices.

1. Separate G into $n^{1/3} \times n^{1/3}$ small grid graphs, or “blocks”. There are $n^{1/3}$ blocks, and each block contains $n^{2/3}$ vertices.
2. Transform each block B into a special planar graph, “gadget graph”, with $O(n^{1/3})$ vertices. The reachability among the vertices in R_B should be unchanged. The total number of vertices in all blocks becomes $O(n^{2/3})$.
3. We apply the algorithm `PlanarReach` to the transformed graph of size $O(n^{2/3})$, then the reachability is computable in $\tilde{O}(\sqrt{n^{2/3}}) = \tilde{O}(n^{1/3})$ space.

In step 1 and 2, we reduce the number of vertices in the graph G while keeping the reachability between the rim vertices of each block so that we can solve the reachability problem of the original graph. Then to this transformed graph we apply `PlanarReach` in step 3, which runs in $\tilde{O}(n^{1/3})$ space.

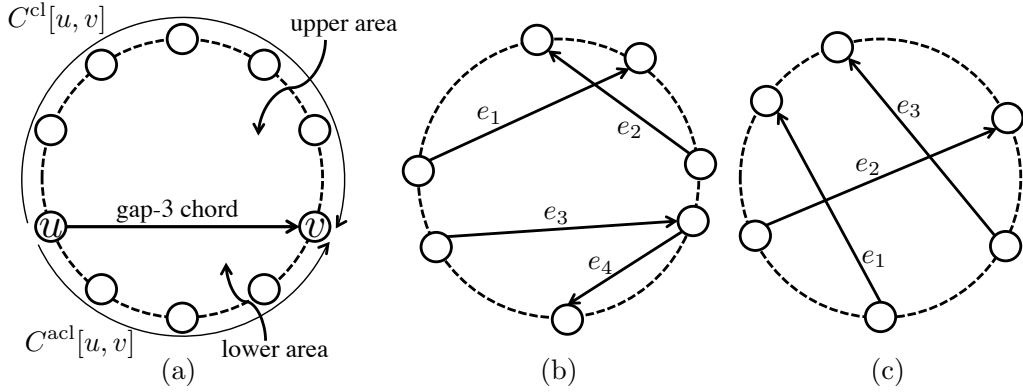
► **Theorem 2.** *There exists an algorithm that computes the grid graph reachability in polynomial-time and $\tilde{O}(n^{1/3})$ space.*

The start vertex s (resp., the end vertex t) may not be on the rim of any block. In such a situation, we make an additional block so that s (resp., t) would be on the rim of the block. This operation would not increase the time and space complexity. In this paper, we assume that s (resp., t) is on the rim of some block.

3 Graph transformation

In this section, we explain an algorithm that modifies each block and analyze time and space complexity of the algorithm. Throughout this section, we let a directed graph $G_0 = (V_0, E_0)$ denote a block of the input grid graph, and let V_0^{rim} denote the set of its rim vertices. We use N to denote the number of vertices of the input grid graph and n to denote $|V_0^{\text{rim}}|$, which is $O(N^{1/3})$; note, on the other hand, that we have $|V_0| = O(n^2) = O(N^{2/3})$. Our task is to transform this G_0 to a plane “gadget graph”, an augmented plane graph, \tilde{G}_p with $O(n) = O(N^{1/3})$ vertices including V_0^{rim} so that the reachability among vertices in V_0^{rim} on G_0 remains the same on \tilde{G}_p .

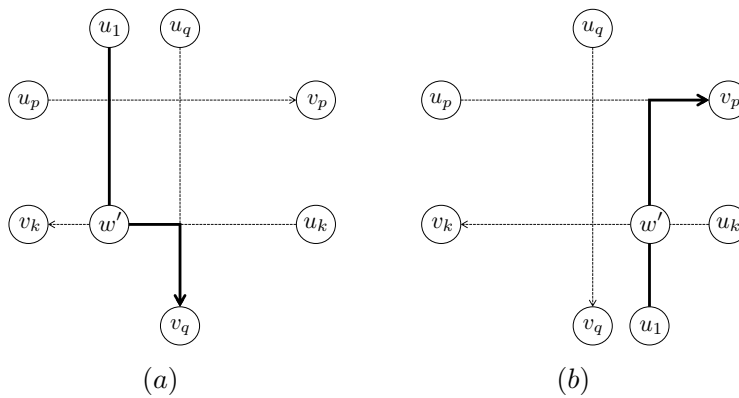
There are two steps for this transformation. We first transform G_0 to a circle graph G_0^{cir} , and then obtain \tilde{G}_p from the circle graph.



■ **Figure 1** An example of the notions on chords. (a) a figure showing a chord, arcs, a lower area, an upper area, (b) a figure showing crossing chords (e_1 and e_2) and semi-crossing chords (e_3 and e_4) and (c) separating chords (e_3 separates e_1 and e_2).

3.1 Circle graph

We introduce the notion of “circle graph”. A *circle graph* is a graph embedded on the plane so that all its vertices are placed on a cycle and all its edges are drawn inside of the cycle. Note that a circle graph may not have an edge between a pair of adjacent vertices on the cycle. We introduce some basic notions on circle graphs. Consider any circle graph $G = (V, E)$, and let C be a cycle on which all vertices of V are placed. For any $u, v \in V$, a *clockwise tour* (resp., *anti-clockwise tour*) is a part of the cycle C from u to v in a clockwise direction (resp., in an anti-clockwise direction). We use $C^{\text{cl}}[u, v]$ (resp., $C^{\text{acl}}[u, v]$) to denote this tour (Figure 1(a)). When we would like to specify the graph G , we use $C_G^{\text{cl}}[u, v]$ (resp., $C_G^{\text{acl}}[u, v]$). The tour $C^{\text{cl}}[u, v]$, for example, can be expressed canonically as a sequence of vertices (v_1, \dots, v_k) such that $v_1 = u$, $v_k = v$, and v_2, \dots, v_{k-1} are all vertices visited along the cycle C clockwise. We use $C^{\text{cl}}(u, v)$ and $C^{\text{acl}}(u, v)$ (resp., $C^{\text{acl}}(u, v)$ and $C^{\text{acl}}(u, v)$) to denote the sub-sequences (v_2, \dots, v_{k-1}) and (v_1, \dots, v_{k-1}) respectively. Note here that it is not necessary that G has an edge between adjacent vertices in such a tour. The length of the tour is simply the number of vertices on the tour. An edge (u, v) of G is called a *chord* if u and v are not adjacent on the cycle C . For any chord (u, v) , we may consider two arcs, namely, $C^{\text{cl}}[u, v]$ and $C^{\text{acl}}[u, v]$; but in the following, we will simply use $C[u, v]$ to denote one of them that is regarded as the arc of the chord (u, v) in the context. When necessary, we will state, e.g., “the arc $C^{\text{cl}}[u, v]$ ” for specifying which one is currently regarded as the arc. A *gap- d* (resp., *gap- d^+*) *chord* is a chord (u, v) whose arc $C[u, v]$ is of length $d + 2$ (resp., length $\geq d + 2$). For any chord (u, v) , the subplane inside of the cycle C surrounded by the chord (u, v) and the arc $C[u, v]$ is called the *lower area* of the chord; on the other hand, the other side of the chord within the cycle C is called the *upper area* (see Figure 1(a)). A *lowest gap- d^+* chord is a gap- d^+ chord that has no other gap- d^+ chord in its lower area. We say that two chords (u_1, v_1) and (u_2, v_2) *cross* if they cross in the circle C in a natural way (see Figure 1(b)). Formally, we say that (u_1, v_1) *crosses* (u_2, v_2) if either (i) u_2 is on the tour $C^{\text{cl}}(u_1, v_1)$ and v_2 is on the tour $C^{\text{acl}}(u_1, v_1)$, or (ii) v_2 is on the tour $C^{\text{cl}}(u_1, v_1)$ and u_2 is on the tour $C^{\text{acl}}(u_1, v_1)$. Also, we say that (u_1, v_1) *semi-crosses* (u_2, v_2) if either (i) u_2 is on the tour $C^{\text{cl}}[u_1, v_1]$ and v_2 is on the tour $C^{\text{acl}}[u_1, v_1]$, or (ii) v_2 is on the tour $C^{\text{cl}}[u_1, v_1]$ and u_2 is on the tour $C^{\text{acl}}[u_1, v_1]$ (see Figure 1(b)). Note that clearly crossing implies semi-crossing. In addition, we say that a chord (u_1, v_1) *separates* two chords (u_2, v_2) and (u_3, v_3) if the endpoints of two chords v_2 and



■ **Figure 2** A common vertex w' of a path from u_k to v_k and a path from u_1 to v_q or v_p for some $p, q < k$.

v_3 are separated by the chord (u_1, v_1) (see Figure 1(c)). Formally, (u_1, v_1) separates (u_2, v_2) and (u_3, v_3) if either (i) v_2 is on the tour $C^{cl}[u_1, v_1]$ and v_3 is on the tour $C^{acl}[u_1, v_1]$, or (ii) v_3 is on the tour $C^{cl}[u_1, v_1]$ and v_2 is on the tour $C^{acl}[u_1, v_1]$. We say that k chords $(u_1, v_1), (u_2, v_2), \dots, (u_k, v_k)$ are *traversable* if the following two conditions are satisfied:

1. (u_1, v_1) semi-crosses (u_2, v_2) ,
2. $\forall i \in [3, k], \exists p, q < i, (u_i, v_i)$ separates (u_p, v_p) and (u_q, v_q) .

Now for the graph $G_0 = (V_0, E_0)$, we define the circle graph $G_0^{cir} = (V_0^{cir}, E_0^{cir})$ by

$$\begin{aligned} V_0^{cir} &= V_0^{rim}, \text{ and} \\ E_0^{cir} &= \{ (u, v) \mid \exists \text{path from } u \text{ to } v \text{ in } G_0 \}, \end{aligned}$$

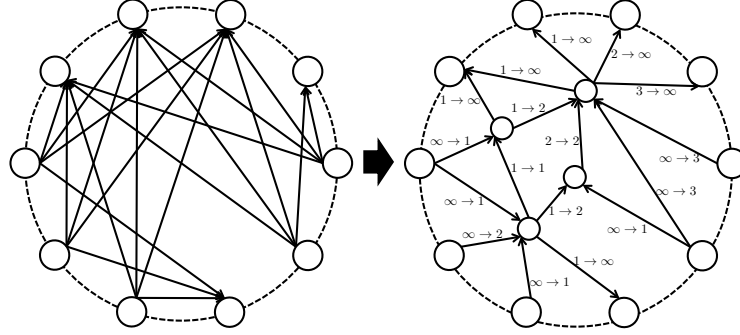
where we assume that the rim vertices of $V_0^{cir} (= V_0^{rim})$ are placed on a cycle C_0 as they are on the rim of the block in the grid graph. Then it is clear that G_0^{cir} keeps the same reachability relation among vertices in $V_0^{cir} = V_0^{rim}$. Recall that G_0 has $O(n^2)$ vertices. Thus, by using PlanarReach, we can show the following lemma.

► **Lemma 3.** G_0^{cir} keeps the same reachability relation among vertices in $V_0^{cir} = V_0^{rim}$. That is, for any pair u, v of vertices of V_0^{cir} , v is reachable from u in G_0^{cir} if and only if it is reachable from u in G_0 . There exists an algorithm that transforms G_0 to G_0^{cir} in $O(n)$ -space and polynomial-time in n .

The notion of traversable is a key for discussing the reachability on G_0^{cir} . Based on the following lemma, we use a traversable sequence of edges for characterizing the reachability on the circle graph G_0^{cir} .

► **Lemma 4.** For a circle graph $G_0^{cir} = (V_0^{cir}, E_0^{cir})$ obtained from a block grid graph G_0 , if there are traversable edges $(u_1, v_1), (u_2, v_2), \dots, (u_k, v_k) \in E_0^{cir}$, then $(u_1, v_k) \in E_0^{cir}$.

Proof. We show that v_k is reachable from u_1 in G_0 by induction on k . First, we consider the case $k = 2$, namely (u_1, v_1) semi-crosses (u_2, v_2) . G_0 contains a path p_{u_1, v_1} which goes from u_1 to v_1 . Also, G_0 contains a path p_{u_2, v_2} which goes from u_2 to v_2 . Since G_0 is planar and $u_1, v_1, u_2,$ and v_2 are the rim vertices and the edges are semi-crossing, there exists a vertex w which is common in p_{u_1, v_1} and p_{u_2, v_2} in G_0 . Since w is reachable from u_1 and v_2 is reachable from w , there exists a path from u_1 to v_2 .



■ **Figure 3** An example of the transformation from a circle graph to a gadget graph.

Next, we assume that the lemma is true for all sequences of traversable edges of length less than k . By the definition, there exist two edges (u_p, v_p) and (u_q, v_q) that the edge (u_k, v_k) separates ($p, q < k$). We have two paths p_{u_1, v_p} from u_1 to v_p and p_{u_1, v_q} from u_1 to v_q in G_0 by the induction hypothesis. Also we have a path p_{u_k, v_k} from u_k to v_k . Since (u_k, v_k) separates (u_p, v_p) and (u_q, v_q) , v_p and v_q are on the different sides of arcs of the edge (u_k, v_k) . If u_1 and v_p are on the same arc of (u_k, v_k) , the paths p_{u_1, v_q} and p_{u_k, v_k} have a common vertex w' (see Figure 2(a)). On the other hand, if u_1 and v_q are on the same arc of (u_k, v_k) , the paths p_{u_1, v_p} and p_{u_k, v_k} have a common vertex w' (see Figure 2(b)). Thus there exists a path from u_1 to v_k via w' in G_0 . ◀

3.2 Gadget graph

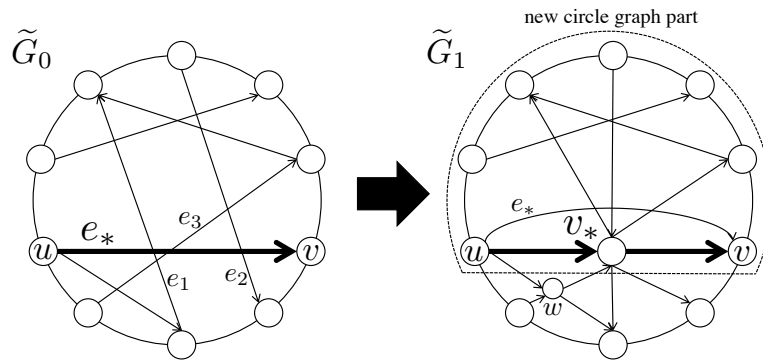
We introduce the notion of “gadget graph”. A gadget graph is a graph that is given a “label set” to each edge.

► **Definition 5.** A *gadget graph* \tilde{G} is a graph defined by a tuple $(\tilde{V}, \tilde{E}, \tilde{K}, \tilde{L})$, where \tilde{V} is a set of vertices, \tilde{E} is a set of edges, \tilde{K} is a *path function* that assigns an edge or \perp to each edge, and \tilde{L} is a *level function* that assigns a *label set* to each edge. A label set is a set $\{i_1 \rightarrow o_1, i_2 \rightarrow o_2, \dots, i_k \rightarrow o_k\}$ of labels where each label $i_j \rightarrow o_j$, $i_j, o_j \in \mathbb{R} \cup \{\infty\}$, is a pair of *in-level* and *out-level*.

Remark. For an edge $(u, v) \in \tilde{E}$, we may use expressions $\tilde{K}(u, v)$ and $\tilde{L}(u, v)$ instead of $\tilde{K}((u, v))$ and $\tilde{L}((u, v))$ for simplicity.

Our goal is to transform a given circle graph (obtained from a block grid graph) $G_0^{\text{cir}} = (V_0^{\text{cir}}, E_0^{\text{cir}})$ in which all vertices in V_0^{cir} are placed on a cycle C to a *plane* gadget graph $\tilde{G}_p = (\tilde{V}_p^{\text{out}} \cup \tilde{V}_p^{\text{in}}, \tilde{E}_p, \tilde{K}_p, \tilde{L}_p)$ where \tilde{V}_p^{out} is the set of *outer vertices* that are exactly the vertices of V_0^{cir} placed in the same way as G_0^{cir} on the cycle C , and \tilde{V}_p^{in} is the set of *inner vertices* placed inside of C . All edges of \tilde{E}_p are also placed inside of C under our embedding. The inner vertices of \tilde{V}_p^{in} are used to replace crossing points of edges of E_0^{cir} to transform to a planar graph (see Figure 3). We would like to keep the “reachability” among vertices in \tilde{V}_p^{out} in \tilde{G}_p while bounding $|\tilde{V}_p^{\text{in}}| = O(n)$.

We explain how to characterize the reachability on a gadget graph. Consider any gadget graph $\tilde{G} = (\tilde{V}, \tilde{E}, \tilde{K}, \tilde{L})$, and let x and y be any two vertices of \tilde{V} . Intuitively, the reachability from x to y is characterized by a directed path on which we can send a token from x to y .



■ **Figure 4** An initial transformation step from \tilde{G}_0 to \tilde{G}_1 .

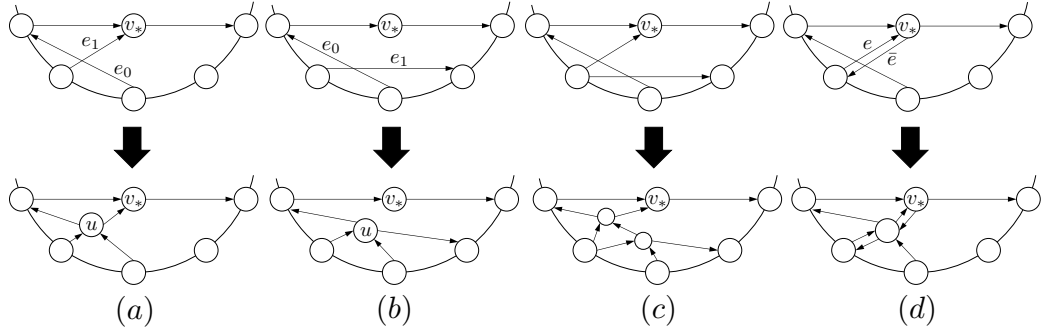
Suppose that there is a directed path $p = (e_1, \dots, e_m)$ from x to y . We send a token through this path. The token has a level, which is initially ∞ when the token is at vertex x . (For a general discussion, we use a parameter ℓ_s for the initial level of the token.) When the token reaches the tail vertex $t(e_j)$ of some edge e_j of p with level ℓ , it can “go through” e_j to reach its head vertex $h(e_j)$ if $\tilde{L}(e_j)$ has an *available label* $i_j \rightarrow o_j$ such that $i_j \leq \ell$ holds for its in-level i_j . If the token uses a label $i_j \rightarrow o_j$, then its level becomes the out-level o_j at the vertex $h(e_j)$. If there are several available labels, then we naturally use the one with the highest out-level. If the token can reach y in this way, we consider that a “token tour” from x to y is “realized” by this path p . Technically, we introduce \tilde{K} so that some edge can specify the next edge. We consider only a path $p = (e_1, \dots, e_m)$ as “valid” such that $e_{i+1} = \tilde{K}(e_i)$ for all e_i such that $\tilde{K}(e_i) \neq \perp$. We characterize the reachability from x to y on gadget graph \tilde{G} by using a valid path realizing a token tour from x to y .

► **Definition 6.** For any gadget graph $\tilde{G} = (\tilde{V}, \tilde{E}, \tilde{K}, \tilde{L})$, and for any two vertices x, y of \tilde{V} , there exists a *token tour* from x to y with initial level ℓ_s if there exists a sequence of edges (e_1, \dots, e_m) that satisfies

1. $x = t(e_1)$ and $y = h(e_m)$,
2. $h(e_i) = t(e_{i+1})$ ($1 \leq i < m$),
3. if $\tilde{K}(e_i)$ is not \perp ($1 \leq i < m$), then $e_{i+1} = \tilde{K}(e_i)$,
4. there exist labels $i_1 \rightarrow o_1 \in \tilde{L}(e_1), \dots, i_m \rightarrow o_m \in \tilde{L}(e_m)$ such that $\ell_s \geq i_1$ and $o_t \geq i_{t+1}$ for all $1 \leq t < m$.

At the beginning of our algorithm, we obtain a gadget graph $\tilde{G}_0 = (\tilde{V}_0, \tilde{E}_0, \tilde{K}_0, \tilde{L}_0)$ whose base graph is equal to G_0^{cir} , and $\tilde{K}_0(e) = \perp$, $\tilde{L}_0(e) = \{0 \rightarrow \infty\}$ for every $e \in \tilde{E}_0$. It is obvious that G_0^{cir} and \tilde{G}_0 have the same reachability. Namely, there exists a token tour from x to y for $x, y \in \tilde{V}_0$ in \tilde{G}_0 if and only if there exists an edge $(x, y) \in \tilde{E}_0$.

We explain first the outline of our transformation from \tilde{G}_0 to \tilde{G}_p . We begin by finding a chord $e_* = (u, v)$ with $\text{gap} \geq 2$ having no other $\text{gap-}2^+$ chord in its lower area, that is, one of the lowest $\text{gap-}2^+$ chords. (If there is no $\text{gap-}2^+$ chord, then the transformation is terminated.) For this e_* and its lower area, we transform them into a planar part and reduce the number of crossing points as follows (see Figure 4): (i) Consider all edges of \tilde{G}_0 crossing this chord e_* (e_1, e_2 and e_3 in Figure 4). Create a new inner vertex v_* of \tilde{G}_p on the chord and bundle all crossing edges going through this vertex v_* ; that is, we replace all edges crossing e_* by edges between their end points in the lower area of e_* and v_* , and edges between v_* and their end points in the upper area of e_* . (ii) Introduce new inner vertices for edges crossing



■ **Figure 5** Examples of vertices made by MakePlanar.

gap-1 chords in the lower area of e_* (w in Figure 4). (iii) Add appropriate label sets to those newly introduced edges so that the reachability is not changed by this transformation. At this point we regard the lower area of e_* as processed, and remove this part from the circle graph part of \tilde{G}_0 by replacing the arc $C[u, v]$ by a tour (u, v_*, v) to create a new circle graph part of \tilde{G}_1 . We then repeat this transformation step on the circle graph part of \tilde{G}_1 . In the algorithm, U_t is the vertices of the circle graph part of \tilde{G}_t , thus $\tilde{G}_t[U_t]$ indicates the circle graph part of \tilde{G}_t . Note that e_* is not removed and becomes a gap-1 chord in the next step.

We explain step (ii) for \tilde{G}_0 in more detail. Since e_* is a gap- 2^+ chord, there exist only gap-1 chords or edges whose one end point is v_* in the lower area of e_* . If there are two edges e_0 and e_1 that cross each other, we replace the crossing point by a new inner vertex u (see Figure 5(a), (b)). The edge e_i becomes two edges $(t(e_i), u)$ and $(u, h(e_i))$ ($i = 0, 1$), and we set $\tilde{K}_1(t(e_i), u) = (u, h(e_i))$. The edges might be divided into more than two segments (see Figure 5(c)). We call the edge of \tilde{G}_0 *original edge* of the divided edges. By the path function, we must move along the original edge. An edge e might have a reverse direction edge $\bar{e} = (h(e), t(e))$ (see Figure 5(d)). In this case, e and \bar{e} share a new vertex for resolving crossing points. For $\tilde{G}_t[U_t]$ ($t > 0$), we process the lower area in the same way. We refer to this algorithm as **MakePlanar**, and the new inner vertices created by **MakePlanar** in step t as V_{MP}^t .

The detailed process of step (iii) is written in Algorithm 2, and Algorithm 1 describes the entire process of step (i), (ii) and (iii). The following lemma shows that an output graph of Algorithm 1 has small size.

► **Lemma 7.** *Algorithm 1 terminates creating a planar graph of size $O(n)$.*

Proof. In the beginning of the algorithm, $|U_0| = n$ and $|U_t|$ decreases by at least 1 for each iteration since the picked edge e_*^t is a gap- 2^+ chord. Hence the algorithm stops after at most n iterations and the number of the new inner vertices made at line 7, or v_*^t , is also at most n . If a gap- k chord is picked, we make at most $2k - 1$ new inner vertices by **MakePlanar**, namely $|V_{\text{MP}}^t| \leq 2k - 1$, since there exist only gap-1 chords in the lower area of the picked edge. The total number of inner vertices becomes at most

$$n + \sum_{i=1}^t (2k_i - 1) = n + 2 \sum_{i=1}^t k_i - t \leq n + 2 \times 2n = 5n$$

where t is the number of iterations and k_i means that a gap- k_i chord was picked in the i -th iteration. After all, $|\tilde{V}_{\text{p}}^{\text{out}} \cup \tilde{V}_{\text{p}}^{\text{in}}| \leq n + 5n = 6n$. ◀

Algorithm 1

Input: A circle graph $G_0^{\text{cir}} = (V_0^{\text{cir}}, E_0^{\text{cir}})$ obtained from a block graph.

Task: Output a plane gadget graph $\tilde{G}_p = (\tilde{V}_p^{\text{out}} \cup \tilde{V}_p^{\text{in}}, \tilde{E}_p, \tilde{K}_p, \tilde{L}_p)$ which satisfies $\tilde{V}_p^{\text{out}} = V_0^{\text{cir}}$ and the reachability among vertices in \tilde{V}_p^{out} in \tilde{G}_p is the same as G_0^{cir} .

- 1: initialize $t = 0$ // loop counter
 - 2: $\tilde{G}_0 = (\tilde{V}_0^{\text{out}} \cup \tilde{V}_0, \tilde{E}_0, \tilde{K}_0, \tilde{L}_0)$ where $\tilde{V}_0^{\text{out}} \leftarrow V_0^{\text{cir}}, \tilde{V}_0 \leftarrow \emptyset, \tilde{E}_0 \leftarrow E_0^{\text{cir}}, \tilde{K}_0(e) \leftarrow \perp, \tilde{L}_0(e) \leftarrow \{0 \rightarrow \infty\}$ for each $e \in E_0^{\text{cir}}$, and $U_0 \leftarrow \tilde{V}_0^{\text{out}}$
 - 3: for every $v \in \tilde{V}_0^{\text{out}}, \ell_{in}^0(v) \leftarrow 0, \ell_{out}^0(v) \leftarrow \infty, p^0(v) \leftarrow v$.
 - 4: **while** $\tilde{G}_t[U_t]$ has a lowest gap- 2^+ chord **do**
 - 5: pick a lowest gap- 2^+ chord e_*^t
 - 6: make a new vertex v_*^t
 - 7: $\tilde{V}_{t+1} \leftarrow \tilde{V}_t \cup \{v_*^t\}$
 - 8: $\tilde{E}_{t+1} \leftarrow (\tilde{E}_t \cup \{(t(e), v_*^t), (v_*^t, h(e)) \mid e \text{ crosses } e_* \text{ or } e = e_*\}) \setminus \{e \mid e \text{ crosses } e_*\}$
 - 9: $U_{t+1} \leftarrow (U_t \cup \{v_*^t\}) \setminus C_{\tilde{G}_t[U_t]}(t(e_*^t), h(e_*^t))$
 - 10: use **MakePlanar** to make the lower area of e_*^t planar and update $\tilde{V}_{t+1}, \tilde{E}_{t+1}$ and \tilde{K}_{t+1} .
 - 11: change the labels by using **Algorithm 2** for keeping reachability
 - 12: output $\tilde{G}_{t+1}[C_{\tilde{G}_t[U_t]}(t(e_*^t), h(e_*^t)) \cup \{v_*^t\} \cup V_{\text{MP}}^t]$, which is the lower area of e_*^t .
 - 13: $t \leftarrow t + 1$
 - 14: **end while**
 - 15: use **MakePlanar** to make $\tilde{G}_t[U_t]$ planar and assign labels by line 17-24 of **Algorithm 2**.
 - 16: output $\tilde{G}_t[U_t \cup V_{\text{MP}}^t]$
-

Now we explain **Algorithm 2** describing how to assign labels to \tilde{G}_{t+1} constructed in **Algorithm 1**. For each outer vertex $v \in \tilde{V}_0^{\text{out}}$, we keep three attributes $p^t(v)$, $\ell_{in}^t(v)$ and $\ell_{out}^t(v)$, and we call them *parent*, *in-level* and *out-level* respectively. We calculate these values from line 2 to 7 and line 25 to 27. $p^t(v)$ is a vertex belonging to the circle graph part of \tilde{G}_t , namely $p^t(v) \in U_t$. From the algorithm, we can show that there are token tours from v to $p^t(v)$ and/or from $p^t(v)$ to v . For the token tour from v to $p^t(v)$, the final level of the token becomes $\ell_{in}^t(v)$. On the other hand, for the token tour from $p^t(v)$, it is enough to have $\ell_{out}^t(v)$ as an initial level to reach v . We will show these facts implicitly in the proof of **Lemma 8**.

At the beginning of each iteration of **Algorithm 1**, we choose a lowest gap- 2^+ chord e_*^t . We collect vertices in U_t which are endpoints for some edges crossing with e_*^t , and we refer to the vertices among them which are in the lower area of e_*^t as S^ℓ and the vertices in the upper area of e_*^t as S^u (see **Figure 6(a)** and line 2). Next we collect vertices whose parents are in S^ℓ (resp., S^u), and we denote them by T^ℓ (resp., T^u) (line 3). Let x' and y' be vertices whose parents are $t(e_*^t)$ and $h(e_*^t)$ respectively. We assign indices to the vertices in T^u and T^ℓ such that the nearer to x' a vertex is located, the larger index the vertex has (see **Figure 6(b)**). We regard T^ℓ as a sequence $(t_1^\ell, t_2^\ell, \dots, t_{|T^\ell|}^\ell)$, and T^u as a sequence $(t_1^u, t_2^u, \dots, t_{|T^u|}^u)$. For each vertex t_i^ℓ in T^ℓ , we calculate $\ell_{in}^{t+1}(t_i^\ell)$ and $\ell_{out}^{t+1}(t_i^\ell)$ in **Algorithm 3**. From line 1 to 4, we decide temporary values of $\ell_{in}^{t+1}(t_i^\ell)$ and $\ell_{out}^{t+1}(t_i^\ell)$ according to reachability among vertices in T^ℓ and T^u in G_0^{cir} . When t_j^u has the maximum index among vertices that t_i^ℓ can reach in T^u , we let $\ell_{in}^{t+1}(t_i^\ell) = j + i/n$. When t_j^u has the minimum index among vertices which can reach t_i^ℓ in T^u , we let $\ell_{out}^{t+1}(t_i^\ell) = j + i/n$. The term i/n is for breaking ties. In the next for-loop, we change the in- and out-levels so that the in-level of the larger indexed vertex is larger than the out-level of the smaller indexed vertex. If there exists a vertex t_j^ℓ such that $i > j$ and $\ell_{out}^{t+1}(t_j^\ell) > \ell_{in}^{t+1}(t_i^\ell)$, then we let $\Delta = (\ell_{out}^{t+1}(t_j^\ell) - j/n) - (\ell_{in}^{t+1}(t_i^\ell) - i/n)$ and add Δ to $\ell_{in}^{t+1}(t_i^\ell)$ and $\ell_{out}^{t+1}(t_i^\ell)$. For preserving the magnitude relationship between in- and out-levels

Algorithm 2

Task: Set \tilde{L}_{t+1} so that \tilde{G}_{t+1} has the same reachability as \tilde{G}_t

- 1: For every edge e appearing in both \tilde{G}_t and \tilde{G}_{t+1} , let $\tilde{L}_{t+1}(e) = \tilde{L}_t(e)$.
- 2: S^ℓ (resp., S^u) $\leftarrow \{v \in U \mid \exists e \in \tilde{E}_t \text{ s.t. } e \text{ crosses } e_*^t, t(e) = v \text{ or } h(e) = v, \text{ and } v \text{ is at the lower (resp., upper) area of } e_*^t\}$
- 3: T^ℓ (resp., T^u) $\leftarrow \{v \in V_0^{\text{cir}} \mid p^t(v) \in S^\ell \text{ (resp. } S^u)\}$
- 4: Fix any vertices $x', y' \in V_0^{\text{cir}}$ such that $p^t(x') = t(e_*^t), p^t(y') = h(e_*^t)$.
- 5: Set an order to T^ℓ according to the order appearing in $C_{G_0^{\text{cir}}}[y', x']$. We regard T^ℓ as a sequence $(t_1^\ell, t_2^\ell, \dots, t_{|T^\ell|}^\ell)$ (see Figure 6(b)).
- 6: Set an order to T^u in the same way as T^ℓ but according to the tour along the other arc. We also regard T^u as a sequence $(t_1^u, t_2^u, \dots, t_{|T^u|}^u)$ (see Figure 6(b)).
- 7: Use Algorithm 3 for calculating $\ell_{in}^{t+1}(v)$ and $\ell_{out}^{t+1}(v)$ for all $v \in T^\ell$.
- 8: **for** $u \in S^\ell$ **do**
- 9: $\tilde{L}_{t+1}(u, v_*^t) \leftarrow \{\ell_{in}^t(v) \rightarrow \ell_{in}^{t+1}(v) \mid p^t(v) = u\}$
- 10: $\tilde{L}_{t+1}(v_*^t, u) \leftarrow \{\ell_{out}^{t+1}(v) \rightarrow \ell_{out}^t(v) \mid p^t(v) = u\}$
- 11: **end for**
- 12: **for** $u \in S^u$ **do**
- 13: $\tilde{L}_{t+1}(u, v_*^t) \leftarrow \{\ell_{in}^t(t_i^u) \rightarrow \max_{t^\ell \in T^\ell} \{\ell_{out}^{t+1}(t^\ell) \mid (t_i^u, t^\ell) \in E_0^{\text{cir}}\} \mid t_i^u \in T^u \text{ and } p^t(t_i^u) = u\}$
- 14: $\tilde{L}_{t+1}(v_*^t, u) \leftarrow \{\min_{t^\ell \in T^\ell} \{\ell_{in}^{t+1}(t^\ell) \mid (t^\ell, t_i^u) \in E_0^{\text{cir}}\} \rightarrow \ell_{out}^t(t_i^u) \mid t_i^u \in T^u \text{ and } p^t(t_i^u) = u\}$
- 15: **end for**
- 16: $\tilde{L}_{t+1}(t(e_*^t), v_*^t) \leftarrow \{\infty \rightarrow 0\}, \tilde{L}_{t+1}(v_*^t, h(e_*^t)) \leftarrow \{\infty \rightarrow 0\}$
- 17: **for all** edge e created by MakePlanar **do**
- 18: Let e' be the original edge of e
- 19: **if** $t(e) = t(e')$ **then**
- 20: $\tilde{L}_{t+1}(e) \leftarrow \{a \rightarrow b \mid a \rightarrow b \in \tilde{L}_t(e')\}$
- 21: **else**
- 22: $\tilde{L}_{t+1}(e) \leftarrow \{b \rightarrow b \mid a \rightarrow b \in \tilde{L}_t(e')\}$
- 23: **end if**
- 24: **end for**
- 25: **for** $v \in \{w \in U_t \mid w \text{ is at the lower area of } e_*^t\}$ **do**
- 26: $p^{t+1}(v) = v_*^t$
- 27: **end for**
- 28: Unchanged $\ell_{in}^t(\cdot), \ell_{out}^t(\cdot)$ and $p^t(\cdot)$ will be taken over to $\ell_{in}^{t+1}(\cdot), \ell_{out}^{t+1}(\cdot)$ and $p^{t+1}(\cdot)$.

Algorithm 3

Task: Calculate $\ell_{in}^{t+1}(v)$ and $\ell_{out}^{t+1}(v)$ for all $v \in T^\ell$.

- 1: **for** $i \in [1, |T^\ell|]$ **do**
- 2: $\ell_{in}^{t+1}(t_i^\ell) \leftarrow \max\{j \mid (t_i^\ell, t_j^u) \in E_0^{\text{cir}}, t_j^u \in T^u\} + i/n$
- 3: $\ell_{out}^{t+1}(t_i^\ell) \leftarrow \min\{j \mid (t_j^u, t_i^\ell) \in E_0^{\text{cir}}, t_j^u \in T^u\} + i/n$
- 4: **end for**
- 5: **for** $i = 1$ to $|T^\ell|$ **do**
- 6: $\Delta \leftarrow \max(0, \max\{\ell_{out}^{t+1}(t_j^\ell) - j/n \mid 1 \leq j < i\} - (\ell_{in}^{t+1}(t_i^\ell) - i/n))$
- 7: **for** $k \in [i, |T^\ell|]$ **do**
- 8: $\ell_{in}^{t+1}(t_k^\ell) \leftarrow \ell_{in}^{t+1}(t_k^\ell) + \Delta$
- 9: $\ell_{out}^{t+1}(t_k^\ell) \leftarrow \ell_{out}^{t+1}(t_k^\ell) + \Delta$
- 10: **end for**
- 11: **end for**

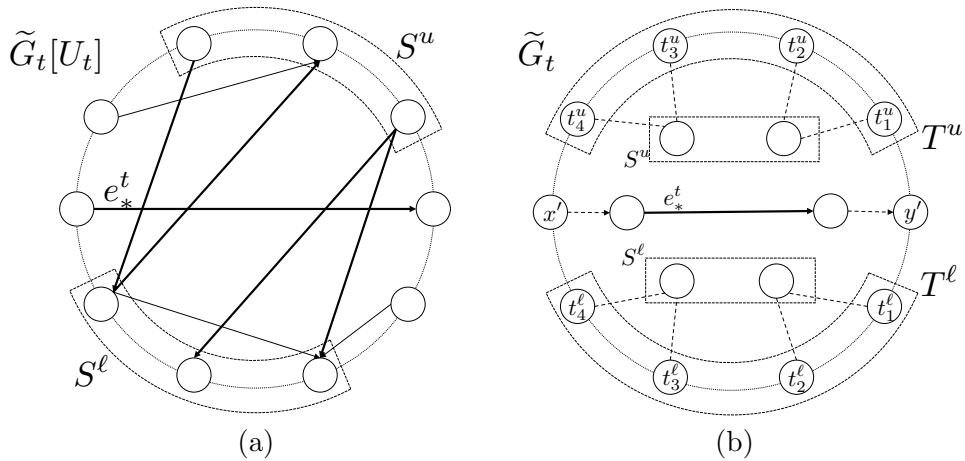


Figure 6 (a) An example of S^ℓ and S^u , (b) An example of T^ℓ and T^u .

of t_i^ℓ and those of t_k^ℓ ($k > i$), we also add Δ to $\ell_{in}^{t+1}(t_k^\ell)$ and $\ell_{out}^{t+1}(t_k^\ell)$.

Back to Algorithm 2. From line 8 to 16, we assign labels to edges newly appearing in \tilde{G}_{t+1} . Let v be any vertex in T^ℓ . For edges in the lower area of e_*^t , the edge $(p^t(v), v_*^t)$ has a label $\ell_{in}^t(v) \rightarrow \ell_{in}^{t+1}(v)$ (line 9), and the edge $(v_*^t, p^t(v))$ has a label $\ell_{out}^{t+1}(v) \rightarrow \ell_{out}^t(v)$ (line 10). Consider edges in the upper area of e_*^t . Let v be any vertex in T^u . The edge $(p^t(v), v_*^t)$ has a label $\ell_{in}^t(v) \rightarrow \ell_{max}$ where ℓ_{max} is the maximum in-level of vertices in T^ℓ that can reach v (line 13). The edge $(v_*^t, p^t(v))$ has a label $\ell_{min} \rightarrow \ell_{out}^t(v)$ where ℓ_{min} is the minimum out-level of vertices in T^ℓ that v can reach (line 14). The edges $(t(e_*^t), v_*^t)$ and $(v_*^t, h(e_*^t))$ have only one label $\infty \rightarrow 0$, which prohibits using these edges (line 16).

From line 17 to 24, we assign labels to edges made by **MakePlanar**. For every edge (u, v) in the lower area of e_*^t , the edge (u, v) might be divided into some edges, for instance $(u, w_1), (w_1, w_2), \dots, (w_k, v)$ by **MakePlanar**. In this case, when (u, v) has a label $a \rightarrow b$, (u, w_1) has a label $a \rightarrow b$ and the other edges have labels $b \rightarrow b$.

From line 25 to 27, we update the parents of the vertices in the lower area of e_*^t . For each vertex v in U_t and in the lower area of e_*^t , we let $p^{t+1}(v) = v_*^t$.

The following lemma shows that paths in \tilde{G}_0 remain in \tilde{G}_t for every t .

► **Lemma 8.** *For any t in Algorithm 1, if there exists an edge from x toward y in \tilde{G}_0 , then there exists a token tour from x to y in \tilde{G}_t whose length is at most $2t + 1$.*

The following lemma shows the other direction: if there exists a token tour from x to y in the gadget graph, then there exists a path from x to y in the circle graph. From Lemma 4, it is enough to prove the following Lemma.

► **Lemma 9.** *For any t and $x, y \in V_0^{\text{cir}}$, if there exists a token tour from x to y in \tilde{G}_t , then there exists a traversable edge sequence (e_1, \dots, e_k) in G_0^{cir} such that $t(e_1) = x$ and $h(e_k) = y$.*

We analyze the space and time complexity of Algorithm 1. Note that, for saving computation space, we do not implement the Algorithm straightforwardly in some points. We begin with the space complexity. We regard the circle graph $G_0^{\text{cir}} = (V_0^{\text{cir}}, E_0^{\text{cir}})$ as the input. For every $v \in V_0^{\text{cir}}$, we keep three attributes $\ell_{in}^t(v)$, $\ell_{out}^t(v)$ and $p^t(v)$ in step t . The in- and out-levels are rational numbers that have the form of $i + j/n$. Thus we keep two integers i and j for each in- and out-level. We use $\tilde{O}(n)$ space for preserving them. In step

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t , we also keep U_t by using $\tilde{O}(n)$ space. We need $\tilde{G}_t[U_t]$, but we do not keep \tilde{E}_t explicitly. For $u, v \in U_t$, whether there exists an edge (u, v) in $\tilde{G}_t[U_t]$ is equivalent to whether there exists an edge (x, y) in E_0^{cir} such that $p^t(x) = u$ and $p^t(y) = v$. Since E_0^{cir} is included in the input, we could calculate it with $\tilde{O}(1)$ space. We keep no other information throughout the Algorithm. The number of edges in $\tilde{G}_t[U_t]$ is at most $2|U_t|^2 = O(n^2)$. Thus, for line 4 and 5, we can find a lowest gap-2⁺ chord by using $\tilde{O}(1)$ space. For line 7 and 9, we use only $\tilde{O}(1)$ space for updating \tilde{V}_t and U_t . For line 8, we ignore the edges in the upper area of e_*^t (these edges belong to $\tilde{G}_{t+1}[U_{t+1}]$, thus we have no need to keep them). For the edges in the lower area of e_*^t , since there exist only gap-1 chords in the area, the number of edges in the area is $O(n)$. We use $\tilde{O}(n)$ space for temporarily keeping them. In **MakePlanar** (line 10), we look through them, and find crossing points and resolve them and set $\tilde{K}_{t+1}(\cdot)$ by using $\tilde{O}(n)$ space.

Now we consider Algorithm 2. The number of edges in $\tilde{G}_t[U_t]$ is at most $2|U_t|^2 = O(n^2)$. Thus, for line 2, we can find S^ℓ and S^u by using $\tilde{O}(1)$ space, and we use $\tilde{O}(n)$ space for keeping them. For line 3 to 6, since $|T^\ell|, |T^u| = O(n)$, we also use $\tilde{O}(n)$ for keeping T^ℓ and T^u . In addition, we use $\tilde{O}(n)$ space for calculating $\ell_{in}^{t+1}(v)$ and $\ell_{out}^{t+1}(v)$ for all $v \in T^\ell$. In Algorithm 3, we use $\tilde{O}(1)$ space for each operation and the length of for-loops is $O(n)$. Thus we use $\tilde{O}(1)$ space in all. For line 8 to 11, we only refer to in- and out-levels that we are keeping. For line 12 to 15, we do not keep and ignore the labels belonging to edges in the upper area. For line 16, we use $\tilde{O}(1)$ space. For line 17 to 24, we check whether an edge in the lower area was divided by **MakePlanar** and we use additional $\tilde{O}(1)$ space. For line 25 to 27, we can find all vertices in the lower area of e_*^t by using $\tilde{O}(n)$ space, and we use additional $\tilde{O}(1)$ space for updating $p^{t+1}(\cdot)$.

We go back to Algorithm 1. For line 12, we output the information of the vertices, edges, labels and values of the path function in the lower area of e_*^t . Here we have to calculate the labels on the gap-1 chords (other information is preserved now). Let the gap-1 chord be (v_*^p, v_*^q) . If $p < q$, this edge was made in step q and the labels on the edge were calculated at line 13 of Algorithm 2. Thus, for any $v \in V_0^{\text{cir}}$ such that $p^t(v) = v_*^p$, we calculate $\ell_{out} = \max_{t^\ell \in V_0^{\text{cir}}, p^t(t^\ell) = v_*^q} \{\ell_{out}^t(t^\ell) \mid (v, t^\ell) \in E_0^{\text{cir}}\}$, and $\ell_{in}^t(v) \rightarrow \ell_{out}$ becomes one of the labels on the edge (if the vertex v is not in T^u , ℓ_{out} is not defined and a label for v does not exist). On the other hand, if $p > q$, this edge was made in step p and the labels on the edge were calculated at line 14 of Algorithm 2. Thus, for any $v \in V_0^{\text{cir}}$ such that $p^t(v) = v_*^q$, we calculate $\ell_{in} = \min_{t^\ell \in V_0^{\text{cir}}, p^t(t^\ell) = v_*^p} \{\ell_{in}^t(t^\ell) \mid (t^\ell, v) \in E_0^{\text{cir}}\}$, and $\ell_{in} \rightarrow \ell_{out}^t(v)$ becomes one of the labels on the edge (if the vertex v is not in T^u , ℓ_{in} is not defined and a label for v does not exist). We use additional $\tilde{O}(1)$ space for these calculation. For line 15, we trace line 10 to 12. In total, we use $\tilde{O}(n)$ space.

Next consider the time complexity. In Lemma 7, we proved that the while-loop at line 4 stops after at most n steps. Since the sizes of U_t , S^ℓ , S^u , T^ℓ and T^u are all $O(n)$, every operation in the Algorithms takes $\text{poly}(n)$ time. Thus this algorithm runs in polynomial time.

► **Lemma 10.** *Algorithm 1 runs in polynomial time with using $\tilde{O}(n)$ space.*

From Lemma 8, 9 and 10, we can obtain \tilde{G}_p with $\tilde{O}(n) = \tilde{O}(N^{1/3})$ space and polynomial time. By applying **PlanarReach** to the plane gadget graph with $O(N^{2/3})$ vertices, we can prove Theorem 2.

4 Conclusion

We presented an $\tilde{O}(n^{1/3})$ space algorithm for the grid graph reachability problem. The most natural question is whether we can apply our algorithm to the planar graph reachability problem. Although the directed planar reachability is reduced to the directed reachability on grid graphs [1], the reduction blows up the size of the graph by a large polynomial factor and hence it is not useful. Moreover, it is known that there exist planar graphs that require quadratic grid area for embedding [10]. However we do not have to stick to grid graphs. We can apply our algorithm to graphs which can be divided into small blocks efficiently. For instance we can use our algorithm for king's graphs [6]. More directly, for using our algorithm, it is enough to design an algorithm that divides a planar graph into small blocks efficiently.

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