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L-STEP MAJORITY LOGIC DECODING

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I. INTRODUCTION

In the search for multiple-error-correcting codes which can be easily and rapidly decoded, some coding theorists have turned their attentions to threshold (majority logic) decoding. Much work has been done in this area recently, and quite a few classes of new codes suitable for threshold decoding have been developed.

The main desirable feature of majority logic decoding is that it can be very simply implemented. While the BCH codes are the best known class of codes for correcting large numbers of random errors, the cost and complexity of its implementation and the time required for decoding leaves something to be desired. On the other hand, although most of the known majority logic decodable codes are somewhat inferior to the BCH codes, the simplicity of a majority logic decoder and the ease of decoding give this method of decoding an attractive prospect. Furthermore majority decoding can also automatically correct many more patterns of errors other than those guaranteed by the decoding schemes without any additional equipment.

This paper is a survey of the L-step majority decoding. One-step majority decoding is studied extensively in a separate report.[3] Majority logic decoding of block codes was first introduced in 1954 by Reed [16] who invented a decoding scheme for the Reed-Muller codes. In 1958 Yale [26] and Zierler [27] applied majority logic decoding to the maximal length sequence codes. In 1961 Mitchell [14] majority decoded the cyclic Hamming codes, the augmented maximal length sequence codes, and the (17,7), (21,11), and (73,45) BCH codes. In his book of 1963, "Threshold Decoding" [13] Massey presented majority decoding algorithms for block codes that are one-step and L-step orthogonalizable. Rudolph's majority decoding algorithm [17,18] differs from

previous ones in that the parity checks need not be orthogonal. He also introduced finite geometry in the construction of majority logic decodable codes. His geometric codes were further studied by Chow,[4] Weldon,[15,24,25] Goethals and Delsarte.[5]

Weldon in 1966 devised a class of cyclic codes based on perfect difference sets.[21] Kasami, Lin, and Peterson [9,10,11] showed that all binary Reed-Muller codes with one digit dropped can be made cyclic by rearranging the digits. Generalization to the nonbinary case was then easily made. The non-primitive Reed-Muller codes were studied by Weldon.[22,23] All these codes are subclasses of Rudolph's projective geometry codes and hence are majority decodable.

In 1967 Townsend and Weldon [20] devised a new class of linear block codes, called self-orthogonal quasi-cyclic codes, which is obtained from disjoint difference sets. The code is one-step decodable. In 1969 Gore [6] generalized Massey's concept of threshold decoding and showed that the Reed-Solomon codes are completely threshold decodable. The formal equivalence between Massey's L-step orthogonalization decoding procedure and Reed's majority decoding scheme is also demonstrated by Gore [7] through the formal equivalence of their respective decoding circuit.

II. L-STEP DECODING ALGORITHM

We shall begin our discussion with a series of definitions and examples to make the understanding of the basic idea behind L-step majority decoding as simple as possible.

Consider an (n,k) block code over GF(q) with parity check matrix [H]. We expand this parity check matrix by taking all possible linear combinations of its rows and call this expanded matrix [H_E]. If $C = (C_0, C_1, \dots, C_{n-1})$ is a code vector, obviously

$$[H_{E}] \cdot c^{T} = 0 \tag{2.1}$$

Each row of equation (2.1) is a parity check equation.

<u>Definition 2.1</u>: A set of J of the parity check equations of equation (2.1) is said to be orthogonal on the ith bit position if the ith bit is checked by every one of those J equations while no other bit position is checked by more than one of the J equations. Let $[H_E^i] = (h_j^i)$ be the submatrix of $[H_E^i]$ corresponding to this set of J equations. Then $[H_E^i]$ will have only nonzero elements in the ith column while each of the remaining columns will have no more than one nonzero entry. For example, the following set of 4 equations is orthogonal on the 0th digit

$$C_0 + C_1 + C_5 = 0$$

 $C_0 + C_2 + C_6 = 0$
 $C_0 + C_3 + C_7 = 0$
 $C_0 + C_4 + C_8 = 0$

$$(2.2)$$

corresponding to the submatrix

$$[H_{E}^{o}] = \begin{bmatrix} 1 & 1 & 0 & 0 & 0 & 1 & 0 & 0 & 0 \\ 1 & 0 & 1 & 0 & 0 & 0 & 1 & 0 & 0 \\ 1 & 0 & 0 & 1 & 0 & 0 & 0 & 1 & 0 \\ 1 & 0 & 0 & 0 & 1 & 0 & 0 & 0 & 1 \end{bmatrix}$$
(2.3)

Suppose $[H_E^i]$ is a submatrix orthogonal on the i^{th} bit position, and let $R = (r_0, r_1, \dots, r_{n-1})$ be a received vector.

Let

$$[H_E^i] \cdot R^T = [S] \tag{2.4}$$

where [S] is a J elements column vector.

Let $R = C + E = (c_0, ..., c_{n-1}) + (e_0, ..., e_{n-1})$, where C is the transmitted code vector and E is the error vector. Then from (2.4)

$$[\mathbf{H}_{E}^{\mathbf{i}}] \cdot \mathbf{R}^{\mathbf{T}} = [\mathbf{H}_{E}^{\mathbf{i}}] \cdot \mathbf{C}^{\mathbf{T}} + [\mathbf{H}_{E}^{\mathbf{i}}] \cdot \mathbf{E}^{\mathbf{T}} = \mathbf{0} + [\mathbf{H}_{E}^{\mathbf{i}}] \cdot \mathbf{E}^{\mathbf{T}} = [\mathbf{S}].$$

Hence

$$[H_{E}^{i}] \cdot E^{T} = [S]$$
 (2.5)

is a set of J equations orthogonal on \mathbf{e}_{i} . Equation (2.5) can be written in the alternate expanded form:

$$\sum_{k=0}^{n-1} h_{jk}^{i} \cdot e_{k} = s_{j}, \qquad j = 1,...,J$$
 (2.6)

Multiply both sides of each equation by $(h_{ji}^{i})^{-1}$ for each j = 1,...,J we

obtain

$$e_{i} + (h_{ji}^{i})^{-1} \sum_{k \neq i} h_{jk}^{i} \cdot e_{k} = (h_{ji}^{i})^{-1} \cdot s_{j} = s_{j}^{i},$$

$$j = k, ..., J \qquad (2.7)$$

To obtain an estimate on e_i , we proceed according to theorem 2.2: Theorem 2.2: e_i is estimated to be equal to that value of GF(q) which is assumed by the majority of s_1' , s_2' , ..., s_J' . In case of a tie between zero and a nonzero element, set $e_i = 0$. e_i will be correctly estimated by this decoding procedure if the number of errors $t \le \frac{J}{2}$.

Proof: (a) If $e_i = 0$, then t of the e_j 's, $j \neq i$, are nonzero. Since each e_j , $j \neq i$, is in no more than one of the equations in (1.7), at most t of the J s_j 's are nonzero. Hence if $t \leq \frac{J}{2}$, at least half of the s_1 ', s_2 ', ..., s_J ' are equal to 0.

(b) If $e_i = \alpha$, $\alpha \in GF(q)$, $\alpha \neq 0$, then t-1 of the e_j 's, $j \neq i$, are nonzero, each appearing in no more than one of the J equations in (2.7). Thus if $t \leq \frac{J}{2}$, at least $J - (t-1) = J+1-t \geq J+1 - \frac{J}{2} = 1 + \frac{J}{2} > \frac{J}{2}$, i.e., more than half, of the $J s_j$'s are equal to α . Hence in both cases, the decoding algorithm works.

Q.E.D.

Theorem 2.2 extends naturally to the decoding of an entire received vector. If for each bit position a set of J or more parity check equations orthogonal on that bit position can be found, then each digit of a received vector can be correctly decoded by the algorithm of theorem 2.2 if a total of

no more than $\left[\frac{J}{2}\right]$ (where $\left[\right]$ stands for "the integer part of") errors have occurred. In particular, for a cyclic code, if a set of J parity check equations orthogonal on any bit position can be found, then by the cyclical nature of the code, such a set can be found for every bit position, and hence the entire code vector can be majority decoded using theorem 2.2. For a systematic code, decoding is complete when the first k (information) digits are determined. Hence for complete decoding of a systematic code, J orthogonal parity checks are necessary only for each of the first k digits.

Theorem 2.3: If a set of J parity check equations can be formed orthogonal on every bit position, then the code has minimum distance at least J + 1.

Proof: Assume that there is a nonzero code vector of weight J or less. A set of J orthogonal parity checks can be formed on one of the nonzero digits. The remaining J-1 or less nonzero digits of the code vector can appear in at most that many parity checks, leaving at least one parity check nonzero, which is not possible. Hence each code vector must have weight J+1 or greater.

Q.E.D.

Corollary 2.4: For a code in systematic form, if a set of J parity check equations orthogonal on each of the information digits can be formed, then the code has minimum distance at least J+1.

Proof: Observing that every nonzero code vector has at least one nonzero information digit, the proof follows similarly to that of theorem 2.3.

As an example of a parity check matrix of a systematic code orthogonal on each of the first k digits, consider the parity check matrix of the following (26,13) code:

Here J = 4 and d = 5. The code can therefore correct 2 errors by the algorithm of theorem 2.2. The code can be over any finite Galois field.

Thus far, the decoding of a digit depends on the existence of a set of parity check equations orthogonal on that digit which are obtainable directly from the original parity check matrix. Such majority decoding is called one-step decoding. However, not all codes can be decoded by such a procedure. For some codes it is necessary to arrive at a set of parity check sums orthogonal on a digit from the original parity check matrix in L steps.

<u>Def 2.5</u>: A set of J parity check equations is said to be orthogonal on the sum of i bit positions $(\alpha_1, \alpha_2, \ldots, \alpha_i)$ if the coefficients in positions $\alpha_1, \alpha_2, \ldots, \alpha_i$ are 1 in each equation and no other position has more than one nonzero coefficient in the J parity check equations.

From the original parity check matrix, suppose that we can find sets of parity checks orthogonal on selected sums of noise bits. By applying the algorithm of theorem 2.2 on these sums, they can be determined. These sums of

noise bits can then be regarded as new check sums and used to form new sets of parity check sums orthogonal on some smaller sums. Eventually after repeating such steps L times a set of parity check sums orthogonal on a bit position is formed. Such a decoding procedure is called L-step decoding. For a cyclic code, a procedure for decoding one digit decodes all the other digits.

III. HAMMING CODES

Massey has shown that for M = 2,3,4,..., the Mth-order binary Hamming $(2^M-1, 2^M-M-1)$, d = 3, codes can be L-step orthogonalized for L no greater than M-1. However, it has been shown in a separate report on one-step decoding [3] that all Hamming codes can be one-step decoded by a somewhat different one-step decoding algorithm. Since one-step decoding is simpler than L-step decoding, we shall just look at one example of L-step decoding of a Hamming code for illustration purpose and then move along.

Example: Consider the (7,4) binary Hamming code with d=3. Here M=3 and it will be shown that the code can be 2-step decoded.

The parity check matrix is given by:

$$[H] = \begin{bmatrix} 1 & 1 & 0 & 1 & 1 & 0 & 0 \\ 1 & 1 & 1 & 0 & 0 & 1 & 0 \\ 1 & 0 & 1 & 1 & 0 & 0 & 1 \end{bmatrix}$$
 (3.1)

The corresponding parity checks are

$$e_0 + e_1 + e_3 + e_4 = S_1$$
 (3.2)

$$e_0 + e_1 + e_2 + e_5 = S_2$$
 (3.3)

$$e_0 + e_2 + e_3 + e_6 = S_3$$
 (3.4)

Equations (3.2) and (3.3) are 2 equations orthogonal on $e_0 + e_1$ while (3.3) and (3.4) are orthogonal on $e_0 + e_2$. Thus if no more than one error has occurred, the majority decoding algorithm of theorem 2.2 can 2.2 can be used to determine $e_0 + e_1$ and $e_0 + e_2$ from these two sets of orthogonal equations.

Let

$$e_0 + e_1 = t_1$$
 (3.5)

and

$$e_0 + e_2 = t_2$$
 (3.6)

Now equations (3.5) and (3.6) are a set of 2 equations orthogonal on \mathbf{e}_0 , and hence theorem 2.2 can again be applied to determine \mathbf{e}_0 provided that no more than one error has occurred. Thus \mathbf{e}_0 can be estimated by the above procedure of 2-step orthogonalization and decoding. A similar procedure can be followed for \mathbf{e}_1 , \mathbf{e}_2 , and \mathbf{e}_3 .

IV. SOME CLASSES OF CODES BASED ON FINITE GEOMETRIES

A class of codes can be defined by taking the incidence matrix of a (b, v, r, k, λ) balanced incomplete block design (BIBD)[12] as its parity check matrix.[17,18] I have called this class of codes the class I BIBD codes.[3] The codes can be one-step decoded by a majority algorithm that does not depend on orthogonal parity checks,[18] and can correct $\left[\frac{r+\lambda-1}{2\lambda}\right]$ errors by such a procedure. The investigation of the possibility of L-step decoding this class of codes is worthwhile if better error correcting capabilities can be achieved by such a procedure than by one-step decoding. So far such an investigation has only been performed on two subclasses of the class I BIBD codes, namely those BIBD's that are related to the projective geometry [1] and the Euclidean geometry. It is well known that block designs can be derived from both of these geometries. We shall now first review some of the properties of finite geometries.

A. Projective geometry

An m-dimensional finite projective geometry over $GF(p^t)$ is denoted by $PG(m, p^t)$. A $PG(m, p^t)$ may be considered as a set of points and subspaces. Each point can be represented by a nonzero element of a $GF(p^{(m+1)t})$. Two elements α and β of $GF(p^{(m+1)t})$ represent the same point if $\alpha = \theta \circ \beta$, where $\theta \in GF(p^t)$. There are thus $v = (p^{(m+1)t}-1)/(p^t-1)$ points in a $PG(m, p^t)$. A set of all the points linearly dependent on s+1, s<m, linearly independent points over $GF(p^t)$ constitutes an s-dimensional subspace $PG(s, p^t)$. There are

$$b(s,m,p^{t}) = \frac{(p^{(m+1)t}-1)(p^{mt}-1)\dots(p^{(m-s+1)t}-1)}{(p^{(s+1)t}-1)(p^{st}-1)\dots(p^{t}-1)}$$
 distinct PG(s,p^t) contained

in a given $PG(m, p^t)$. Furthermore, each $PG(n, p^t)$, n<s, is contained in exactly

$$\lambda(n, s, m, p^{t}) = \frac{(p^{(m-n)t}-1)(p^{(m-n-1)t}-1)\dots(p^{(m-s+1)t}-1)}{(p^{(s-n)t}-1)(p^{(s-n-1)t}-1)\dots(p^{t}-1)}$$
(4.1)

 $PG(s, p^t)$ which are contained in a given $PG(m, p^t)$. By associating the points with varieties, the $PG(s, p^t)$ with blocks, one can obtain from a given $PG(m, p^t)$ a BIBD with parameters:

$$b = b(s, m, p^{t})$$

$$v = (p^{(m+1)t}-1) / (p^{t}-1)$$

$$k = \frac{(p^{(s+1)t}-1)}{(p^{t}-1)}$$

$$r = \lambda(0, s, m, p^t)$$
 (4.2)

$$\lambda = \begin{cases} \lambda(1, s, m, p^t) & \text{for } s > 1 \\ \\ 1 & \text{for } s = 1 \end{cases}$$

The incidence matrix $[S] = (s_{ij})$ of a $PG(m, p^t)$ is defined by setting $s_{ij} = 1$ if the jth point is in the ith $PG(s, p^t)$, and $s_{ij} = 0$ otherwise.

Theorem 4.1: The incidence matrix [S] of a PG(m, p^t) is cyclic, i.e., every cyclic permutation of a row of [S] is also a row of [S].

Proof: Let α be a primitive root of $GF(p^{(m+1)t})$. Then α^V is a primitive root of $GF(p^t)$. α^0 , α^1 ,...., α^{V-1} are linearly independent over $GF(p^t)$ and are used to represent all the V points in a $PG(m, p^t)$. Let β_0 , β_1 ,..., β_s be a set of s+1 linearly independent points defining a $PG(s, p^t)$, which is all the distinct linear combinations of the β 's over $GF(p^t)$. Each point can be expressed in the form

$$c_{i} = \sum_{j=0}^{s} \theta_{ij} \beta_{j}$$
, $\theta_{ij} \in GF(p^{t})$.

Cyclic permutation of the row representing this $PG(s, p^t)$ is equivalent to multiplying each point C_i by α , where α^v is identical to α^0 . Since

$$\alpha \cdot C_{i} = \sum_{j=0}^{s} \theta_{ij} (\alpha \beta_{j}),$$

this results in a set of points which are all the distinct linear combinations of the $\alpha\beta$'s. It only remains to show that $\alpha\beta$ 0, $\alpha\beta$ 1,..., $\alpha\beta$ s are a set of s+1 linearly independent points and hence also defines a PG(s, p^t). Assuming that they are linearly dependent over GF(p^t), then

$$\sum_{j=0}^{s} \omega_{j}(\alpha\beta_{j}) = 0, \qquad \omega_{j} \in GF(p^{t})$$

for some ω_j , j = 0, 1,..., s. However since $\alpha \neq 0$, this implies that

$$\sum_{j=0}^{s} \omega_{j} \beta_{j} = 0,$$

which is a contradiction. Hence $\alpha\beta_0,\ldots,\alpha\beta_s$ are linearly independent.

Because of theorem 4.1, in the construction of an incidence matrix of a $PG(m, p^t)$ one does not have to determine all the b distinct $PG(s, p^t)$. One only has to determine the b/v $PG(s, p^t)$ which cannot be obtained from each other by permutation on the incidence matrix, and permutes these rows to obtain the remaining rows.

B. Euclidean geometry

From a given $PG(m, p^t)$, if any $PG(m-1, p^t)$ and all its points are deleted, a new system of points and subspaces results. This new m-dimensional geometry over $GF(p^t)$ is called the Euclidean geometry, denoted by $EG(m, p^t)$. Each point of an $EG(m, p^t)$ can be represented by an element of a $GF(p^{mt})$. There are thus

$$v = p^{mt}$$

points in an EG(m, p^t). Each PG(s, p^t) contained in the original PG(m, p^t) but not in the deleted PG(m-1, p^t) becomes an EG(s, p^t), since by deleting a PG(m-1, p^t) from PG(m, p^t) we also delete a PG(s-1, p^t) from each of these PG(s, p^t). Therefore the number of EG(s, p^t) contained in an EG(m, p^t) is

$$b(s, m, p^{t}) - b(s, m-1, p^{t}).$$

The number of EG(s, p^t) containing a given EG(n, p^t), n < s, is the same as the number of PG(s, p^t) containing a given PG(n, p^t), namely λ (n, s, m p^t). Let β_1 , β_2 ,..., β_s be s elements of GF(p^{mt}) linearly independent over GF(p^t). Then an EG(s, p^t) can be considered as the set of points $a_1\beta_1+a_2\beta_2+...+a_s\beta_s+\gamma$

where a_1, a_2, \ldots, a_s run independently over $GF(p^t)$ and γ is some element in $GF(p^{mt})$. As with the projective geometry, the $EG(s, p^t)$ of an $EG(m, p^t)$ form a balanced incomplete block design with parameters

$$b = b(s, m, p^{t}) - (b(s, m-1, p^{t}))$$

$$v = p^{mt}$$

$$r = \lambda(0, s, m, p^{t})$$

$$k = p^{st}$$

$$\lambda = \begin{cases} \lambda(1, s, m, p^{t}) & \text{if } s < 1\\ 1 & \text{if } s = 1 \end{cases}$$
(4.3)

C. Class A PG Codes

As stated in the beginning of this section, the class I BIBD codes are defined by taking the incidence matrix of a balanced incomplete block design as its parity check matrix. We shall now define the class A PG codes by taking the incidence matrix of a projective geometry as its parity check matrix. Since a balanced incomplete block design can be derived from a projective geometry, the class A PG code is naturally a subcode of the class I BIBD code. Since there are m-1 levels of PG(s, p^t) in a PG(m, p^t), corresponding to s=1,2,...,m-1, there are m-1 different incidence matrices for each PG(m, p^t). Furthermore, the code which is defined by an incidence matrix of a projective geometry may be taken over any finite Galois field. Hence there can be infinitely many class A PG codes.

The class A PG codes can be one-step majority decoded by an algorithm which does not depend on orthogonal parity checks. The number of errors that

can be corrected by this procedure is

$$t = \frac{r + \lambda - 1}{2\lambda} \tag{4.4}$$

where r and λ are as in (4.2). However the class A PB codes can also be L-step majority decoded which may or may not yeild better error correcting capabilities. Consider the incidence matrix of the b distinct PG(1, p^t), every pair of points is in one and only one PG(1, p^t). Hence a set of r orthogonal parity ckecks can be formed on any bit position, and the code is one-step orthogonalizable. From now on we shall only consider the case s > 1 where the code cannot be one-step orthogonalized.

Now consider the parity check matrix of a class A PG code which is the incidence matrix of the b distinct PG(s, p^t) in a PG(m, p^t). Each PG(s-1, p^t) of PG(m, p^t) is contained in exactly

$$J^{o} = \lambda(s-1, s, m, p^{t}) = \frac{p^{(m-s+1)t}-1}{p^{t}-1}$$
 (4.5)

 $PG(s, p^t)$. Since the s linearly independent points defining a given $PG(s-1, p^t)$ plus another point not in this $PG(s-1, p^t)$ define one and only one $PG(s, p^t)$, each point in the $PG(m, p^t)$ which is not in a given $PG(s-1, p^t)$ appears in at most one of the J^O $PG(s, p^t)$ containing this $PG(s-1, p^t)$. Consequently a set of J^O parity checks orthogonal on the points of each $PG(s-1, p^t)$ can be formed. In general, a given $PG(s-i-1, p^t)$ is contained in exactly

$$J^{i} = \lambda(s-i-1, s-i, m, p^{t}) = \frac{p^{(m-s+i+1)t}-1}{p^{t}-1}$$
 (4.6)

PG(s-i, p^t). These again form Jⁱ orthogonal parity check sums on the points of the given PG(s-i-1, p^t). Thus beginning with i=0 and finishing with i=s-1, one arrives in s steps at a set of J^{s-1} parity check sums orthogonal on one point. Looking at equation (4.6) one can see that J^o < J¹ < ... < J^{s-1}. Applying the majority decoding algorithm of theorem 2.2 at each step, the error at a point can eventually be correctly estimated if the total number of errors t $\leq \frac{\min \left\{J^i\right\}}{2} = \frac{J^o}{2}$.

As already pointed out, a class A PG code defined by an incidence matrix [S] of a projective geometry may be taken over any GF(q). The choice of GF(q) does not affect the error correcting ability of the code, as is obvious in the above discussion. The number of check digits per block length is equal to the rank of [S], the parity check matrix, over GF(q). Thus the efficiency of the code depends on the choice of the field. All the work done in this area so far has been limited to the choice of q = p, where the code is commonly known as projective geometry code. However, since all practical codes are binary codes, GF(2) seems to be a more practical choice although this may yield a less efficient code.

To summarize, the class A PG code can correct $\left\lceil \frac{r+\lambda-1}{2\lambda} \right\rceil$ errors using one-step decoding and $\left\lceil \frac{J^0}{2} \right\rceil$ errors using L-step decoding. The choice of which decoding procedure to use is not obvious. One-step decoding is easier to implement and is definitely the better choice when it can correct the same number of errors as L-step decoding, i.e., $\left\lceil \frac{r+\lambda-1}{2\lambda} \right\rceil = \left\lceil \frac{J^0}{2} \right\rceil$. However in the case where $\left\lceil \frac{J^0}{2} \right\rceil > \left\lceil \frac{r+\lambda-1}{2\lambda} \right\rceil$, a trade off must be made between ease of implementation on one hand and error correcting capabilities on the other.

D. Class A EG Codes

A class A EG code can be defined in a similar manner as the class A PG code by taking the incidence matrix [S] of all the EG(s, p^t) contained in an EG(m, p^t) as the parity check matrix. This class A EG code is also a subcode of the class I BIBD code. The field of the code can again be chosen arbitrarily without affecting the error correcting capabilities of the code. The choice of the field GF(q), however, does affect the number of check digits which is the rank of [S] over GF(q). When q = p, the code is commonly known as Euclidean geometry code. However GF(2) is a more suitable choice for practical applications.

When one-step majority decoding is applied, the class A code can correct $\left[\frac{r+\lambda-1}{2\lambda}\right]$ errors, where r and λ are as in (4.3). Just as the class A PG code, the class A EG code can also be L-step decoded. The procedure is exactly analogous to that of the PG code. Each EG(s-i-1, p^t) is contained in exactly J^i EG(s-i, p^t). Since the (s-i-1) linearly independent points defining an EG(s-i-1, p^t) plus another point not in this EG(s-i-1, p^t) define one and only one EG(s-i-1, p^t), each point in the EG(m, p^t) which is not in a given EG(s-i-1, p^t) is in at most one of the J^i EG(s-i, p^t) containing this EG(s-i-1, p^t). Thus in s steps a set of J^{s-1} EG(1, p^t) orthogonal on a point can be formed. Applying the majority decoding algorithm of theorem 2.2 at each step, the error at a point can eventually be correctly estimated if the total number of errors $t \leq \frac{J^0}{2}$.

E. Class B PG Codes

The incidence matrix of a balanced incomplete block design can also be used to generate another class of codes, which I have called the class III

BIBD codes.[3] Consider the parity check matrix [H] of a code which is in the systematic form. Then [H] is of the form

$$[H] = [P \quad I] \tag{4.7}$$

where I is an identity matrix. A class III BIBD code is defined by substituting the incidence matrix [S] of a balanced incomplete block design into P of (4.7). If [S] is also the incidence matrix of a projective geometry, then we define the resulting code to be a class B PG code. Naturally this code is a subclass of the class III BIBD codes. Once again the choice of the field of the code can be arbitrary. However, here the choice of the field does not have any effect on the efficiency of the code, since from (4.7) it is obvious that the code has block length b+v, b parity check digits, and v information digits, where b and v are as in (4.2). Furthermore since b \geq v, the class B PG code can only have a maximum efficiency of no more than 0.5.

Since the (b+v,v) class B PG code is in systematic form, the first v digits of each code word are the information digits. For each received vector, it is then only necessary to decode these first v digits. It is now obvious that each of these first v digits can be decoded in exactly the same manner as the decoding of each digit of a class A PG code, because each parity check digit appears in one and only one parity check equation. Consequently a class A PG code and a class B PG code can correct $\left\lceil \frac{r+\lambda-1}{2\lambda} \right\rceil$ errors using onestep decoding and $\left\lceil \frac{J^0}{2} \right\rceil$ errors using L-step decoding. The choice of which decoding procedure to use faces the same considerations as those of the class A codes.

F. Class B EG Codes

V. GENERALIZED THRESHOLD DECODING

In all of the previous sections, L-step majority logic decoding had depended on the existence of a set of J parity check equations orthogonal on the sum of s positions, and the coefficients of each of these s positions are 1 in all of the parity check equations. If any of those coefficients is other than 1, the decoding scheme will obviously still work provided that the coefficients of each of the s positions are the same throughout the set of J parity check equations. In other words, if $a_1e_1+b_1e_2$ and $a_2e_1+b_2e_2$ are in two different parity check equations orthogonal on e and e, majority logic decoding is seen to work easily just as well in determining the sum ae 1+be 2 provided $a_1 = a_2 = a$ and $b_1 = b_2 = b$. However if $a_1 \neq a_2$ or $b_1 \neq b_2$, it is not obvious at all that majority decoding would work. After all it does not make any sense to take a majority vote on $a_1e_1 + b_1e_2$ and $a_2e_1 + b_2e_2$ when they are two different entities. Thus if a set of J parity check equations is orthogonal on s positions with the coefficients in these positions arbitrary, it seems apparent that the previously given majority decoding algorithm cannot be applied. However, by interpreting the meaning of a parity check in a somewhat different manner, Gore was able to show that our previously given majority decoding algorithm can still be applied in precisely the same form to the case of arbitrary coefficients. He called it "generalized threshold decoding"[6] although the algorithm is the same.

A. Generalized threshold decoding

Consider a parity check equation on position i and s other positions (m_1, m_2, \ldots, m_s) . With the coefficient in position i normalized, the equation

can be written as

$$e_{i} + \sum_{j=1}^{s} \beta_{m_{j}} e_{m_{j}} = A.$$
 (5.1)

We can interpret (5.1) as an equation which gives the error digit in position i if no errors have occurred in positions m_1 , m_2 , ..., m_s . Such an equation is called a generalized parity check equation and is written as

$$C(i; m_1, m_2, ..., m_s) = A.$$
 (5.2)

Thus every parity check equation can be written as some generalized parity check equation.

We define here again a set of J parity check equations to be orthogonal on position i and s other positions (m_1, m_2, \ldots, m_s) if the coefficient in position i is 1 in each equation, and the coefficients in positions m_1, m_2, \ldots, m_s are arbitrary, while no other position has more than one nonzero coefficient in the J parity check equations. Also we define a set of J generalized parity check equations to be orthogonal on position i and s other positions (m_1, m_2, \ldots, m_s) if i and m_1, m_2, \ldots, m_s are in every one of these J generalized parity checks while no other position appears in more than one of these equations. Since every parity check equation is in fact a generalized parity check, a set of J parity check equations orthogonal on position i and s other positions (m_1, m_2, \ldots, m_s) can be written as a set J generalized parity check equations orthogonal on the same set of positions. For example, (5.3) is a set of 4 parity check equations orthogonal on position 1 and positions 2 and 3.

(5.3) may be written as a set of 4 generalized parity check equations also orthogonal on position 1 and positions 2 and 3 as in (5.4).

$$C_1(1;2,3,4,8) = A_1$$
 $C_2(1;2,3,5,9) = A_2$
 $C_3(1;2,3,6,10) = A_3$
 $C_4(1;2,3,7,11) = A_4$
(5.4)

The majority logic decoding algorithm given previously in section II can now be applied directly to the case of arbitrary coefficients. This is stated in theorem 5.1.

Theorem 5.1: A generalized parity check equation on position i and s other positions (m_1, m_2, \ldots, m_s) can be constructed from a set of J generalized parity check equations orthogonal on the same set of positions

$$C_1(i; m_1, m_2, ..., m_s, S_1) = A_1$$

 $C_2(i; m_1, m_2, ..., m_s, S_2) = A_2$
 \vdots
 $C_J(i; m_1, m_2, ..., m_s, S_J) = A_J$

where S_1 , S_2 , ..., S_J represent disjoint sets of positions, provided that

[J/2] or fewer errors have occurred. The new generalized parity check equation $C(i; m_1, m_2, \ldots, m_s) = B$ is obtained by setting B to be that value assumed by the majority of the A_j . If zero (0) is assumed by exactly half of the A_j , B is given the value 0.

Proof: The proof is exactly the same as that of Theorem 2.2. Because of the definition of a generalized parity check equation, it is only necessary to show that if no errors occur in m_1, m_2, \dots, m_s , the above assignment of the value of B indeed gives the correct error digit in position i. Thus assuming no errors in positions m_1, m_2, \dots, m_s for all cases, if $e_i = 0$, due to the orthogonality of the set of equations, at most $\left\lfloor \frac{J}{2} \right\rfloor$ of the A_j are incorrect leaving at least $J - \left\lfloor \frac{J}{2} \right\rfloor \ge \left\lfloor \frac{J}{2} \right\rfloor$ of the A_j correctly giving the value of 0. If $e_i \ne 0$, then at most $\left\lfloor \frac{J}{2} \right\rfloor - 1$ of the A_j will be incorrect, leaving at least $J - \left\lfloor \frac{J}{2} \right\rfloor + 1 > \left\lfloor \frac{J}{2} \right\rfloor$ of the A_j correctly giving the value of e_i .

Q.E.D.

If one can apply the algorithm of theorem 5.1 repeatedly on successively smaller and smaller sets of positions m_1, m_2, \ldots, m_s and arrive at the end of L steps a generalized parity check equation on i alone, i.e., $C(i) = e_i$, then L-step majority decoding is achieved. It is now obvious that the only difference between the algorithm of Section II and that of theorem 5.1 lies only in their interpretations of the result of a majority voting. The new interpretation extends the domain of the application of the majority logic decoding to include those cases of arbitrary coefficients, and hence are rightfully called generalized threshold decoding.

As a simple example of the application of this generalized threshold decoding, let us consider again the binary (7,4) Hamming code which has already

been shown to be 2-step decodable previously. Referring to equations (3.2), (3.3), and (3.4), the corresponding generalized parity checks are

$$C_1(0;1,3,4) = S_1$$
 (5.5)

$$C_2(0;1,2,5) = S_2$$
 (5.6)

$$C_3(0;2,3,6) = S_3.$$
 (5.7)

Equations (5.5) and (5.6) are a set of 2 generalized parity checks orthogonal on position 0 and position 1, while (5.6) and (5.7) are orthogonal on position 0 and position 2. Thus if no more than one error has occurred, the generalized threshold decoding algorithm of theorem 5.1 can be used to determine C(0;1) and C(0;2) from these two sets of orthogonal generalized parity checks. Let

$$C(0;1) = t_1$$
 (5.8)

and
$$C(0;2) = t_2.$$
 (5.9)

Now (5.8) and (5.9) are a set of 2 generalized parity checks orthogonal on position 0 alone, and hence theorem 5.1 can again be applied to determine C(0), which is equal to the error digit in position 0. Hence e_0 can be estimated by the above procedure of 2-step majority decoding. A similar procedure can be followed for the remaining digits.

B. Reed-Solomon Codes

The r-th order extended Reed-Solomon (ERS) code is defined to be the code of length $q=p^m$ and over GF(q), whose generator matrix G is given by

where α is a primitive element of GF(q). The code has minimum distance d=q-r and is therefore capable of correcting $\left[\frac{q-r-1}{2}\right]$ or fewer errors. It will be shown that the code can be r-step majority decoded up to its guaranteed minimum distance.

Let (b_1, b_2, \ldots, b_n) be the set of coefficients of a parity check equation. This vector is in the null space of the generator matrix and hence

$$[b_1 \ b_2 \cdots \ b_n]G^T = 0.$$
 (5.11)

Since the rows of G are all linearly independent, the rank of matrix G is (r+1). We can therefore select arbitrarily (q-r-1) of the b_j and determine the remaining (r+1) b_j from (5.11). Suppose we wish to determine the error digit in position i. We can assign the value 1 to b_i and 0 to any (q-r-2) of the remaining (q-1) b_j . Then from (5.11) we can determine the value of b_j for the remaining (r+1) positions $(m_1, m_2, \ldots, m_{r+1})$. Therefore

$$e_i + \sum_{k=1}^{r+1} b_{m_k} e_{m_k} = S$$
 (5.12)

is a parity check equation from which we can obtain a generalized parity check equation on position i and positions $(m_1, m_2, \dots, m_{r+1})$

$$C(i; m_1, m_2, ..., m_{r+1}) = S.$$
 (5.13)

However $(m_1, m_2, \dots, m_{r+1})$ in (5.13) can be chosen arbitrarily from (q-1) positions except i. Hence we can fix r of the positions and choose the (r+1)st position from the remaining (q-r-1) positions successively, yielding a set of J = (q-r-1) generalized parity check equations orthogonal on i and r other positions. If $\left[\frac{J}{2}\right]$ or fewer errors have occurred, we can apply the generalized threshold decoding algorithm of theorem 5.1 and obtain a new generalized parity check on i and the other r positions. Again since the r positions can be chosen arbitrarily, we can repeat the procedure, obtaining at the k-th step a new generalized parith check on i and (r+1-k) other positions from a set of J_k orthogonal generalized parity checks, where

$$J_k = q - r - 2 + k.$$
 (5.14)

Hence at the end of (r+1) steps, the generalized parity check on position i is determined, which is $\mathbf{e_i}$. Further, since (5.14) is a monotonically increasing function of \mathbf{k} , $\mathbf{e_i}$ will be correctly determined by the above (r+1)-step majority decoding procedure if $\left[\frac{\mathbf{J}}{2}\right] = \left[\frac{\mathbf{q-r-1}}{2}\right]$ or fewer errors have occurred, which is the original guaranteed error correcting ability of the code. Hence the r-th order ERS code can be completely (r+1)-step majority decoded.

VI. RUDOLPH'S THRESHOLD DECODING OF CYCLIC CODES

Closely related to the subject of majority decoding is the threshold decoding. In threshold decoding, a threshold function assigns an error digit or output digit to each received vector. Majority logic decoding can obviously be considered as one form of threshold decoding. Rudolph [19] has devised a threshold decoding algorithm which can decode any cyclic code over GF(p) up to its minimum distance by a single threshold element. The algorithm depends on a general decomposition theorem for complex-valued functions defined on the space of all r-tuples with elements from the ring of integers modulo p.

We first define the following notations.

- 1) The symbol \dagger defines the mapping $x^{\dagger} = \xi^{x}$, where $\xi = \exp(2\pi i/p)$. If (a_{ij}) is a matrix, then $(a_{ij})^{\dagger} = (a_{ij}^{\dagger})$.
- 2) A_r stands for the $p^r \times r$ matrix whose rows are the p-ary representations of the numbers $0,1,\ldots,p^r-1$ in that order.
- 3) If $x = \rho \in \theta$ is any complex number where ρ and θ are real numbers and $\rho \geq 0$, $-\frac{1}{2} \leq \theta < \rho \frac{1}{2}$, then we define a threshold function T such that $T(x) = [\theta + \frac{1}{2}]$, where [z] denotes the integer part of z. If y is an integer, then

$$y + T(x) = y + \left[\theta + \frac{1}{2}\right]$$

$$= \left[y + \theta + \frac{1}{2}\right]$$

$$= T(p \in {}^{y+\theta})$$

$$= T(e^{y} \cdot p \in {}^{\theta})$$

$$= T(y^{\dagger} \cdot x). \tag{6.1}$$

Let H be the rXn parity check matrix of a linear cyclic code over GF(p). Let y be a received vector when the code vector c is sent. Then y-c=e where e is the error vector. Let s be the syndrome when y is received. Then $s = yH^T$. The set of all syndromes is the set of p-ary numbers $0,1,\ldots,p^T-1$, i.e., the rows of A_r . Let e_s be the error pattern associated with a syndrome s. Then the decoded code word $\hat{c} = y-e_s$, where $s = yH^T$. Considering only the first digit, then

$$\hat{c}_{0} = y_{0} - e_{s0} \pmod{p}$$
. (6.2)

Let f be a function that maps the syndrome s to the first digit of its corresponding error pattern, i.e., $f(s) = -e_{so}$. Then

$$\hat{c}_{0} = y_{0} + f(s).$$
 (6.3)

By the decomposition theorem for complex-valued functions, there exists a weight vector \mathbf{w} such that $\mathbf{f}(\mathbf{s})$ can be expressed in the form

$$f(s) = T[(sA_r^T)^{\dagger}w]. \qquad (6.4)$$

Since $s = yH^T$,

$$f(s) = T[(yH^{T}A_{r}^{T})^{\dagger}w]$$

$$= T[(yH^{T})^{\dagger}w], \qquad (6.5)$$

where

$$H' = A_{r}H. \tag{6.6}$$

Equation (6.3) can now be rewritten as

$$\hat{c}_{O} = y_{O} + T[(yH'^{T})^{\dagger}w] \quad (mod p). \tag{6.7}$$

By the property (6.1),

$$\hat{c}_{o} = T[y_{o}^{\dagger}(yH^{\dagger})^{\dagger}w] \qquad (mod p). \tag{6.8}$$

Now

$$a^{\dagger} \cdot b^{\dagger} = \epsilon^{a} \cdot \epsilon^{b}$$

$$= \epsilon^{a+b}$$

$$= (a+b)^{\dagger}. \qquad (6.9)$$

Therefore

$$y_{o}^{\dagger}(y_{H}^{T})^{\dagger} = y_{o}^{\dagger}[(y_{o} y_{1} \dots y_{n-1})_{H}^{T}]^{\dagger}$$

$$= [(y_{o} y_{o} \dots y_{o}) + (y_{o} y_{1} \dots y_{n-1})_{H}^{T}]^{\dagger}$$

$$= [(y_{o} y_{1} \dots y_{n-1})]^{\dagger} + (y_{o} y_{1} \dots y_{n-1})_{H}^{T}$$

$$= [(y_{o} y_{1} \dots y_{n-1})_{B}]^{\dagger}, \qquad (6.10)$$

where B is the matrix obtained by adding 1 to each element in the first row of ${\rm H'}^{\rm T}$. Now (6.8) can be rewritten as

$$\hat{c}_{Q} = T[(yB)^{\dagger}w] \qquad (mod p). \tag{6.11}$$

Equation (6.11) is the threshold decoding function for y_0 . To decode the remaining digits, it is only necessary to shift the vector y successively in

(6.11), since the code is a cyclic code. It only remains to find the weight vector w such that it satisfies equation (6.4) and gives the simplest decoding circuit.

As an example, we again consider the binary (7,4) Hamming code, whose parity check matrix is

$$H = \begin{bmatrix} 1 & 1 & 1 & 0 & 1 & 0 & 0 \\ 1 & 1 & 0 & 1 & 0 & 0 & 1 \\ 1 & 0 & 1 & 0 & 0 & 1 & 1 \end{bmatrix}. \tag{6.12}$$

The eight different syndromes and their corresponding error patterns are tabulated below together with the function $f(s) = -e_{so}$:

	s		1			es					f(s)	
0	0	0		0	0	0	0	0	0	0	0	
0	0	1		0	0	0	0	0	1	0	0	
0	1	0		0	0	0	1	0	0	0	0	
0	1	1		0	0	0	0	0	0	1	0	(6.13)
. 1	0	0		0	0	0	0	1	0	0	0	
1	0	1		0	0	1	0	0	0	0	0	
1	1	0	1	0	1	0	0	0	0	0	0	
1	1	1		1	0	0	0	0	0	0	1	

Note that for the binary case $\in \exp(\frac{2\pi i}{2}) = -1$. We now have to find a weight vector w such that (6.4) is satisfied for all s. One of the possible solutions is

$$w = \begin{bmatrix} 2 \\ 1 \\ 1 \\ 0 \\ 1 \\ 0 \\ 0 \\ 0 \end{bmatrix} . \tag{6.14}$$

The matrix H' as defined by (6.6) is obtained by substituting in A_3 and (6.12),

$$H' = \begin{pmatrix} 0 & 0 & 0 & 0 & 0 & 0 & 0 \\ 1 & 0 & 1 & 0 & 0 & 1 & 1 \\ 1 & 1 & 0 & 1 & 0 & 0 & 1 \\ 0 & 1 & 1 & 1 & 0 & 1 & 0 \\ 1 & 1 & 1 & 0 & 1 & 0 & 0 \\ 0 & 1 & 0 & 0 & 1 & 1 & 1 \\ 0 & 0 & 1 & 1 & 1 & 0 & 1 \\ 1 & 0 & 0 & 1 & 1 & 1 & 0 \end{pmatrix} . \tag{6.15}$$

Finally the B matrix in (6.11) is obtained by adding 1 to each element in the first row of the transpose of H'.

$$B = \begin{bmatrix} 1 & 0 & 0 & 1 & 0 & 1 & 1 & 0 \\ 0 & 0 & 1 & 1 & 1 & 1 & 0 & 0 \\ 0 & 1 & 0 & 1 & 1 & 0 & 1 & 0 \\ 0 & 0 & 1 & 1 & 0 & 0 & 1 & 1 \\ 0 & 0 & 0 & 0 & 1 & 1 & 1 & 1 \\ 0 & 1 & 0 & 1 & 0 & 1 & 0 & 1 \\ 0 & 1 & 1 & 0 & 0 & 1 & 1 & 0 \end{bmatrix} . \tag{6.16}$$

Therefore the threshold function for the first decoded digit is

$$\hat{c}_{o} = T \left\{ \left(y \left(\begin{array}{c} 1 & 0 & 0 & 1 & 0 & 1 & 1 & 0 \\ 0 & 0 & 1 & 1 & 1 & 1 & 0 & 0 \\ 0 & 1 & 0 & 1 & 1 & 0 & 1 & 0 \\ 0 & 1 & 0 & 1 & 1 & 0 & 0 & 1 & 1 \\ 0 & 0 & 0 & 0 & 1 & 1 & 1 & 1 & 1 \\ 0 & 1 & 0 & 1 & 0 & 1 & 0 & 1 \\ 0 & 1 & 1 & 0 & 0 & 1 & 1 & 1 \end{array} \right) \right)^{\dagger} \left(\begin{array}{c} 2 \\ 1 \\ 1 \\ 0 \\ 0 \\ 0 \end{array} \right) \right\}$$

$$= T \left\{ \left(y \left(\begin{array}{c} 1 & 0 & 0 & 0 \\ 0 & 0 & 1 & 1 \\ 0 & 1 & 0 & 1 \\ 0 & 0 & 1 & 0 \\ 0 & 0 & 0 & 1 & 1 \\ 0 & 1 & 0 & 1 \\ 0 & 0 & 0 & 0 \\ 0 & 0 & 1 & 1 \\ 0 & 1 & 0 & 0 \\ 0 & 0 & 1 & 1 \\ 0 & 1 & 0 & 0 \\ 0 & 1 & 1 & 0 \end{array} \right) \right)^{\dagger} \left(\begin{array}{c} 2 \\ 1 \\ 1 \\ 1 \\ 1 \end{array} \right) \right\}. \tag{6.17}$$

The threshold functions for the remaining digits are obtained from (6.17) by simply cyclic shifting the received vector y. Note that since $\epsilon = -1$ for the binary case, the function \dagger simply maps even integers to 1 and odd integers to -1, while the function T maps positive integers to 0 and negative integers to 1.

VII. SUMMARY

In the preceding sections a survey on L-step majority decoding has been given. In Chapter II the procedure of L-step decoding was explained. The application of L-step majority decoding to Hamming codes was shown in Chapter III. In Chapter IV four classes of codes, including two new classes, constructed from finite geometry were introduced. These codes can all be L-step majority decoded. Chapter V explained Gore's generalized threshold decoding which increases the number of classes of majority decodable codes. Finally a new threshold decoding procedure for cyclic codes over GF(p) by Rudolph was presented in Chapter VI.

As a final word, further research should be done in the area of majority logic decoding to find better majority logic decodable codes and decoding algorithm; better codes in the sense of higher efficiency and greater number of correctable errors, and better algorithm in the sense of easier implementation and correcting larger number of errors.

REFERENCES

- 1. Berman, G., 1952, "Finite Projective Geometries," Canadian Journal of Mathematics, Vol. IV, No. 3.
- Bose, R. C., 1939, "On the Construction of Balanced Incomplete Block Designs," Annals of Eugenics, 9, pp. 353-399.
- Chien, R. T. and Ng, S. W., 1970, "One-step Majority Logic Decoding," Coordinated Science Lab., University of Illinois, Urbana, Ill., CSL Report I-138.
- 4. Chow, D., 1967, "A Geometric Approach to Coding Theory with Application to Information Retrieval," Coordinated Science Lab., University of Illinois, Urbana, Ill., CSL Report R-368.
- Goethals, J. M. and Delsarte, P., 1968, "On a Class of Majority Logic Decodable Cyclic Codes," IEEE Trans. Information Theory, IT-14, pp. 182-188.
- 6. Gore, W. C., 1969, "Generalized Threshold Decoding and the Reed-Solomon Codes," IEEE Trans. Information Theory, Vol. IT-15, pp. 78-81.
- 7. Gore, W. C., 1969, "The Equivalence of L-step Orthogonalization and a Reed Decoding Procedure," IEEE Trans, Information Theory, Vol. IT-15, p. 184.
- 8. Graham, R. L. and MacWilliams, F. J., 1966, "On the Number of Information Symbols in Difference Set Cyclic Codes," B.S.T.J., Vol. 45, pp. 1057-1070.
- 9. Kasami, T. and Lin, S., 1966, "Some Codes Which are Invariant Under Doubly Transitive Permutation Group and their Connection with Balanced Incomplete Block Designs," AFCRL-66-142.
- 10. Kasami, T., Lin, S. and Peterson, W. W., 1966, "Some Results on Cyclic Codes which are Invariant Under the Affine Group," AF Cambridge Res. Ctr. Rept. 9, Proj. AF 19 (628) 4371.
- 11. Kasami, T., Lin, S. and Peterson, W. W., 1968, "New Generalization of the Reed-Muller Codes. Part I: Primitive Codes, IEEE Trans. Information Theory, IT-14, pp. 189-199.
- 12. Mann, H. B., 1949, Analysis and Designs of Experiments, Dovis Pulbications, New York.
- 13. Massey, J., 1963, Threshold Decoding, M.I.T. press.

- 14. Mitchell, M. E., et al., 1961, "Coding and Decoding Operations Research," for U.S.A.F. Cambridge Research Ctr., Bedford, Mass., General Electric Co. Final Rept., Contract AF 19 604)-6183.
- Peterson, W. W. and Weldon, E. J., <u>Error-Correcting Codes</u>, 2nd Edition, to be published.
- 16. Reed, I. S., 1954, "A Class of Multiple-Error-Correcting Codes and the Decoding Scheme," IRE Trans. on Information Theory, IT-4, pp. 38-49.
- 17. Rudolph, L. D., 1964, "Geometric Configuration and Majority Logic Decodable Codes," MEE Thesis, University of Oklahoma, Norman, Okla.
- 18. Rudolph, L. D., 1967, "A Class of Majority Logic Decodable Codes," IEEE Trans. Inform. Theory, IT-13, pp. 305-6.
- 19. Rudolph, L. D., 1969, "Threshold Decoding of Cyclic Codes," IEEE Trans. Inform. Theory, IT-15, pp. 414-418.
- 20. Townsend, R. L. and Weldon, E. J., 1967, "Self-Orthogonal Quasi-Cyclic Codes," IEEE Trans. Inform. Theory, IT-13, pp. 183-195.
- 21. Weldon, E. J., 1966, "Difference Set Cyclic Codes," B.S.T.J., Vol. 45, pp. 1045-55.
- 22. Weldon, E. J. 1967, "Non-primitive Reed-Muller Codes," presented at First Princeton Symposium on Circuit Theory.
- 23. Weldon, E. J., 1968, "New Generalization of the Reed-Muller Codes Part II: Nonprimitive Codes," IEEE Trans. Inform. Theory, IT-14, pp. 199-205.
- 24. Weldon, E. J., 1968, "Euclidean Geometry Cyclic Codes," Chapt. 23 in R. C. Bose and T. A. Dowling (eds.) Proceedings of the Conference on Combinatorial Mathematics and Its Applications (Apr. 10-14, 1967), University of North Carolina Press.
- 25. Weldon, E. J., 1968, "Majority-Logic Decodable Codes," Chapt. 11 in Principles of Data Communication by R. W. Lucky, J. Salz, and E. J. Weldon, McGraw Hill.
- 26. Yale, R. B., 1958, "Error-Correcting Codes and Linear Recurring Sequences," M.I.T. Lincoln Lab., Lexington, Mass., Lincoln Laboratory Group Rept. 34-77.
- 27. Zierler, N., 1958, "On a Variation of the First Order Reed-Muller Codes," M.I.T. Lincoln Lab., Lexington, Mass., Lincoln Laboratory Group Rept. 34-80.

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