

---

**CSL** *COORDINATED SCIENCE LABORATORY*

**ON DECODING PROJECTIVE  
GEOMETRY CODES**

C. L. CHEN

**UNIVERSITY OF ILLINOIS - URBANA, ILLINOIS**

ON DECODING PROJECTIVE GEOMETRY CODES

By

C. L. Chen

This work was supported by the Rome Air Development Center under Contract No. F30602-70-C-0014 (EMKC); and by the Joint Services Electronics Program (U. S. Army, U. S. Navy, and U. S. Air Force) under Contract DAAB-07-67-C-0199.

Reproduction in whole or in part is permitted for any purpose of the United States Government.

This document has been approved for public release and sale; its distribution is unlimited.

ON DECODING PROJECTIVE GEOMETRY CODES\*

by

C. L. Chen  
Coordinated Science Laboratory  
University of Illinois at Urbana-Champaign  
Urbana, Illinois

---

\*This work was supported by the Rome Air Development Center under contract No. F30602-70-C-0014 (EMKC) and by the Joint Services Electronics Program (U.S. Army, U.S. Navy, and U.S. Air Force) under Contract DAAB-07-67-C-0199.

### Abstract

In this report, it is shown that Projective Geometry codes are orthogonalizable in less than or equal to 3 steps. Thus, this class of codes is majority-logic decodable in no more than 3 steps.

The improved decoding algorithm reduces the decoding complexity of Projective Geometry codes enormously in most cases. This should make Projective Geometry codes very attractive for practical use on error-control systems.

## 1. Introduction

Projective Geometry (PG) codes<sup>[3,5,7,8]</sup> form a subclass of the class of cyclic codes that are majority-logic decodable.<sup>[4]</sup> Because these codes are majority-logic decodable, they can be very simply implemented. In addition, the decoding algorithm for them can automatically correct more error patterns than those guaranteed by the decoding algorithm itself without additional cost. Thus PG codes are attractive from a practical point of view.

The decoding complexity of PG codes grows exponentially with  $L$ , the number of levels (or steps) of majority logic required.<sup>[5,10]</sup> It is desirable, therefore, to decode these codes in as few steps as possible. Unfortunately, the Reed decoding algorithm<sup>[5,6]</sup> for this class of codes often require that  $L$  be large.

In this report, an improved decoding algorithm for PG codes is presented. It will be shown that PG codes can be orthogonalized<sup>[4,5]</sup> in no more than 3 steps. That is, these codes can be majority-logic decoded in less than or equal to 3 steps. The results reduce the decoding complexity of PG codes enormously in most cases. Thus, they should make this class of codes very attractive for practical use on error-control systems.

The basic concept behind the improved decoding algorithm is also applicable to the Euclidean Geometry (EG) codes<sup>[5,9]</sup> which also form an important subclass of the class of majority-logic decodable codes. The decoding of EG codes is presented in a separate report.<sup>[2]</sup>

In section 2 of this report, we shall review some of the properties of PG codes and discuss the existing decoding algorithms for these codes. In section 3, we shall propose an improved decoding algorithm for PG codes. We shall show that PG codes are majority-logic decodable in less than or equal to 3 steps.

In the following discussions the reader will be assumed to be familiar with the concept of orthogonality in majority-logic decoding<sup>[4,5]</sup> and the structure of finite geometries.<sup>[1,5,8]</sup> Where possible the notation and conventions employed in Reference 5 will be used.

## 2. Projective Geometry Codes

It is well-known that the elements of a Galois field can be arranged in such a way that they form a concrete representation of a finite projective geometry.<sup>[1,5]</sup> Specifically, a projective geometry of dimension  $m$  over  $GF(p^S)$ ,  $PG(m, p^S)$ , can be constructed as follows. A point  $(\alpha)$  of the geometry is associated with the set of elements

$$(\alpha) = \alpha, \alpha\beta, \alpha\beta^2, \dots, \alpha\beta^{p^S-2}$$

where  $\alpha$  is a non-zero element of  $GF(p^{(m+1)S})$  and  $\beta$  is a primitive element of  $GF(p^S)$ . The number of points in the geometry is equal to  $\frac{p^{(m+1)S}-1}{p^S-1}$ .

Let  $\alpha$  be a primitive element of  $GF(p^{(m+1)S})$ . An  $r$ -flat consists of the points  $(\alpha^j)$  such that

$$(\alpha^j) = \beta^{i_1} \alpha^{e_1} + \beta^{i_2} \alpha^{e_2} + \dots + \beta^{i_{r+1}} \alpha^{e_{r+1}} \quad (1)$$

where  $\alpha^{e_1}, \alpha^{e_2}, \dots, \alpha^{e_{r+1}}$  are linearly independent, and  $\beta^{i_1}, \beta^{i_2}, \dots, \beta^{i_{r+1}}$  take on all possible combinations of values in  $GF(p^S)$  except that they cannot be simultaneously zero. Thus an  $r$ -flat contains  $\frac{p^{(r+1)S}-1}{p^S-1}$  points.

The points of  $PG(m, p^S)$  can be considered as the location number of a cyclic code of length  $n = \frac{p^{(m+1)S}-1}{p^S-1}$  over  $GF(p)$ . An  $r$ -flat in  $PG(m, p^S)$  can be

associated with a polynomial in the algebra of polynomials modulo  $x^n-1$ . This polynomial is taken to have coefficient 1 in positions corresponding to the  $\frac{p^{(r+1)s}-1}{p^s-1}$  points of the flat and 0 elsewhere.

A projective Geometry (PG) code of order  $r$  and length  $n = \frac{p^{(m+1)s}-1}{p^s-1}$  with symbols from  $GF(p)$  has the property that the polynomials corresponding to all  $r$ -flats of  $PG(m, p^s)$  are in its null space. Because of this property, the Reed algorithm<sup>[5,6]</sup> can be used to decode a PG code. The key point of the Reed algorithm for PG codes is that the check sums corresponding to the  $r$ -flats intersecting a particular  $(r-1)$ -flat are orthogonal on the check sum corresponding to the  $(r-1)$ -flat. The number  $J$  of  $r$ -flats intersecting a particular  $(r-1)$ -flat can be shown<sup>[3,5,8]</sup> to be equal to

$$J = \frac{p^{(m-r+1)s}-1}{p^s-1} \quad (2)$$

Now, the Reed decoding algorithm for an  $r$ -th order PG code can be described as follows. At the first step of decoding, the check sum corresponding to an  $(r-1)$ -flat is determined by the value assumed by a majority of the check sums corresponding to the  $J$   $(r+1)$ -flats that intersect on the given  $(r-1)$ -flat. The check sums corresponding to all  $(r-1)$ -flats can be correctly determined in this way provided that  $\lceil \frac{J}{2} \rceil^*$  or fewer errors occurred. In the next step of decoding, each of the check sums corresponding to all  $(r-2)$ -flats is determined by a majority decision on a set of check sums corresponding to some  $(r-1)$ -flats that intersect on the given  $(r-2)$ -flat. It is easy to see from Eq.(2) that the number of  $(r-1)$ -flats that intersect on a given  $(r-2)$ -flat is

---

\* $\lceil x \rceil$  is equal to the largest integer less than or equal to  $x$ .

at least equal to  $J$ . Therefore, each of the check sums corresponding to all  $(r-2)$ -flats can be determined provided that  $\lfloor \frac{J}{2} \rfloor$  or fewer errors occurred. This decoding process can be repeated until the error digits corresponding to all 0-flats are determined. It requires  $r$  steps of majority logic elements for this decoding algorithm.

The decoding complexity of the Reed algorithm for PG codes grows exponentially with the number of decoding steps employed. It is important, therefore, to try to cut down the number of decoding steps. In this regard, Weldon proposed a modified decoding algorithm for PG codes.<sup>[5,10]</sup> The modified decoding algorithm can correct as many guaranteed error patterns as the original Reed algorithm.

The modified decoding algorithm by Weldon requires only two steps of majority logic elements. At the first step of decoding, the check sums corresponding to all  $(r-1)$ -flats are determined from the  $r$ -flats that intersect on the given  $(r-1)$ -flats in exactly the same way as in the original Reed algorithm. At the second step, the error digits corresponding to all 0-flats are determined from the  $(r-1)$ -flats using the idea of non-orthogonal check sums due to Rudolph.<sup>[7]</sup> Though this modified algorithm reduces the number of decoding steps to two, the decoder may not cost less than the decoder using the original algorithm. The reason is that a single majority gate with a very large number of inputs has to be used in the second step of the decoding.

In the next section, we shall introduce an improved decoding algorithm for PG codes. This improved algorithm can correct as many guaranteed error patterns as the original algorithm. In addition, the new algorithm reduces the number of decoding steps without increasing the number of inputs to the majority gates in every step of the decoding. Applying the idea behind the new



algorithm, we shall show that PG codes are orthogonalizable in less than or equal to 3 steps. Thus, PG codes are majority-logic decoding in no more than 3 steps.

### 3. Improved Decoding Algorithm

The Reed algorithm for PG codes is a step-by-step decoding algorithm. It determines the next lower dimension of the associated projective geometry from the given dimension of the associated projective geometry. Actually, it is possible to speed up the decoding process by jumping from the flats of a given dimension to the flats of several dimensions lower. Based upon this fact, we have a new improved decoding algorithm.

In the first step of the improved decoding algorithm,  $(r-1)$ -flats are determined from the sets of  $r$ -flats that intersect on the given  $(r-1)$ -flats in the same way as in the Reed algorithm. Now, suppose that  $k$  is the smallest number such that a set of  $J$   $(r-1)$ -flats that intersect on the given  $k$ -flat can be constructed. Obviously  $k$  is less than or equal to  $(r-2)$ . Then, in the second step of decoding, each of the  $k$ -flats is determined from a set of  $J$   $(r-1)$ -flats that are orthogonal on the given  $k$ -flat. This process can be repeated again and again until all error digits or  $0$ -flats are determined.

In general, the problem of finding the smallest  $k$  such that a set of at least  $J$   $(r-1)$ -flats orthogonal on a given  $k$ -flat can be constructed has not been solved. Thus, the number of decoding steps that can be reduced by the improved decoding algorithm can not be expressed explicitly. However, using the basic concept of the improved algorithm, we shall show that PG codes are orthogonalizable in less than or equal to 3 steps. Thus, the number of decoding steps is reduced to no more than 3 for any  $r$ -th order codes.

We have demonstrated in the last section that  $PG(m, p^S)$  can be constructed from  $GF(p^{(m+1)S})$ . By this construction, the elements  $\alpha^j, \beta\alpha^j, \beta^2\alpha^j, \dots, \beta^{p^S-2}\alpha^j$  of  $GF(p^{(m+1)S})$  represent the same point of  $PG(m, p^S)$ , where  $\alpha$  and  $\beta$  are primitive elements of  $GF(p^{(m+1)S})$  and  $GF(p^S)$ , respectively. Thus, there are  $(p^S-1)$  replications of each of the points of the  $r$ -flat in  $GF(p^{(m+1)S})$ .

It is well-known that an  $m$ -dimensional Euclidean geometry  $EG(m+1, p^S)$  can also be constructed from  $GF(p^{(m+1)S})$ . Every element in  $GF(p^{(m+1)S})$  is taken to be a point of  $EG(m+1, p^S)$ . An  $(r+1)$ -flat that passes through the origin consists of the points  $\alpha^j$  such that

$$\alpha^j = \beta^{i_1} \alpha^{e_1} + \beta^{i_2} \alpha^{e_2}, \dots + \beta^{i_{r+1}} \alpha^{e_{r+1}} \quad (3)$$

where  $\alpha^{e_1}, \alpha^{e_2}, \dots, \alpha^{e_{r+1}}$  are linearly independent, and  $\beta^{i_1}, \beta^{i_2}, \dots, \beta^{i_{r+1}}$  take on all possible combinations of values in  $GF(p^S)$ .

Comparing Eq.(1) and Eq.(3), we have the following result.

Lemma 1 The points in the  $(p^S-1)$  replications of an  $r$ -flat of  $PG(m, p^S)$  in  $GF(p^{(m+1)S})$  plus the origin form an  $(r+1)$ -flat in  $EG(m+1, p^S)$  that passes through the origin.

The number  $M$  of  $r$ -flats in  $PG(m, p^S)$  can be shown to be equal to <sup>[5,10]</sup>

$$M = \frac{(p^{S(m+1)}-1)(p^{Sm}-1)\dots(p^{S(m-r+1)}-1)}{(p^{S(r+1)}-1)(p^{Sr}-1)\dots(p^S-1)} \quad (4)$$

It is easy to show that the number of  $(r+1)$ -flats in  $EG(m+1, p^S)$  that passes through the origin is also equal to  $M$ . (See Appendix.) Thus, the converse of Lemma 1 is true.

Lemma 2 Every  $(r+1)$ -flat in  $EG(m+1, p^S)$  that passes through the origin consists of  $(p^S - 1)$  replications of an  $r$ -flat of  $PG(m, p^S)$ .

Lemma 1 and Lemma 2 set up a unique correspondence between an  $r$ -flat of  $PG(m, p^S)$  and an  $(r+1)$ -flat of  $EG(m+1, p^S)$ . From this correspondence, any two  $(r+1)$ -flats of  $EG(m+1, p^S)$  that intersect on a given  $(k+1)$ -flat passing through the origin correspond to two  $r$ -flats of  $PG(m, p^S)$  that intersect on a  $k$ -flat. Therefore, we have

Lemma 3 If it is possible to construct  $I$   $(r+1)$ -flats that are orthogonal on a given  $(k+1)$ -flat passing through the origin in  $EG(m+1, p^S)$ , then it is possible to construct  $I$   $r$ -flats that are orthogonal on a given  $k$ -flat in  $PG(m, p^S)$ .

In Reference 2, it has been shown that EG codes can be orthogonalized in less than or equal to 3 steps. By Lemma 3, we can show that PG codes are also orthogonalizable in no more than 3 steps. To prove this, we shall first list some of the results in Reference 2 as the following three lemmas.

Lemma 4 If it is possible to construct  $I$   $r$ -flats that are orthogonal on a given  $k$ -flat in  $EG(m, p^S)$ , then it is also possible to construct  $I$   $(r+1)$ -flats that are orthogonal on a given  $(k+1)$ -flat in  $EG(m+1, p^S)$ , and vice versa.

Lemma 5 If  $r \leq \frac{m}{2}$ , it is possible to construct  $I = \frac{p^{(m-r)S} - 1}{p^S - 1}$   $r$ -flats that are orthogonal on the origin in  $EG(m, p^S)$ .

Lemma 6 If  $r > \frac{m}{2}$ , then the  $r$ -th order EG code associated with  $EG(m, p^S)$  can be orthogonalized in 3 steps.

Lemma 7 If  $r \leq \frac{m}{2} + 1$ , the  $r$ -th order PG code associated with  $PG(m, p^S)$  can be orthogonalized in 1 or 2 steps.

Proof: If  $r = 1$ , then the first order PG code is 1 step orthogonalizable. For the case  $r > 1$ , we shall show that a 0-flat can be determined from a set of at least  $J = \frac{p^{(m-r+1)s}-1}{p^s-1}$   $(r-1)$ -flats that are orthogonal on the given 0-flat.

By Lemma 5,  $J$   $(r-1)$ -flats that are orthogonal on the origin in  $EG(m, p^s)$  can be constructed. In addition, from Lemma 4, it is possible to construct  $J$   $r$ -flats that are orthogonal on a 1-flat passing through the origin in  $EG(m+1, p^s)$ . Finally, from Lemma 3, it is possible to construct  $J$   $(r-1)$ -flats that are orthogonal on a given 0-flat in  $PG(m, p^s)$ . Therefore, if  $1 < r \leq \frac{m}{2} + 1$ , the  $r$ -th order PG code can be 2-step orthogonalized.

Q.E.D.

Lemma 8 If  $r > \frac{m}{2} + 1$  the  $r$ -th order PG code associated with  $PG(m, p^s)$  can be orthogonalized in 3 steps.

Proof: Lemma 4 and Lemma 6 imply that all 1-flats passing through the origin in  $EG(m+1, p^s)$  can be orthogonalized in 3 steps from all of the  $(r+1)$ -flats in  $EG(m+1, p^s)$  with  $J = \frac{p^{(m-r+1)s}-1}{p^s-1}$ . Thus, the  $r$ -th order PG code associated with  $PG(m, p^s)$  is 3-step orthogonalizable by Lemma 3.

Q.E.D.

Directly from Lemma 7 and Lemma 8, we have our main theorem on decoding PG codes.

Theorem A PG code can be orthogonalized in less than or equal to 3 steps.

The new decoding procedure for the  $r$ -th order PG code associated with  $PG(m, p^s)$  can be described as follows. At the first step,  $(r-1)$ -flats are determined from all  $r$ -flats. If  $r = 1$ , this is the end of the decoding. At the second step, there are two cases to consider. If  $(r-1) \leq \frac{m}{2}$ , all 0-flats are determined from the  $(r-1)$ -flats. This finishes the decoding. If  $(r-1) > \frac{m}{2}$ ,

$(r-1-\lfloor \frac{m}{2} \rfloor)$ -flats are determined from the  $(r-1)$ -flats. It requires another step to finish up the decoding in this case. At the third step, all 0-flats are determined from the  $(r-1-\lfloor \frac{m}{2} \rfloor)$ -flats. The decoding procedure is depicted as follows

- a.  $r = 1$                        $1 \rightarrow 0$                       (1 step)
- b.  $1 < r \leq \frac{m}{2} + 1$        $r \rightarrow (r-1) \rightarrow 0$               (2 steps)
- c.  $r > \frac{m}{2} + 1$                $r \rightarrow (r-1) \rightarrow r-1-\lfloor \frac{m}{2} \rfloor \rightarrow 0$       (3 steps)

In conclusion, the new improved decoding algorithm successfully reduces the number of majority-logic decoding steps from  $r$  to no more than 3 for any value of  $r$ . Thus it greatly reduces the decoding complexity of PG codes in most cases. These results together with the results on EG codes in Reference 2 should make the finite geometry codes very attractive for practical use on error-control systems.

Appendix

From Mann or CarMichael, [1,11] the number  $N$  of  $r$ -flats in  $EG(m, p^s)$

is equal to

$$N = \frac{(p^{ms}-1)(p^{(m-1)s}-1)\dots(p^{(m-p+1)s}-1)p^{(m-r)s}}{(p^{rs}-1)(p^{(r-1)s}-1)\dots(p^s-1)} \quad (a)$$

Let  $A$  be the number of  $k$ -flats that are contained in an  $r$ -flat of  $EG(m, p^s)$ .

From Eq.(a)

$$A = \frac{(p^{rs}-1)(p^{(r-1)s}-1)\dots(p^{(r-k+1)s}-1)p^{(r-k)s}}{(p^{ks}-1)(p^{(k-1)s}-1)\dots(p^s-1)} \quad (b)$$

Let  $B$  be the number of  $k$ -flats in  $EG(m, p^s)$ . By Eq.(a)  $B$  is equal to

$$B = \frac{(p^{ms}-1)(p^{(m-1)s}-1)\dots(p^{(m-k+1)s}-1)p^{(m-k)s}}{(p^{ks}-1)(p^{(k-1)s}-1)\dots(p^s-1)} \quad (c)$$

Now let  $M$  be the number of  $r$ -flats that contain a given  $k$ -flat in  $EG(m, p^s)$ .

Then

$$\begin{aligned} M &= \frac{N \cdot A}{B} \\ &= \frac{(p^{(m-k)s}-1)(p^{(m-k-1)s}-1)\dots(p^{(m-r+1)s}-1)}{(p^{(r-k)s}-1)(p^{(r-k-1)s}-1)\dots(p^s-1)} \quad (d) \end{aligned}$$

If  $k = 0$ , then the number of  $(r+1)$ -flats that pass through a given point of  $EG(m+1, p^s)$  is equal to

$$\frac{(p^{(m+1)s}-1)(p^{ms}-1)\dots(p^{(m-r+1)s}-1)}{(p^{(r+1)s}-1)(p^{rs}-1)\dots(p^s-1)}$$

which is the same as the expression in Eq.(4).

References

1. CarMichael, R. C., Introduction to the Theory of Group of Finite Order, Dover, New York, 1937.
2. Chen, C. L., "On Decoding Euclidean Geometry Codes," CSL Report R-479, Coordinated Science Laboratory, University of Illinois, Urbana, Illinois, June, 1970.
3. Goethals, J. M. and P. Delsarte, "On a Class of Majority-Logic Decodable Cyclic Codes," IEEE Trans., IT-14, pp. 182-188, March, 1968.
4. Massey, J. L., Threshold Decoding, MIT Press, Mass., 1963.
5. Peterson, W. W., and E. J. Weldon, Jr., Error-Correcting Codes, Edition II, MIT Press, Cambridge, Mass., 1970.
6. Reed, I. S., "A Class of Multiple-Error-Correcting Codes and the Decoding Scheme," IRE Trans., IT-4, pp. 38-49, September, 1954.
7. Rudolph, L. D., "A Class of Majority Logic Decodable Codes," IEEE Trans., IT-13, pp. 305-307, April, 1967.
8. Weldon, E. J., Jr., "New Generalizations of the Reed-Muller Codes -- Part II: Nonprimitive Codes," IEEE Trans., IT-14, pp. 199-205, March, 1968.
9. Weldon, E. J., Jr., "Euclidean Geometry Cyclic Codes," Proceedings of Symposium of Combinatorial Mathematics at the University of North Carolina, Chapel Hill, North Carolina, 1967.
10. Weldon, E. J., Jr., "Some Results on Majority-Logic Decoding," pp. 149-162 in H. B. Mann (ed.), Error Correcting Codes, John Wiley and Sons, New York, 1968.
11. Mann, H. B., Analysis and Design of Experiments, Dover, New York, 1949.

Distribution List as of August 1, 1970

ESD (ESTI)  
L. G. Hanscom Field  
Bedford, Mass 01731 2 Copies

Mr I. A. Balton  
Institute for Exploratory Research  
Code: AMSEL-XL  
U. S. Army Electronics Command  
Fort Monmouth, New Jersey 07703

LTC Howard W. Jackson  
Deputy Dir of Electr. & Solid St. Sciences  
Air Force Office of Scientific Research  
1400 Wilson Boulevard  
Arlington, Virginia 22209 5 Copies

Defense Documentation Center  
Attn: DDC-TCA  
Cameron Station  
Alexandria, Virginia 22314 50 Copies

Director, Electronics Program  
Attn: Code 427  
Office of Naval Research  
800 North Quincy Street  
Arlington, Virginia 22217 3 Copies

Naval Air Systems Command  
AIR 03  
Washington, D. C. 20360 2 Copies

Naval Electronic Systems Command  
ELEX 03, Room 2046 Munitions Building  
Department of the Navy  
Washington, D.C. 20360 2 Copies

Director  
Naval Research Laboratory  
Washington, D. C. 20390  
Attn: Code 2027 6 Copies

Commander  
U. S. Naval Ordnance Laboratory  
Attn: Librarian  
White Oak, Md. 20910 2 Copies

Commanding General  
Attn: STEWS-RE-L, Technical Library  
White Sands Missile Range  
New Mexico 88002 2 Copies

Commander  
Naval Electronics Laboratory Center  
Attn: Library  
San Diego, Calif 92152 2 Copies

Dr L. M. Hollingsworth  
AFCLR (CRN)  
L. G. Hanscom Field  
Bedford, Massachusetts 01731

Division of Engineering & Applied Physics  
210 Pierce Hall  
Harvard University  
Cambridge, Massachusetts, 02138

Director  
Research Laboratory of Electronics  
Massachusetts Institute of Technology  
Cambridge, Massachusetts 02139

Miss R. Joyce Harman  
Project MAC, Room 810  
545 Technology Square  
Cambridge, Mass 02139

Professor R. H. Rediker  
Electrical Engineering Prof.  
Mass. Institute of Technology  
Building 13-3050  
Cambridge, Mass 02139

Raytheon Company  
Research Division Library  
28 Seyon Street  
Waltham, Massachusetts 02154

Sylvania Electronic Systems  
Applied Research Laboratory  
Attn: Documents Librarian  
40 Sylvan Road  
Waltham, Mass 02154

Commanding Officer  
Army Materials & Mechanics Res. Center  
Attn: Dr H. Priest  
Watertown Arsenal  
Watertown, Massachusetts 02172

MIT Lincoln Laboratory  
Attn: Library A-082  
P. O. Box 73  
Lexington, Mass. 02173

Commanding Officer  
Office of Naval Research Branch Office  
495 Summer Street  
Boston, Massachusetts 02210

Commanding Officer (Code 2064)  
U. S. Naval Underwater Sound Laboratory  
Fort Trumbull  
New London, Connecticut: 06320

Dept of Eng & Applied Science  
Yale University  
New Haven, Conn 06520

Commanding General  
U. S. Army Electronics Command  
Attn: AMSEL-CT-A  
Fort Monmouth, New Jersey 07703

Commanding General  
U. S. Army Electronics Command  
Attn: AMSEL-CT-D  
Fort Monmouth, New Jersey 07703

Commanding General  
U. S. Army Electronics Command  
Attn: AMSEL-CT-I  
Fort Monmouth, New Jersey 07703

Commanding General  
U. S. Army Electronics Command  
Attn: AMSEL-CT-L (Dr W. S. McAfee)  
Fort Monmouth, New Jersey 07703

Commanding General  
U. S. Army Electronics Command  
Attn: AMSEL-CT-O  
Fort Monmouth, New Jersey 07703

Commanding General  
U. S. Army Electronics Command  
Attn: AMSEL-CT-R  
Fort Monmouth, New Jersey 07703

Commanding General  
U. S. Army Electronics Command  
Fort Monmouth, New Jersey 07703  
Attn: AMSEL-CT-S

Commanding General  
U. S. Army Electronics Command  
Attn: AMSEL-DL  
Fort Monmouth, New Jersey 07703

Commanding General  
U. S. Army Electronics Command  
Attn: AMSEL-GG-DD  
Fort Monmouth, New Jersey 07703

Commanding General  
U. S. Army Electronics Command  
Attn: AMSEL-KL-D  
Fort Monmouth, New Jersey 07703

Commanding General  
U. S. Army Electronics Command  
Attn: AMSEL-KL-E  
Fort Monmouth, New Jersey 07703

Commanding General  
U. S. Army Electronics Command  
Attn: AMSEL-KL-I  
Fort Monmouth, New Jersey 07703

Commanding General  
U. S. Army Electronics Command  
Attn: AMSEL-KL-SM  
Fort Monmouth, New Jersey 07703

Commanding General  
U. S. Army Electronics Command  
Attn: AMSEL-KL-S  
Fort Monmouth, New Jersey 07703

Commanding General  
U. S. Army Electronics Command  
Attn: AMSEL-KL-T  
Fort Monmouth, New Jersey 07703

Commanding General  
U. S. Army Electronics Command  
Attn: AMSEL-NL-A  
Fort Monmouth, New Jersey 07703

Commanding General  
U. S. Army Electronics Command  
Attn: AMSEL-NL-C  
Fort Monmouth, New Jersey 07703

Commanding General  
U. S. Army Electronics Command  
Attn: AMSEL-NL-D (Dr H. Bennett)  
Fort Monmouth, New Jersey 07703

Commanding General  
U. S. Army Electronics Command  
Attn: AMSEL-NL-P  
Fort Monmouth, New Jersey 07703

Commanding General  
U. S. Army Electronics Command  
Attn: AMSEL-SC  
Fort Monmouth, New Jersey 07703

Commanding General  
U. S. Army Electronics Command  
Attn: AMSEL-VL-D  
Fort Monmouth, New Jersey 07703

Commanding General  
U. S. Army Electronics Command  
Attn: AMSEL-VL-F  
Fort Monmouth, New Jersey 07703

Commanding General  
U. S. Army Electronics Command  
Attn: AMSEL-WL-D  
Fort Monmouth, New Jersey 07703

Commanding General  
U. S. Army Electronics Command  
Attn: AMSEL-XL-DT  
Fort Monmouth, New Jersey 07703

Commanding General  
U. S. Army Electronics Command  
Attn: AMSEL-XL-D  
Fort Monmouth, New Jersey 07703

Mr Norman J. Field, AMSEL-RD-S  
Chief, Office of Science & Technology  
Research and Development Directorate  
U. S. Army Electronics Command  
Fort Monmouth, New Jersey 07703

Project Manager  
Common Positioning & Navigation Systems  
Attn: Harold H. Bahr (AMCPM-NS-TM),  
Bldg. 439  
U. S. Army Electronics Command  
Fort Monmouth, New Jersey 07703

U. S. Army Munitions Command  
Attn: Science & Technology  
Info Br., Bldg 59  
Picatinny Arsenal, SMUPA-RT-S  
Dover, New Jersey 07801

European Office of Aerospace Research  
Technical Information Office  
Box 14, FPO New York 09510

Director  
Columbia Radiation Laboratory  
Columbia University  
538 West 120th St.  
New York, N.Y. 10027

New York University  
Engineering Library  
Bronx, New York 10453

Mr Jerome Fox, Research Coordinator  
Polytechnic Institute of Brooklyn  
333 Jay St.  
Brooklyn, N. Y. 11201

Airborne Instruments Laboratory  
Deerpark, New York 11729

Dr. W. R. Lepage, Chairman  
Syracuse University  
Dept of Electrical Engineering  
Syracuse, N. Y. 13210

Rome Air Development Center  
Attn: Documents Library (EMTLD)  
Griffiss Air Force Base, N. Y. 13440

Mr H. E. Webb (EMBIS)  
Rome Air Development Center  
Griffiss Air Force Base, N.Y. 13440

Professor James A. Cadzow  
Department of Electrical Engineering  
State University of New York at Buffalo  
Buffalo, N. Y. 14214

Dr. A. G. Jordan  
Head of Dept of Elec Engineering  
Carnegie-Mellon University  
Pittsburgh, Penn 15213

Hunt Library  
Carnegie-Mellon University  
Schenley Park  
Pittsburgh, PA. 15213

Lehigh University  
Dept of Electrical Engineering  
Bethlehem, Pennsylvania 18015

Commander (ADL)  
Naval Air Development Center  
Attn: NADC Library  
Johnsville, Warminster, Pa 18974

Technical Director (SMUFA-A2000-107-1)  
Frankford Arsenal  
Philadelphia, Pennsylvania 19137

Mr M. Zane Thornton, Chief, Network  
Engineering, Communications and  
Operations Branch, Lister Hill  
National Center/ Biomedical Communications  
8600 Rockville Pike  
Bethesda, Maryland 20014

U. S. Post Office Dept  
Library- Room 6012  
12th & Pennsylvania Ave. N.W.  
Washington, D.C. 20260

Technical Library  
DDR&E  
Room 3C-122, The Pentagon  
Washington, D.C. 20301



Distribution List (Cont'd.)

Director for Materials Sciences  
Advanced Research Projects Agency  
Department of Defense  
Washington, D.C. 20301

Assistant Director, (Research)  
Office of Director of Defense Research  
& Engineering  
Pentagon, Rm 3C128  
Washington, D.C. 20301

Chief, R & D Division (340)  
Defense Communications Agency  
Washington, D.C. 20305

Commanding General  
U. S. Army Materiel Command  
Attn: AMCRD-TP  
Washington, D.C. 20315

Director, U. S. Army Materiel  
Concepts Agency  
Washington, D.C. 20315

Hq USAF (AFRDD)  
The Pentagon  
Washington, D.C. 20330

Hq USAF (AFRDDG)  
The Pentagon  
Washington, D.C. 02330

Hq USAF (AFRDS)  
The Pentagon  
Washington, D.C. 20330

AFSC (SCTSE)  
Andrews Air Force Base, Maryland 20331

Dr I. R. Mirman  
Hq AFSC (SGGP)  
Andrews AFB, Maryland 20331

Naval Ship Systems Command  
Ship 031  
Washington, D.C. 20360

Naval Ship Systems Command  
Ship 035  
Washington, D.C. 20360

Commander  
U. S. Naval Security Group Command  
Attn: G43  
3801 Nebraska Avenue  
Washington, D.C. 20390

U. S. Naval Oceanographic Office  
Attn: M. Rogofsky, Librarian (Code 640)  
Washington, D.C. 20390

Director  
Naval Research Laboratory  
Washington D.C. 20390  
Attn: Dr A. Brodzinsky, Sup. Elec Div

Director  
Naval Research Laboratory  
Washington, D.C. 20390  
Attn: Maury Center Library (Code 8050)

Director  
Naval Research Laboratory  
Washington, D.C. 20390  
Attn: Dr W. C. Hall, Code 7000

Director  
Naval Research Laboratory  
Attn: Library, Code 2029 (ONRL)  
Washington, D.C. 20390

Dr G.M.R. Winkler  
Director, Time Service Division  
U. S. Naval Observatory  
Washington, D.C. 20390

Colonel E. P. Gaines, Jr  
ACDA/FO  
1901 Pennsylvania Ave. N. W.  
Washington, D.C. 20451

Commanding Officer  
Harry Diamond Laboratories  
Attn: Mr Berthold Altman (AMXD)-TI)  
Connecticut Ave & Van Ness St., N.W.  
Washington, D.C. 20438

Central Intelligence Agency  
Attn: CRS/ADD Publications  
Washington, D.C. 20505

Dr H. Harrison, Code RRE  
Chief, Electrophysics Branch  
National Aeronautics & Space Admin.  
Washington, D.C. 20546

The John Hopkins University  
Applied Physics Laboratory  
Attn: Document Librarian  
8621 Georgia Avenue  
Silver Spring, Maryland 20910

Commanding Officer (AMXRD-BAT)  
U. S. Army Ballistics Research Laboratory  
Aberdeen Proving Ground  
Aberdeen, Maryland 21005

Technical Director  
U. S. Army Land Warfare Laboratory  
Aberdeen Proving Ground  
Aberdeen, Maryland 21005

Electromagnetic Compatibility  
Analysis Center (ECAC)  
Attn: (ACOAT)  
North Severn  
Annapolis, Maryland 21402

Commanding Officer  
U. S. Army Engineer Topographic Labs  
Attn: STINFLO Center  
Fort Belvoir, Virginia 22060

Director (NV-D)  
Night Vision Laboratory, USAECOM  
Fort Belvoir, Virginia 22060

U. S. Army Mobility Equipment Research  
and Development Center  
Attn: Technical Document Center  
Bldg 315  
Fort Belvoir, Virginia 22060

Dr Alvin D. Schnitzler  
Institute for Defense Analyses  
Science and Technology Division  
400 Army-Navy Drive  
Arlington, Virginia 22202

Director, Physical & Eng. Sciences Div.  
3045 Columbia Pike  
Arlington, Virginia 22204

Commanding General  
U. S. Army Security Agency  
Attn: IARD-T  
Arlington Hall Station  
Arlington, Virginia 22212

Dr Joel Trimble, Code 437  
Information Systems Branch  
Office of Naval Research  
800 North Quincy Street  
Arlington, Virginia 22217

Commanding General  
USACDC Institute of Land Combat  
Attn: Technical Library, rm 636  
2461 Eisenhower Avenue  
Alexandria, Virginia 22314

VELA Seismological Center  
300 North Washington St.  
Alexandria, Virginia 22314  
U. S. Naval Weapons Laboratory  
Dahlgren, Virginia 22448

Research Laboratories for the Eng.  
Sciences, School of Engineering &  
Applied Science  
University of Virginia  
Charlottesville, Va. 22903

Dr Herman Robl  
Deputy Chief Scientist  
U. S. Army Research Office (Durham)  
Box CM, Duke Station  
Durham, North Carolina 27706

Richard O. Ullsh (CRDARD-IP)  
U. S. Army Research Office (Durham)  
Box CM, Duke Station  
Durham, North Carolina 27706

ADTC (ADBEPS-12)  
Eglin AFB, Florida 32542

Commanding Officer  
Naval Training Device Center  
Orlando, Florida 32813

Technical Library, AFETR  
(ETV, MU-135)  
Patrick AFB, Florida 32925

Commanding General  
U. S. Army Missile Command  
Attn: AMSMI-RR  
Redstone Arsenal, Alabama 35809

Redstone Scientific Information Center  
Attn: Chief, Document Section  
U. S. Army Missile Command  
Redstone Arsenal, Alabama 35809

AUL3T-9663  
Maxwell AFB, Alabama 36112

Hq AEDC (AETS)  
Attn: Library/Documents  
Arnold AFS, Tennessee 37389

Case Institute of Technology  
Engineering Division  
University Circle  
Cleveland, Ohio 44106

NASA Lewis Research Center  
Attn: Library  
21000 Brookpark Road  
Cleveland, Ohio 44135

Director  
Air Force Avionics Laboratory  
Wright-Patterson AFB, Ohio 45433  
AFAL (AVTA) R. D. Larson  
Wright-Patterson AFB, Ohio 45433

AFAL (AVT) Dr H. V. Noble, Chief  
Electronics Technology Division  
Air Force Avionics Laboratory  
Wright-Patterson AFB, Ohio 45433

Dr Robert E. Fontana  
Head, Dept of Electrical Engineering  
Air Force Institute of Technology  
Wright Patterson AFB, Ohio 45433

Dept of Electrical Engineering  
Clippinger Laboratory  
Ohio University  
Athens, Ohio 45701

Commanding Officer  
Naval Avionics Facility  
Indianapolis, Indiana 46241

Dr John C. Hancock, Head  
School of Electrical Engineering  
Purdue University  
Lafayette, Ind 47907

Professor Joseph E. Rowe  
Chairman, Dept of Electrical  
Engineering  
The University of Michigan  
Ann Arbor, Michigan 48104

Dr G. J. Murphy  
The Technological Institute  
Northwestern University  
Evanston, Ill 60201

Commanding Officer  
Office of Naval Research Branch Office  
219 South Dearborn St.  
Chicago, Illinois 60604

Illinois Institute of Technology  
Dept of Electrical Engineering  
Chicago, Illinois 60616

Deputy for Res. and Eng (AMSE-DRE)  
U. S. Army Weapons Command  
Rock Island Arsenal  
Rock Island, Illinois 61201

Commandant  
U. S. Army Command & General Staff  
College  
Attn: Acquisitions, Library Division  
Fort Leavenworth, Kansas 66027

Dept of Electrical Engineering  
Rice University  
Houston, Texas 77001

HQ AMD (AMR)  
Brooks AFB, Texas 78235

USAFSAM (SMKOR)  
Brooks AFB, Texas 78235

Mr E. R. Locke  
Technical Adviser, Requirements  
USAF Security Service  
Kelly Air Force Base, Texas 78241

Director Electronics Research Center  
The University of Texas at Austin  
Eng-Science Bldg 110  
Austin, Texas 78712

Department of Electrical Engineering  
Texas Technological University  
Lubbock, Texas 79409

Commandant  
U. S. Army Air Defense School  
Attn: Missile Sciences Div., C&S Dept  
P. O. Box 9390  
Fort Bliss, Texas 79916

Director  
Aerospace Mechanics Sciences  
Frank J. Seiler Research Laboratory (OAR)  
USAF Academy  
Colorado Springs, Colorado 80840

Director of Faculty Research  
Department of the Air Force  
U. S. Air Force Academy  
Colorado Springs, Colorado 80840

Major Richard J. Gowen  
Tenure Associate Professor  
Dept of Electrical Engineering  
U. S. Air Force Academy  
Colorado Springs, Colorado 80840

Distribution List (Cont'd.)

Academy Library (DFSLEB)  
U. S. Air Force Academy  
Colorado Springs, Colorado 80840

M. A. Rothenberg (STEPD-SC(S))  
Scientific Director  
Desert Test Center  
Bldg. 100, Soldiers Circle  
Fort Douglas, Utah 84113

Utah State University  
Dept of Electrical Engineering  
Logan, Utah 84321

School of Engineering Sciences  
Arizona State University  
Tempe, Ariz 85281

Commanding General  
U. S. Army Strategic Communications  
Command  
Attn: SCC-CG-SAE  
Fort Huachuca, Arizona 85613

The University of Arizona  
Dept of Electrical Engineering  
Tucson, Arizona 85721

Cpt C. E. Baum  
AFWL (WLEE)  
Kirkland AFB, New Mexico 78117

Los Alamos Scientific Laboratory  
Attn: Report Library  
P. O. Box 1663  
Los Alamos, N.M 87544

Commanding Officer  
Atmospheric Sciences Laboratory  
White Sands Missile Range, N. Mex 88002

Commanding Officer  
(AMSEL-BL-WS-R)  
Atmospheric Sciences Laboratory  
White Sands Missile Range  
New Mexico 88002

Chief, Missile Electronic Warfare  
Tech Area  
(AMSEL-WL-M)  
Electronic Warfare Laboratory, USACOM  
White Sands Missile Range, N.M. 88002

Director  
Electronics Sciences Lab  
University of Southern California  
Los Angeles, Calif 90007

Engineering & Mathematical Sciences Library  
University of California at Los Angeles  
405 Hilgred Avenue  
Los Angeles, Calif. 90024

Aerospace Corporation  
P. O. Box 95085  
Los Angeles, California 90045  
Attn: Library Acquisitions Group

Hq SAMSO (SMITA/Lt Belate)  
AF Unit Post Office  
Los Angeles, Calif. 90045

Dr Sheldon J. Wells  
Electronic Properties Information Center  
Mail Station E-175  
Hughes Aircraft Company  
Culver City, California 90230

Director, USAF PROJECT RAND  
Via: Air Force Liaison Office  
The RAND Corporation  
Attn: Library D  
1700 Main Street  
Santa Monica, California 90406

Deputy Director and Chief Scientist  
Office of Naval Research Branch Office  
1030 East Green Street  
Pasadena, California 91101

Aeronautics Library  
Graduate Aeronautical Laboratories  
California Institute of Technology  
1201 E. California Blvd.  
Pasadena, California 91109

Professor Nicholas George  
California Inst. of Technology  
Pasadena, California 91109

Commanding Officer  
Naval Weapons Center  
Corona Laboratories  
Attn: Library  
Corona, California 91720

Dr F. R. Charvat  
Union Carbide Corporation  
Materials Systems Div.  
Crystal Products Dept.  
8888 Balboa Avenue  
P.O. Box 23017  
San Diego, Calif 92123

Hollander Associates  
P. O. Box 2276  
Fullerton, California 92633

Commander  
U. S. Naval Missile Center (56322)  
Point Mugu, California 93041

W. A. Eberspacher, Associate Head  
Systems Integration Division  
Code 5340A, Box 15  
U. S. Naval Missile Center  
Point Mugu, California 93041

Sciences-Engineering Library  
University of California  
Santa Barbara, California 93106

Commander (Code 753)  
Naval Weapons Center  
Attn: Technical Library  
China Lake, California 93555

Library (Code 2124)  
Technical Report Section  
Naval Postgraduate School  
Monterey, California 93940

Glen A. Myers (Code 52Mv)  
Assoc. Professor of Elec. Engineering  
Naval Postgraduate School  
Monterey, California 93940

Dr Leo Young  
Stanford Research Institute  
Menlo Park, Calif. 94025

Lenkurt Electric Co., Inc.  
1105 County Road  
San Carlos, California 94070  
Attn: Mr E. K. Peterson

Director  
Microwave Laboratory  
Stanford University  
Stanford, California 94305

Director  
Stanford Electronics Laboratory  
Stanford University  
Stanford, California 94305

Director, Electronics Research Laboratory  
University of California  
Berkeley, California 94720

DELETE:

Mr. Norman J. Field, AMSEL-RD-S  
Chief, Office of Science & Technology  
Research and Development Directorate  
U.S. Army Electronics Command  
Fort Monmouth, New Jersey 07703

REPLACE WITH:

Mr Norman J. Field, AMCPM-AA-PM  
Chief, Program Management Division  
Project AACOMS, USAECOM, Bldg 2525  
Fort Monmouth, New Jersey 07703

## DOCUMENT CONTROL DATA - R &amp; D

(Security classification of title, body of abstract and indexing annotation must be entered when the overall report is classified)

1. ORIGINATING ACTIVITY (Corporate author) University of Illinois Coordinated Science Laboratory Urbana, Illinois 61801		2a. REPORT SECURITY CLASSIFICATION	
		2b. GROUP	
3. REPORT TITLE  ON DECODING PROJECTIVE GEOMETRY CODES			
4. DESCRIPTIVE NOTES (Type of report and inclusive dates)			
5. AUTHOR(S) (First name, middle initial, last name)  C. L. Chen			
6. REPORT DATE September, 1970	7a. TOTAL NO. OF PAGES 11	7b. NO. OF REFS 11	
8a. CONTRACT OR GRANT NO. DAAB 07-67-C-0199; also in part Rome Air Development Center Contract F30602-70-C-0014	9a. ORIGINATOR'S REPORT NUMBER(S)  R-486		
c. d.	9b. OTHER REPORT NO(S) (Any other numbers that may be assigned this report)  UILU-ENG 70-231		
10. DISTRIBUTION STATEMENT  This Document has been approved for public release and sale; its distribution is unlimited.			
11. SUPPLEMENTARY NOTES		12. SPONSORING MILITARY ACTIVITY Joint Services Electronics Program Thru U. S. Army Electronics Command Fort Monmouth, New Jersey 07703	
13. ABSTRACT  In this report, it is shown that Projective Geometry codes are orthogonalizable in less than or equal to 3 steps. Thus, this class of codes is majority-logic decodable in no more than 3 steps.  The improved decoding algorithm reduces the decoding complexity of Projective Geometry codes enormously in most cases. This should make Projective Geometry codes very attractive for practical use on error-control systems.			

14. KEY WORDS	LINK A		LINK B		LINK C	
	ROLE	WT	ROLE	WT	ROLE	WT
Projective Geometry Codes Majority-Logic Decoding Euclidean Geometry Codes						