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HPC²—A fully-portable, algebra-based framework for heterogeneous computing. Application to CFD

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ABSTRACT

The variety of computing architectures competing in the exascale race makes the portability of codes of major importance. In this work, the HPC² code is presented as a fully-portable, algebra-based framework suitable for heterogeneous computing. In its application to CFD, the algorithm of the time-integration phase relies on a reduced set of only three algebraic operations: the sparse matrix-vector product, the linear combination of vectors and the dot product. This algebraic approach combined with a multilevel MPI+OpenMP+OpenCL parallelization naturally provides portability. The performance has been studied on different architectures including multicore CPUs, Intel Xeon Phi accelerators and GPUs of AMD and NVIDIA. The multi-GPU scalability is demonstrated up to 256 devices. The heterogeneous execution is tested on a CPU+GPU hybrid cluster. Finally, results of the direct numerical simulation of a turbulent flow in a 3D air-filled differentially heated cavity are presented to show the capabilities of the HPC² dealing with large-scale CFD simulations.

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1. Introduction

2 Massively-parallel devices of various architectures are being
3 adopted by the newest supercomputers in order to overcome the
4 actual power constraint in the context of the exascale challenge
5 [1]. This trend is being reflected in most of the fields that rely
6 on large-scale simulations such as computational fluid dynam-
7 ics (CFD). Examples of CFD applications using accelerators can
8 be found, for instance, in [2] (single-GPU, portable, OpenCL), [3–
9 5] (multi-GPU, vendor-locked, CUDA) and [6] (petascale, multi-GPU,
10 portable, CUDA+OpenCL).

11 Although the majority of problems in the field of mathematical
12 physics involve sparse matrix and vector operations and hence al-
13 gorithms with very low arithmetic intensity, most of the emerging
14 HPC architectures are FLOP-oriented, i.e. FLOPS to memory band-
15 width ratio is very high. Consequently, the achievable performance
16 is usually reduced to a small fraction of the peak performance as
17 proven by the HPCG Benchmark [7] results.

18 Therefore, in the design of large-scale simulation tools, soft-
19 ware portability and efficiency are of crucial importance. The com-
20 puting operations that form the algorithm, so-called kernels, must

21 be compatible with distributed- and shared-memory MIMD par-
22 allelism and, more importantly, with stream processing, which is
23 a more restrictive parallel paradigm. Consequently, the fewer the
24 kernels of an application, the easier it is to provide portability. Fur-
25 thermore, if the majority of kernels represent linear algebra op-
26 erations, then standard optimized libraries (e.g. ATLAS, cBLAST)
27 or specialized in-house implementations can be used and easily
28 switched.

29 In this context, we proposed in a previous work [8] a portable
30 algebraic implementation approach for direct numerical simula-
31 tions (DNS) and large eddy simulation (LES) of incompressible tur-
32 bulent flows on unstructured meshes. Roughly, the implementation
33 consists in replacing classical stencil data structures and sweeps by
34 algebraic data structures and kernels. As a result, the algorithm re-
35 lies on a reduced set of only three algebraic operations: the sparse
36 matrix-vector product (*SpMV*), the linear combination of vectors
37 (*axpy*) and the dot product (*dot*).

38 On the other hand, the hybridization of HPC systems makes the
39 design of simulation codes a rather complex problem. Heteroge-
40 neous implementations such as an MPI+OpenMP+OpenCL paral-
41 lelization [9] can target a wide range of architectures and combine
42 different kinds of parallelism. Hence, they are becoming increas-
43 ingly necessary in order to engage all available computing power
44 and memory throughput of CPUs and accelerators. Examples of
45 CFD codes capable of heterogeneous computing can be found, for

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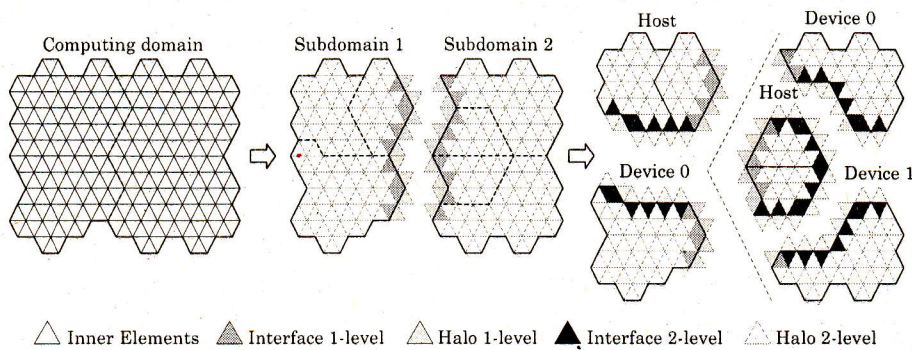


Fig. 1. Multi-level decomposition example for a cell-centred scheme among two dual-CPU nodes with one and two acceleration devices, respectively.

46 instance, in [10] the performance of the PyFR framework is tested
 47 on a hybrid node with a multicore CPU, NVIDIA and AMD GPUs.
 48 Further, in [11] the scalability of the HOSTA code is tested on up
 49 to 1024 TianHe-1A hybrid nodes.

50 Following the spirit of Oyarzun et al. [8], and increasing the
 51 level of abstraction, we present in this paper the HPC² (Hetero-
 52 geneous Portable Code for HPC). It is a fully-portable, algebra-
 53 based framework capable of heterogeneous computing with many
 54 potential applications in the fields of computational physics and
 55 mathematics. This framework aims to provide a user-friendly en-
 56 vironment suitable for writing numerical algorithms in terms of
 57 portable linear algebra kernels.

58 2. Multi-level domain decomposition

59 The computational domain is essentially a graph, i.e. a set of
 60 objects in which some pairs are in some sense related (such as
 61 mesh nodes, cells, faces, vertices, etc.), which may be subject to
 62 calculations. It typically arises from the spatial discretization of the
 63 physical domain, forming a fundamental part of many applications.
 64 The optimal distribution of the workload of the computational do-
 65 main across the HPC system is of great importance in heteroge-
 66 neous computing for attaining maximum performance.

67 By way of example, let us consider a generic numerical algo-
 68 rithm which operates on a computational domain. The algorithm
 69 is to be executed on an HPC system that consists of computing
 70 nodes interconnected via a communication infrastructure. Hence,
 71 a traditional first-level domain decomposition approach with MPI
 72 parallelization is used in order to distribute the workload among
 73 multiple nodes. In doing so, domain elements are assigned to sub-
 74 domains using a partitioning library (e.g. ParMETIS [12]) that fulfils
 75 the requested load balancing and minimizes the number of cou-
 76 plings between cells of different subdomains. As a result, first-level
 77 subdomain elements are classified into *Inner* and *Interface* cate-
 78 gories (see Fig. 1). Namely, *Interface* elements are those coupled
 79 with elements from other subdomains. Consequently, those other's
 80 neighbouring elements form a *Halo*. A communication between
 81 parallel processes is needed in order to update the data in *Halo*
 82 elements needed by a kernel when processing *Interface* elements.
 83 The *Halo* update is represented with non-blocking point-to-point
 84 MPI communications between neighbouring processes. An overlap
 85 of these communications with computations partially eliminates
 86 the data transfer overhead. In such a case, the *Halo* update is car-
 87 ried out simultaneously to the execution of the kernel only for the
 88 *Inner* elements. Afterwards, once the *Halo* is updated, the *Inter-*
 89 *face* elements are computed.

90 Similarly, first-level subdomains are decomposed further in or-
 91 der to distribute the workload of each node among its computing
 92 devices, such as multiple CPUs (called host) and co-processors of
 93 different kinds (called devices), as shown in the right part of Fig. 1.

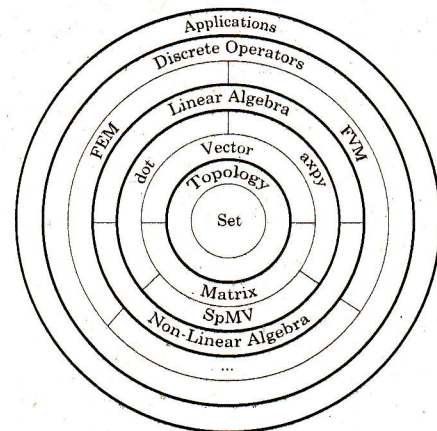


Fig. 2. Representation of the HPC² code structure.

This second-level decomposition must conform to the actual per- 94
 95 formance of devices for the sake of load balancing. As a result, at
 96 the second level, the *Halo* and *Interface* elements are classified as
 97 (1) external ones that need MPI communications because are cou-
 98 pled with other subdomains of the first-level decomposition, and
 99 (2) internal ones that only participate in the intra-node exchanges.
 100 The external *Interface* and *Halo* elements which are assigned to a
 101 device with a separate memory space need more expensive multi-
 102 stage device-host-MPI-host-device communications. The volume of
 103 this expensive *Interface* is reduced several times with the two-level
 104 partitioning since in the one-level case the internal *Interface* would
 105 become external.

106 Finally, the third-level decomposition among NUMA nodes of
 107 the host (e.g. CPU sockets or parts of a multicore CPU grouped
 108 in a shared L3 inner cache ring) allows allocating data in accord-
 109 ance with the physical resources to which a group of threads is
 110 assigned.

111 3. The HPC² framework

112 In this section, we present the HPC²: a fully-portable, algebra-
 113 based framework suitable for heterogeneous computing with the
 114 aim of providing a user-friendly environment for writing algo-
 115 rithms in the fields of computational physics and mathematics.

116 3.1. Structure of HPC²

117 The code is structured following a multi-layer design repre-
 118 sented in Fig. 2 as concentric rings. In this scheme, the layers

119 are defined to be detached maintaining the object dependency re-
 120 stricted to the inner layers. Therefore, each outer layer represents
 121 a higher level of abstraction.

122 The first layer (centre) is composed of the *Set* and *Topology* ob-
 123 jects. This layer represents the computational domain (detailed in
 124 Section 2) hence it is the core of any numerical simulation. Firstly,
 125 the *Set* is a basic data structure which aims to mimic the algebraic
 126 concept of a set *i.e.* a collection of objects of some kind. It is de-
 127 signed to be automatically distributed in the system and assigned
 128 to devices at runtime according to the execution parameters. Thus,
 129 it becomes generic and architecture-independent from the outer
 130 layers' point of view. Secondly, the *Topology* is designed to be in
 131 agreement with a *Set*. It consists of the representation of the cou-
 132 plings between the objects of the *Set*. Therefore, it contains the re-
 133 quired information to perform the data exchanges (*Halo* updates).
 134 Note that the *Topology* is bounded to a *Set*, then it can only man-
 135 age the data exchanges of objects belonging to that *Set*. However,
 136 different *Topology* may be assigned to the same *Set* depending on
 137 the numerical schemes (*e.g.* the second- and fourth-order schemes
 138 define different couplings between the same set of unknowns).

139 In the second layer, two more complex algebraic objects, the
 140 *Vector* and the *Matrix*, are derived from *Set*. The *Vector* object re-
 141 presents discrete functions on the computational domain (*e.g.* pres-
 142 sure, velocity, temperature). To comply with the algebraic concept
 143 of vector it must be equipped with the algebraic operations: dot
 144 product, scalar multiplication, vector addition, and linear combina-
 145 tion. These operations are contained in the *dot* and *axy* kernels.
 146 The *Matrix* object is provided with the matrix multiplication, rep-
 147 resented by the *SpMV* kernel, in order to perform linear transfor-
 148 mations.

149 The third layer contains linear and non-linear algorithms. These
 150 algorithms are written using only *Matrix* and *Vector* kernels, and
 151 hence they only maintain an inner-layer dependency. Some exam-
 152 ples of algorithms already implemented are the Conjugate Gradi-
 153 ent solver, the Adams-Bashforth time integrator and the Courant-
 154 Friedrichs-Lewy (CFL) condition.

155 The fourth layer consists of the preprocessing mechanisms
 156 which can be defined for different numerical methods. Namely,
 157 given a mesh, the preprocessing constructs the *Set* and *Topol-*
 158 *ogy* objects. Then, it generates the coefficients of the operators
 159 such as Gradient, Divergence or Laplacian. Additionally, this layer
 160 can involve external simulation codes which generate the opera-
 161 tors as an input for the HPC² time integration core.

162 Finally, the outermost layer is left for applications. The prepro-
 163 cessing mechanisms are used to generate the required *Matrix* and
 164 *Vector* objects. The combination of these objects, and its kernels,
 165 together with the algorithms described in the third layer allows
 166 the implementation of complex algorithms in the fields of compu-
 167 tational physics and mathematics. By way of example, the reader
 168 is referred to the Algorithm 1 (in Section 4) for modelling DNS of
 169 incompressible turbulent flows with heat transfer is composed of
 170 only three linear algebra kernels: *SpMV*, *dot*, *axy*.

171 3.2. Software implementation details

172 The structure of HPC² aims to restrict the implementation
 173 specifics to the inner layers, *i.e.* the core and algebraic layers. Our
 174 heterogeneous implementation relies on MPI, OpenMP and OpenCL
 175 frameworks. Firstly, the non-blocking MPI point-to-point commu-
 176 nications are used for distributed-memory parallelization. Sec-
 177 ondly, OpenMP is used for shared-memory MIMD parallelization
 178 for multicore CPUs and manycore accelerators. The dynamic loop
 179 scheduling is mostly used in order to avoid imbalance between
 180 threads that may appear due to the interference with OpenCL
 181 and MPI processes. Additionally, vectorization for SIMD extensions
 182 on the lowest level is achieved with compiler-specific directives,

Algorithm 1 Time-integration step.

1. Compute the convective, the diffusive and the source term of momentum Eq. (1):

$$R(\mathbf{u}_s^n, \mathbf{u}_c^n, \theta_c^n) \equiv -C_c^{3d}(\mathbf{u}_s^n)\mathbf{u}_c^n - D_c^{3d}\mathbf{u}_c^n + \mathbf{f}_c(\theta_c^n)$$
2. Compute the predictor velocity: $\mathbf{u}_c^p = \mathbf{u}_c^n + \Delta t \left\{ \frac{3}{2}R(\mathbf{u}_s^n, \mathbf{u}_c^n) - \frac{1}{2}R(\mathbf{u}_s^{n-1}, \mathbf{u}_c^{n-1}) \right\}$
3. Solve the Poisson equation given in Eq. (4): $L\tilde{\mathbf{p}}_c^{n+1} = M\mathbf{u}_c^p$ where $\mathbf{u}_s^p = \Gamma_{c \rightarrow s}\mathbf{u}_c^p$
4. Correct the staggered velocity field: $\mathbf{u}_s^{n+1} = \mathbf{u}_s^p - G\tilde{\mathbf{p}}_c^{n+1}$ where $G = -\Omega_s^{-1}M^T$
5. Correct the cell centered velocity field: $\mathbf{u}_c^{n+1} = \mathbf{u}_c^p - G_c\tilde{\mathbf{p}}_c^{n+1}$ where $G_c \doteq -\Gamma_{s \rightarrow c}\Omega_s^{-1}M^T$
6. Compute the convective and the diffusive terms intertemperature transport Eq. (2):

$$R_\theta(\mathbf{u}_s^n, \theta_c^n) \equiv -C_c(\mathbf{u}_s^n)\theta_c^n - Pr^{-1}D_c\theta_c^n$$
7. Compute temperature at the next time-step:

$$\theta_c^{n+1} = \theta_c^n + \Delta t \left\{ \frac{3}{2}R_\theta(\mathbf{u}_s^n, \theta_c^n) - \frac{1}{2}R_\theta(\mathbf{u}_s^{n-1}, \theta_c^{n-1}) \right\}$$

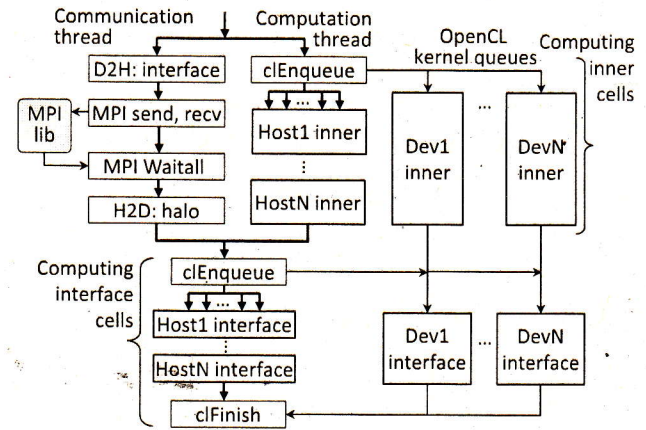


Fig. 3. Heterogeneous execution of a kernel over a multi-level decomposed domain.

183 such as `#pragma ivdepfor` for the Intel compiler. Finally, OpenCL im-
 184 plementation provides the kernels portability across various stream
 185 processing-based accelerators, including AMD and NVIDIA GPUs,
 186 FPGA accelerators and ARM-based systems-on-a-chip (SoCs).

187 The heterogeneous execution mode is implemented using
 188 nested OpenMP regions (Fig. 3). The outer parallel region spawns
 189 two threads: one for handling communications and another for
 190 computations. The communication thread executes device-to-host
 191 (D2H), MPI, and host-to-device (H2D) transfers. The computing
 192 thread submits kernels for the background OpenCL execution, then
 193 OpenMP-parallel processing is carried out within the inner paral-
 194 lel region. In doing so, the OpenMP and OpenCL computations are
 195 carried out simultaneously with the data exchanges engaging all
 196 available computing resources and hiding the communication over-
 197 head.

198 In order to optimize the matrix data structures and kernels, it is
 199 necessary to reorder the rows and provide it with a proper storage
 200 format depending on its sparsity pattern and the target architec-
 201 ture. The reordering aims to improve the data locality by reduc-
 202 ing the matrix bandwidth [13]. The implemented storage formats
 203 include the standard Compressed Sparse Row (CSR) and different
 204 variants of the ELLPACK (sliced and block-transposed). Further de-
 205 tails on the implementation and the performance of these formats
 206 can be found in our previous work [8]. Note, this reordering and

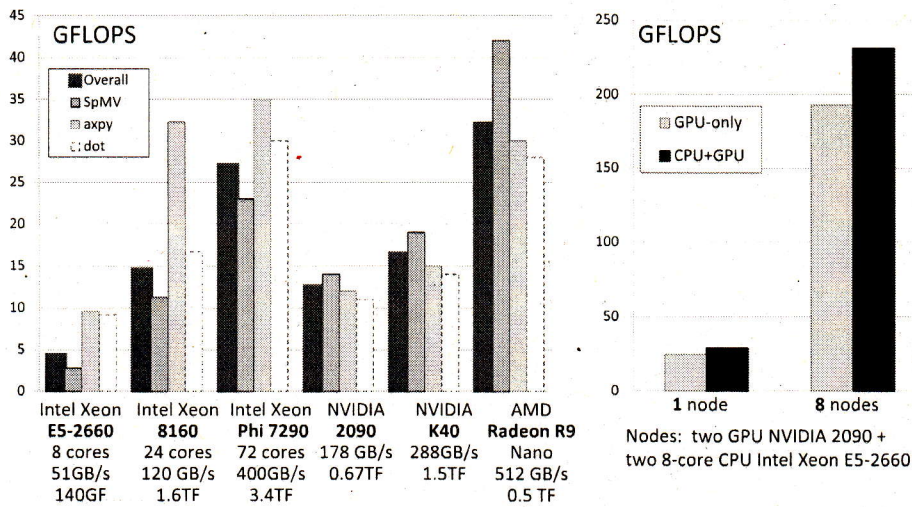


Fig. 4. Left: performance of the overall DNS algorithm (Algorithm 1) and the three basic kernels tested on different devices. Right: heterogeneous execution of the overall DNS algorithm vs GPU-only.

207 storage adaptation are internal routines that are hidden from the
208 outer layers.

209 3.3. Performance study

210 Firstly, the performance of HPC² depends mainly on the algebraic
211 kernels that compose its core. It must be noted that these algebraic
212 operations have very low arithmetic intensity. For double-precision
213 values, the FLOP per byte ratio is typically around 1/8 (one operation
214 per 8-byte argument). Therefore, it is clearly a memory-bounded
215 application that requires a lot of attention to memory access optimization.
216 For this reason, the theoretically achievable performance can hardly reach
217 several few percents of the device's theoretical peak. For instance, for
218 NVIDIA 2090 GPU this limit is around 3% (i.e. $(0.125\text{FLOP}/\text{Byte} \cdot 178\text{GB/s})/670\text{GFLOPS}$).
219 This performance level is rather consistent with the results of the well-known
220 HPCG benchmark [7] that reproduces a memory-bounded sparse algebraic
221 application.

222 Single device tests have been run to study the performance of the HPC²
223 on the following devices: Intel Xeon E5-2660, Intel Xeon E5-2620 v2,
224 Intel Xeon 8160, Intel Xeon Phi 7290, NVIDIA Tesla 2090, NVIDIA Tesla
225 K40, and AMD Radeon R9 Nano. Single-device results shown in Fig. 4 (left)
226 illustrate the performance comparison for the overall DNS algorithm (see
227 Algorithm 1 in Section 4) and its three major kernels on different kinds of
228 devices: several generations of multicore and manycore CPUs and GPUs of
229 AMD and NVIDIA. Additionally, performance on ARM-based SoCs can be
230 found in our previous work [14]. The mesh size per device was around
231 1 million cells unstructured and hexa-dominant). As expected, the achieved
232 performance is directly related to the bandwidth capacity of the devices.
233 Consequently, the AMD GPU outperforms the FLOPS-oriented high-end
234 computing devices due to its higher memory bandwidth. On the other hand,
235 the attained performance and its ratio between devices differ for each
236 kernel. This requires a careful workload balancing based on the performance
237 of the overall algorithm but not the separate kernels.

238 The benefits of the heterogeneous CPU+GPU execution have been measured
239 on a hybrid cluster using two 8-core CPUs (E5-2660) and two GPUs (NVIDIA
240 2090). Comparison with the GPU-only execution is shown in Fig. 4 (right)
241 for an unstructured hexa-dominant mesh with 1 million cells per node. The
242 performance gain compared to the GPU-only mode was 19%. Furthermore,
243 the heterogeneous efficiency (i.e. the ratio between the heterogeneous

244 performance and the sum of the CPU-only and the GPU-only performance)
245 appeared to be near 100% on 8 nodes. This efficiency was expected to
246 reduce since the CPUs should be more involved in communications. However,
247 the communication overhead appears to be much more efficiently hidden
248 when overlapping with GPUs.

249 Finally, multi-GPU strong and weak scaling results are shown in Fig. 5
250 for hexahedral meshes that represent the computational domain of the DNS
251 configuration described in the following section. The HPC5 supercomputer
252 of the Kurchatov Institute was used for these tests. Its nodes are equipped
253 with two dual-GPU NVIDIA K80 devices. It can be observed that the parallel
254 efficiency goes down rather rapidly in the strong speedup tests, allowing to
255 speed-up around $8 \times$ at a reasonable efficiency level. This is due to the natural
256 fact that the GPUs decrease throughput notably when the workload per
257 device goes down. In contrast, the weak scaling efficiency with a sufficient
258 workload per device appears rather high. For the mesh of 1.3 billion cells,
259 it is 94% on 256 GPUs when scaling from one 4-GPU node to 64 nodes
260 with the load of 5 million cells per device. At the smaller workload of 1
261 million cells per device it is lower, 67% (around 1.5 times slowdown),
262 when scaling from 1 to 256 devices because, firstly, the computing load is
263 not sufficient to hide all the communications, and, secondly, the scaling
264 range is 4 times bigger.

271 4. Challenging HPC²: DNS of a turbulent differentially heated cavity

272 A DNS of a turbulent air-filled differentially heated cavity (DHC) has
273 been chosen as a first CFD case to show the capabilities of the HPC² dealing
274 with large-scale CFD simulations. Firstly, the description of the case in
275 conjunction with the numerical methods is detailed. Then, the DHC results
276 are briefly presented.

277 4.1. Mathematical model and numerical method

278 We consider a cavity of height H , width L and depth D filled with an
279 incompressible Newtonian viscous fluid of kinematic viscosity ν , thermal
280 diffusivity α and density ρ . The geometry of the problem is displayed in
281 Fig. 6(left). The Boussinesq approximation is used to account for the
282 density variations. Thermal radiation is neglected. Under these assumptions,
283 the velocity, \mathbf{u} , and the temperature, θ , are governed by the following set
284 of dimensionless

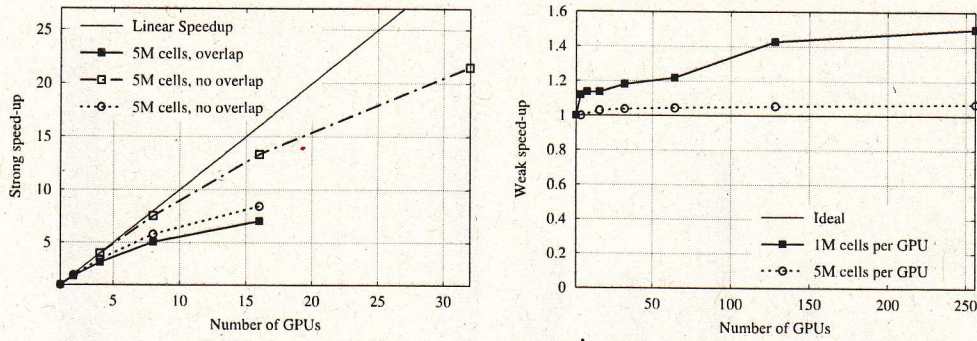


Fig. 5. Strong (left) and weak (right) scaling on multiple GPUs for different mesh sizes.

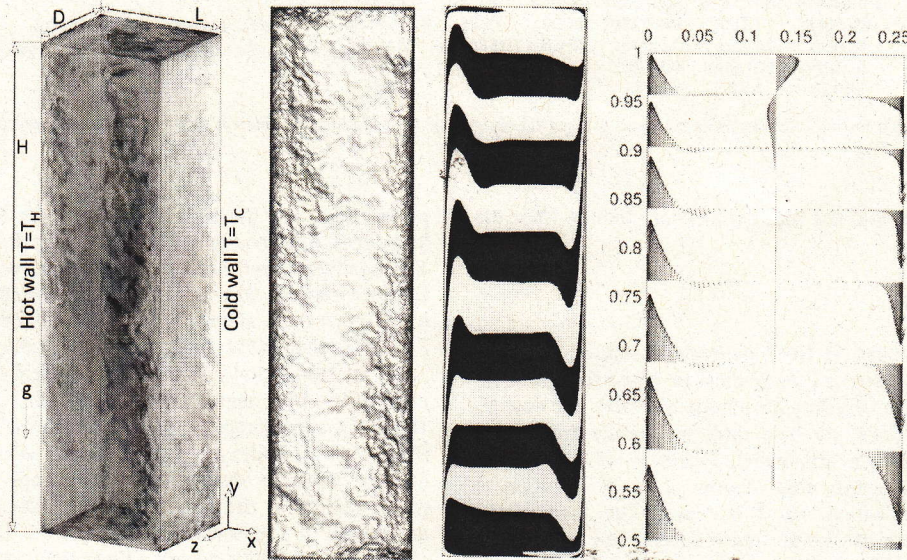


Fig. 6. From left to right: DHC schema, instantaneous schlieren-like snapshot from the DNS and the averaged temperature field at the cavity mid-depth (the isotherms are uniformly distributed between -0.5 and 0.5), the airflow map in the upper part of the cavity.

286 PDEs

$$\partial_t \mathbf{u} + (\mathbf{u} \cdot \nabla) \mathbf{u} = PrRa^{-1/2} \nabla^2 \mathbf{u} - \nabla p + \mathbf{f}, \quad (1)$$

287

$$\partial_t \theta + (\mathbf{u} \cdot \nabla) \theta = Ra^{-1/2} \nabla^2 \theta, \quad (2)$$

288 where $Pr = \nu/\alpha$, $Ra = (g\beta\Delta\theta H^3)/(\nu\alpha)$ and $\mathbf{f} = (0, Pr\theta, 0)$
 289 (Boussinesq approximation) are the Prandtl and Rayleigh number
 290 (based on the cavity height), and the body force vector,
 291 respectively. Notice that with the reference quantities, $L_{ref} = H$
 292 and $t_{ref} = (H^2/\alpha)Ra^{-1/2}$, the vertical buoyant velocity, $Pr^{1/2}$, and
 293 the characteristic dimensionless Brunt-Väisälä frequency, N , are
 294 independent of the Ra . The configuration considered here resembles
 295 the experimental set-up performed by Saury et al. [15] and
 296 Belleoud et al. [16]: the height, H/L , and depth, D/L , aspect ratios
 297 are 3.84 and 0.86 , whereas the Rayleigh and Prandtl numbers
 298 are $Ra = 1.2 \times 10^{11}$ and $Pr = 0.71$ (air), respectively. The cavity
 299 is subjected to a temperature difference $\Delta\theta$ across the vertical
 300 isothermal walls ($\theta(0, y, z) = 0.5$, $\theta(L/H, y, z) = -0.5$). The temper-
 301 ature at the rest of walls is given by the “Fully Realistic” boundary
 302 conditions proposed in [17]. They are time-independent analytical
 303 functions that fit the experimental data of Salat et al. [18]. The
 304 no-slip boundary condition is imposed on the walls.

305 The governing Eqs. (1) and (2) are discretized using a
 306 symmetry-preserving discretization [19]. Shortly, the temporal evolu-
 307 tion of the spatially discrete velocity vector, \mathbf{u}_c , is governed

308 by the following operator-based finite-volume discretization of
 309 Eqs. (1)

$$\Omega_c^{3d} \frac{d\mathbf{u}_c}{dt} + C_c^{3d}(\mathbf{u}_s)\mathbf{u}_c + D_c^{3d}\mathbf{u}_c + \Omega_c^{3d} G_c \mathbf{p}_c = \mathbf{f}_c, \quad M\mathbf{u}_s = \mathbf{0}_c,$$

310 where the $\mathbf{p}_c \in \mathbb{R}^n$ and $\mathbf{u}_c \in \mathbb{R}^{3n}$ are the cell-centered pressure and
 311 velocity fields. For simplicity, \mathbf{u}_c is defined as a column vector and
 312 arranged as $\mathbf{u}_c = (\mathbf{u}_1, \mathbf{u}_2, \mathbf{u}_3)^T$, where $\mathbf{u}_i = ((u_i)_1, (u_i)_2, \dots, (u_i)_n)^T$
 313 are the vectors containing the velocity components corresponding
 314 to the x_i -spatial direction. The auxiliary discrete staggered velocity
 315 $\mathbf{u}_s = ((u_s)_1, (u_s)_2, \dots, (u_s)_m)^T \in \mathbb{R}^m$ is related to the centered ve-
 316 locity field via a linear shift transformation (interpolation) $\Gamma_{c \rightarrow s} \in$
 317 $\mathbb{R}^{m \times 3n}$, $\mathbf{u}_s \equiv \Gamma_{c \rightarrow s} \mathbf{u}_c$. The dimensions of these vectors, n and m , are
 318 the number of control volumes and faces on the computational
 319 domain, respectively. The subindices c and s refer to whether the
 320 variables are cell-centered or staggered at the faces. The matrices
 321 $\Omega_c^{3d} \in \mathbb{R}^{3n \times 3n}$, $C_c^{3d}(\mathbf{u}_s) \in \mathbb{R}^{3n \times 3n}$ and $D_c^{3d} \in \mathbb{R}^{3n \times 3n}$ are block diagonal
 322 matrices given by

$$\Omega_c^{3d} = I_3 \otimes \Omega_c, \quad C_c^{3d}(\mathbf{u}_s) = I_3 \otimes C_c(\mathbf{u}_s), \quad D_c^{3d} = I_3 \otimes D_c,$$

323 where $I_3 \in \mathbb{R}^{3 \times 3}$ is the identity matrix. $C_c(\mathbf{u}_s) \in \mathbb{R}^{n \times n}$ and $D_c \in$
 324 $\mathbb{R}^{n \times n}$ are the collocated convective and diffusive operators, respec-
 325 tively. The temporal evolution of the discrete temperature $\theta_c \in$
 326 \mathbb{R}^n (see Eq. (2)) is discretized in the same vein. For a detailed
 327 explanation of the spatial discretization, the reader is referred to

Table 1

Physical and numerical simulation parameters of the DNS of the turbulent DHC displayed in Fig. 6. From left to right: number of control volumes and concentration factors for each spatial direction, the size of the first off-wall control volume in the x -direction (also in wall-units), the non-dimensional time-step, the starting time for averaging and the time-integration period.

N_x	N_y	N_z	γ_x	γ_y	γ_z	$(\Delta x)_{\min}$	$(\Delta x)_{\min}^+$	Δt	t_0	Δt_{avg}
450	900	256	2	1	1	4.28×10^{-5}	≤ 0.5	3.65×10^{-4}	≈ 300	≈ 300

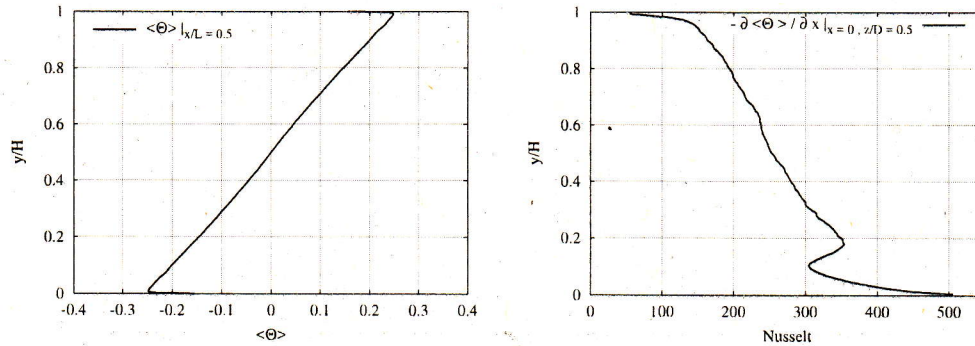


Fig. 7. Left: average temperature profiles at the cavity mid-depth at mid-width. Right: averaged Nusselt number at the cavity mid-depth.

328 Trias et al. [19]. Regarding the temporal discretization, a second-
 329 order explicit one-leg scheme is used for both the convective and
 330 the diffusive terms [20]. Finally, the pressure-velocity coupling is
 331 solved by means of a classical fractional step projection method
 332 [21]: a predictor velocity, \mathbf{u}_s^p , is explicitly evaluated without con-
 333 sidering the contribution of the pressure gradient. Then, by impos-
 334 ing the incompressibility constraint, $M\mathbf{u}_s^{n+1} = \mathbf{0}_c$, it leads to a
 335 Poisson equation for $\tilde{\mathbf{p}}_c^{n+1}$ to be solved once each time-step,

$$L\tilde{\mathbf{p}}_c^{n+1} = M\mathbf{u}_s^p \quad \text{with} \quad L = -M\Omega_s^{-1}M^T, \quad (3)$$

336 where $\tilde{\mathbf{p}}_c = \Delta t \mathbf{p}_c$ and the discrete Laplacian operator, L , is repre-
 337 sented by a symmetric negative semi-definite matrix.

338 In summary, the method is based on only five basic (linear) op-
 339 erators: the cell-centered and staggered control volumes, Ω_c and
 340 Ω_s , the matrix containing the face normal vectors, N_s , the cell-to-
 341 face scalar field interpolation, $\Pi_{c \rightarrow s}$ and the divergence operator,
 342 M . Once these operators are constructed, the rest follows straight-
 343 forwardly from them. The algorithm to solve one time-integration
 344 step is outlined in Algorithm 1. At this point, it must be noted that,
 345 except the non-linear convective term, $C_c^{3d}(\mathbf{u}_s^n)\mathbf{u}_c^n$ and $C_c(\mathbf{u}_s^n)\theta_c^n$,
 346 all the operations correspond to sparse matrix-vector products
 347 ($SpMV$), most of them sharing the same matrix portrait. Regarding
 348 the convection (steps 1 and 2 in Algorithm 1), it can also
 349 be reduced to $SpMV$ operations by simply noticing that the coef-
 350 ficients of the convective operator, $C_c(\mathbf{u}_s^n)$, must be re-computed
 351 accordingly to the adopted numerical schemes [19]. Moreover, the
 352 computation of these coefficients can also be viewed as a $SpMV$.
 353 Therefore, the convective operator is represented as a concate-
 354 nation of two $SpMV$ s: (i) firstly, to compute the coefficients of
 355 the convective operator, $C_c(\mathbf{u}_s^n)$. (ii) then, to compute the matrix-
 356 vector product to obtain the resulting vector, e.g. $C_c(\mathbf{u}_s^n)\theta_c^n$.

357 Regarding the time-integration scheme (steps 2 and 7 in
 358 Algorithm 1), and without loss of generality, a second-order
 359 Adams-Bashforth has been adopted here. Since it is a fully explicit
 360 schemes a CFL-like condition is required in order to keep the nu-
 361 merical scheme inside the stability region [20]. This necessarily
 362 leads to rather small time-steps, Δt and subsequently to a good
 363 initial guess for the Poisson equation. This justifies the fact that a
 364 relatively simple linear solver for the Poisson equation (step 3 in
 365 Algorithm 1) suffices to maintain the norm of the divergence of
 366 the velocity field, $M\mathbf{u}_s^{n+1}$, at a low enough level [22]. Furthermore,
 367 since the matrix, $-L$, is symmetric and positive-definite, a Pre-

conditioned Conjugate Gradient is used with a simple $SpMV$ -based
 preconditioner (either the Jacobi or the approximate inverse). In
 conclusion, the overall Algorithm 1 relies on the set of three basic
 algebra operations: $SpMV$, dot and $axpy$.

4.2. Results and discussion

372 Since no subgrid-scale model is used in the computation, the
 373 grid resolution and the time-step, Δt , have to be fine enough to
 374 resolve all the relevant turbulence scales. Furthermore, the starting
 375 time for averaging, t_0 , and the time-integration period, Δt_{avg} , must
 376 also be long enough to properly evaluate the flow statistics. The
 377 procedure followed to verify the simulation results is analogous to
 378 our previous DNS work [23,24]. In this case, the averages over the
 379 three statistically invariant transformations (time, mid-depth plane
 380 and central point symmetry) have been carried out for all fields
 381 and the grid points in the three wall-normal directions are dis-
 382 tributed using a hyperbolic-tangent function, i.e. for the x -direction
 383 $x_i = \frac{1}{2} \left(1 + \frac{\tanh(\gamma_x(2(i-1)/N_x - 1))}{\tanh \gamma_x} \right)$. For details about the physical and
 384 numerical parameters see Table 1. Hereafter, the angular brackets
 385 operator $\langle \cdot \rangle$ denotes averaged variables.

387 Instantaneous flow fields displayed in Fig. 6 illustrate the in-
 388 herent flow complexity of this configuration. Namely, the vertical
 389 boundary layers remain laminar only in their upstream part up to
 390 the point where the waves traveling downstream grow up enough
 391 to disrupt the boundary layers ejecting large unsteady eddies. An
 392 accurate prediction of the flow structure in the cavity lies on the
 393 ability to correctly locate the transition to turbulence while the
 394 high sensitivity of the thermal boundary layer to external distur-
 395 bances makes it difficult to predict (see results for a similar DHC
 396 configuration in [24,25], for instance). In this case, the transition
 397 occurs around $y \approx 0.2$ (see the peak of the averaged local Nusselt
 398 number displayed in the right part of Fig. 6).

399 The average temperature field and the airflow map are dis-
 400 played in Fig. 6 (right). The cavity is almost uniformly stratified
 401 with a dimensionless stratification of $S \approx 0.45$ (see Fig. 7, left). This
 402 value is in a rather good agreement with the experimental results
 403 obtained by Saury et al. [15] ($S = 0.44 \pm 0.03$ with $\epsilon = 0.1$ and
 404 $S = 0.57 \pm 0.03$ with $\epsilon = 0.6$, where ϵ is the wall emissivity). The
 405 averaged Nusselt number at the cavity mid-depth is displayed in
 406 Fig. 7 (right). The profile is again rather similar to the experimen-

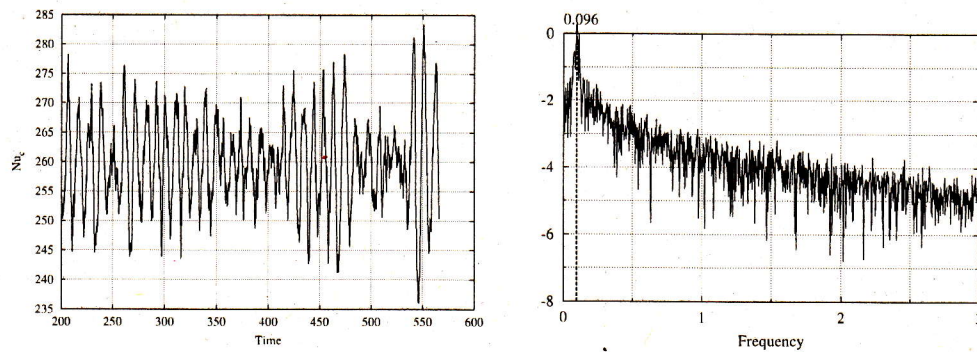


Fig. 8. Time evolution of the Nusselt number at the vertical mid-plane (left) and its normalized density power spectrum (right).

tal results obtained by Saury et al. [15]. In this case, the transition point occurs at a slightly more upstream position. The peak of the averaged local Nusselt number is located at $y \approx 0.2$ whereas in the experimental results this point is at $y \approx 0.3$. Integrating the averaged local Nusselt number over the y -direction, the overall Nusselt is determined. In this case, $\langle Nu \rangle = 259.2$, a slightly higher value than the one obtained by D. Saury et al. [15], i.e. $\langle Nu \rangle = 231 \pm 30$ but very similar to the value obtained by means of LES, i.e. $\langle Nu \rangle = 254$ (see Fig. 11 in [15]).

Another important feature of this kind of configuration is the presence of internal waves. Although in the cavity core the averaged velocity (and its fluctuations) are much smaller compared with those observed in the vertical boundary layers, simulations show that in this region isotherms oscillate around the mean horizontal profile. As mentioned-above, the cavity core remains well stratified (see Figs. 6 and 7, left); therefore, this phenomenon can be attributed to internal waves. This can be confirmed by analysing the Nusselt number through the vertical mid-plane, Nu_c . The time evolution and the normalized density power spectrum are respectively displayed in Figs. 8. The peak in the spectrum is located at 0.096 which is in a good agreement with the dimensionless Brunt-Väisälä frequency, $N = (SPr)^{0.5} / (2\pi)$, where S is the dimensionless stratification of the time-averaged temperature, i.e. $N \approx 0.09$. Both values are very similar confirming that internal waves are permanently excited by the eddies ejected from the vertical boundary layer. Detailed results including turbulent statistics can be downloaded in the following link [20].

5. Conclusions

Motivated by the constant evolution of HPC architectures, the aim of this paper was to design a fully-portable, algebra-based framework suitable for heterogeneous computing with the aim of providing a user-friendly environment for writing algorithms in the fields of computational physics and mathematics. As a computing novelty, the heterogeneous MPI+OpenMP+OpenCL implementation of kernels has been combined with a multi-level domain decomposition strategy for distributing the workload among heterogeneous computing resources. Results have shown that the heterogeneous performance of the HPC² on a hybrid CPU+GPU cluster is nearly identical to the sum of the CPU-only and the GPU-only performance. The multi-GPU scalability of a CFD simulation has been demonstrated on up to 64 nodes equipped with 4 GPU devices. In addition, the performance has been studied on various architectures including different generations of multicore-CPU, AMD and NVIDIA GPUs, manycore accelerators (with the same kernel code, only changing the local workgroup sizes). These results demonstrate the portability of the proposed approach.

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