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Towards integrated devices for ultra-fast all-optical signal processing in optical networks

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ABSTRACT

Over the last decades, the internet traffic has been rapidly growing resulting in Terabits per second aggregate data throughputs. We have also been witnessing that the electronic signal processing the way it is known today is approaching its limitations due to a grooving manifestation of the electronic bottleneck. It is believed that the ultra-fast signal processing based on all-optical integrated devices (passive, active or both) could play an important role in communication systems of the future. In this paper we discuss integrated devices, approaches and techniques we have developed and demonstrated towards achieving these ambitious goals and leading also to improve the scalability of optical networks such as Optical Code Division Multiple Access (OCDMA), namely: all-optical ultrafast demultiplexers, self-clocked all-optical time gates, all-optical thresholders, filters, AWGs, tunable optical delay lines, and devices for dispersion management.

Keywords: Scalability, integrated Mach-Zehnder switch, integrated Sagnac switch, subwavelength waveguide grating, integrated optical filters, integrated true delay lines, ultra-fast all-optical signal processing, silicon photonics.

1. INTRODUCTION

The increasing demand for high-throughput high-data rates communication is pushing researchers to look for novel devices and highly scalable network architectures. Various multiplexing techniques have been explored. To further increase the network scalability, approaches such as Dense Wavelength Division Multiplexing (DWDM), Optical Code Division Multiple Access (OCDMA) over Time Division Multiplexing (TDM) [1] and hybrid 'WDM and TDM' [2] have been proposed.

Lot of attention has been paid to Optical CDMA systems which can support bursty traffic and carry variable data rates. In particular, OCDMA schemes using Two-Dimensional Wavelength-Hopping Time-Spreading codes (2D-WH/TS OCDMA) were found to be highly flexible multiplexing systems designed for very efficient and scalable multiple access applications. Additionally, 2D-WH/TS OCDMA can offer increased physical layer privacy and on-demand bandwidth sharing management [3].

To further improve the scalability, by deploying 2D-WH/TS OCDMA while allowing the codes reuse, 'OCDMA over OTDM' architecture was proposed and demonstrated [4]. It was shown that the scalability (the total number of simultaneous users) of this hybrid approach is increased by a factor of $M \times N$, where N is the number of used OTDM channels and M is the number of OCDMA users per the OTDM channel. Basing on this concept, the further increase in scalability was demonstrated by adding a third dimension in the spectral domain. This allowed to increase OCDMA user groups operating in each OTDMA time slot by assigning them into P different spectral sub-bands [5]. It was shown that this scaled-up the number of simultaneous user in proportion to the number of spectral bands, leading to a total number of simultaneous users, $k = P \times M \times N$. However, to be able to achieve all the above, all-optical signal processing devices are required. It will be shown that silicon photonics is an excellent platform for underpinning all-optical devices capable of delivering the ultra-fast operation thus need support for these highly scalable OCDMA systems. Desired devices could be fabricated using existing advanced CMOS technology and their future development can be geared towards the highly advanced all-optical integrated solutions. The key advantage of these devices to be used for signal processing should be their ultra-fast all-optical operation, low latency, and scalability through cascade-ability.

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2. TOWARDS ULTRAFAST ALL-OPTICAL DEVICES SUPPORTING OCDMA

Having in mind the targeted high scalability for OCDMA systems, first we need to start with the selection of coding scheme such OCDMA systems will be using [6] in order to achieve the targeted performance. Improperly selected codes will affect cardinality, network scalability, achievable BER, and will increase Multi Access Interference (MAI). For these reasons, due to its unique properties, the 2D-WH/TS coding scheme is often selected to deliver the desired system performance and scalability of incoherent OCDMA [5]. However, to build these high performance systems based on 2D-WH/TS OCDMA requires ultra-fast all-optical switching devices for time-gating, demultiplexing, and clock recovery but also requires high performance 'passive' all-optical devices such as Array Waveguide Gratings (AWGs), integrated optical filters and delay lines.

2.1 All-optical ultra-fast switching devices for signal processing

To overcome existing bottleneck imposed on signal processing due to limitations of the current electronics known as 'electronic bottle-neck', we need to develop ultra-fast all-optical switching devices. By the term 'all-optical' we understand that any signal (data) analog or digital being in optical form can be controlled (switched, sampled, demultiplexed, routed, thresholded) by the control signal also being in optical domain (i.e., optical clock, synchronization pulse, etc.).

In OCDMA applications, ultra-fast All-optical Asymmetric Demultiplexer (TOAD) [7] was used as ultra-fast all-optical 2ps time gate for removal/suppression of MAI [8]. The limitation of the TOAD device based on the optical semiconductor amplifier (SOA) is an SOA's relatively slow recovery time (5 ps or more). However, thanks to the SOA asymmetric configuration within of the Sagnac interferometer [7] this limitation is mitigated if the SOA based TOAD is used only as an ultra-fast all-optical demultiplexing gate but not as an 'AND' gate. To fully overcome this inconvenience, the exploited nonlinearity of the SOA can be replaced by the ultra-fast nonlinearity observed in the silicon nanowire waveguide. Using this approach, both interferometric switching configurations, Sagnac [10] as well as Mach-Zehnder [9] were successfully demonstrated. Using the above approach a picosecond time gating, ultrafast all-optical clock recovery) were successfully demonstrated [8, 9, 10, 12, 13].

2.2 SWG based Optical Filters, AWGs and delay lines for OCDMA applications

Integrated optical filters, AWGs and delay lines belong to a category of key passive components which have a wide range of applications in optical communications including OCDMA. The AWGs are essential in constructing OCDMA encoders and decoders [1, 2] including 2D-WH/TS code generations [3]. Optical filters are needed for optical clock suppression or extraction during all-optical signal processing during time gaiting or thresholding [5, 8, 9]. Recent advancements in the development of CMOS compatible approaches led to the development of devices based on silicon-on insulator (SOI) as well as deployment of sub-wavelength waveguide gratings (SWG) technology [14, 17, 18, 19, 20, 21, 22, 23, 24, 25, 26]. All these helped with further device miniaturization and offered unique building blocks for future photonic devices. For example, since the effective index of the SWG waveguide is modifiable by selecting different 'block sizes and duty cycles', the properties of these devices could be greatly extended and controlled in order to deliver targeted parameters. This introduces a flexibility into controlling optical properties of the waveguide gratings filtering devices and delay lines [10, 11, 15] were demonstrated offering better compactness and support for bandwidth flexibility. All these new technological advancements could serve as a fundamental 'blocks' towards the development of novel high performance photonic integrated circuits to enable new ways in optical and microwave photonics signal processing functionalities.

3. CONCLUSION

We have discussed a need for highly scalable networks which can support a large number of simultaneous users in a growing presence of an electronic bottleneck. We have shown that a hybrid solutions involving incoherent Optical CDMA based on two-dimensional wavelength-hopping time-spreading codes when supported by ultra-fast all-optical signal processing can be an approach to meet the ever increasing demands in the fiber optic domain in the last mile solution or optical interconnects. To achieve these goals, the development of CMOS compatible all-optical devices is critical. We have indicated that the variety of all-optical signal processing devices, including those capable of ultra-fast

all optical operation, have already been developed and demonstrated. However more research and development is still needed towards advance photonic chips development.

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