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Distributed Speed Control for Multi-Three Phase Electrical Motors with Improved Power Sharing Capability

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Abstract—**This paper proposes a distributed speed control with improved power sharing capability for multi-three phase synchronous machines. This control technique allows the speed to be precisely regulated during power sharing transients among different drives. The proposed regulator is able to control the time constant of the current within the *dq0* reference frame to a step input variation. If compared to current set-point step variations, the proposed droop controller minimises device's stress, torque ripple, and thus mechanical vibrations. Furthermore, since distributed, it shows improved fault tolerance and reliability. The design procedure and the power sharing dynamic have been presented and analysed by means of Matlab/Simulink and validated in a 22kW experimental rig, showing good agreement with the expected performances.**

I. INTRODUCTION

Multi-three phase electrical motors are gaining popularity thanks to the growing interest in the electrification of transportation systems [1]. Due to their advantages if compared against standard three-phase systems [2], multi-three phase electrical motors and multi-drive applications are still widely investigated, i.e. aerospace [3], [4], mining machines [5], [6], ships [7], [8], and road vehicles [9]. One of their main benefit is redundancy, leading to increased fault tolerance and reliability. On the other hand, system design and control complexity increase both on machine and on power conversion side, thus requiring higher development efforts. Furthermore, in order to guarantee constant operating power in case of partial failure, both machine segments and converters must be over-rated decreasing specific power (i.e. power-to-weight ratio) and power density (i.e. power per unit of volume). Among all the possible applications, multi-three phase motors have been widely developed in Integrated Modular Motor Drive (IMMD) scenario [10], [11]. In-fact, thanks to their intrinsic redundancy, they allow more reliable motor drive systems to be developed. Looking at Fig.s 1, full redundancy can be achieved only by the distributed speed configuration in Fig. 1b. Clearly, in case of drive fault in Fig. 1a, the system would be compromised. On the other hand, in the centralised speed configuration where all the currents are fed back to the drive, all the sub-spaces can be controlled applying the Vector Space Decomposition (VSD) [12], and for this reason it allows better current control to be achieved [13].
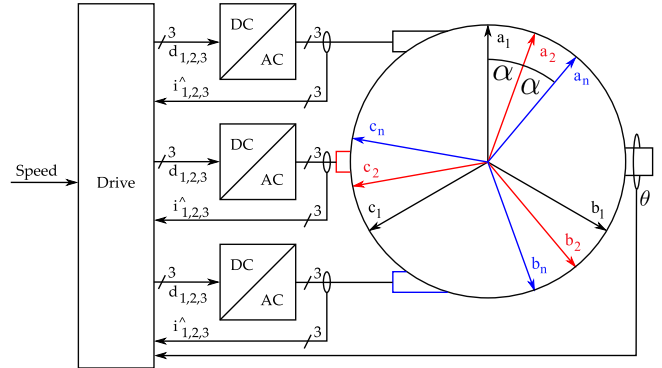
(a) Centralised speed controller. Not redundant on the drive side.
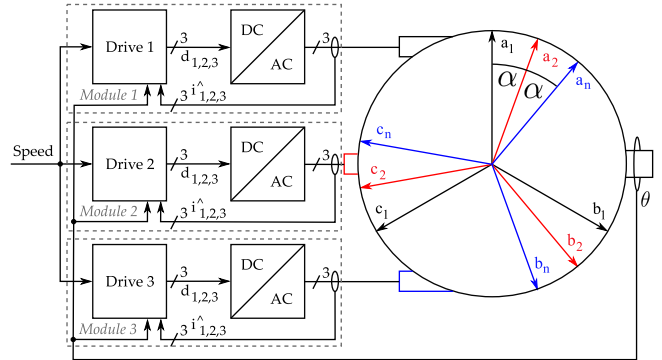
(b) Distributed speed controller. Redundant on the drive side.

Fig. 1. d_k stands for duty cycle, i_k for current ($k = 1, 2, 3$). a_j,b_j,c_j are the phases of the j-th set of windings ($j = 1..n$). θ is the rotor position.

In this paper, a distributed speed control with power sharing capability - preliminarily introduced in [14], [15] - is extended and validated on a multi-three phase machine. Design and experimental validation of power sharing capability among three converters supplying a multi-three phase salient pole synchronous generator with one pole pair and nine phases is presented. Considering the split-phase winding arrangement in Fig. 1 with $n = 3$ three-phase set of windings and defining $m = 3$, the total number of phases is $N = nm$. Therefore, in Fig. 1, $\alpha = \pi/N = \pi/9$. In the next section, the current control loop design based on the first harmonic inductance is provided [12], [16], [17]. In Section III, the
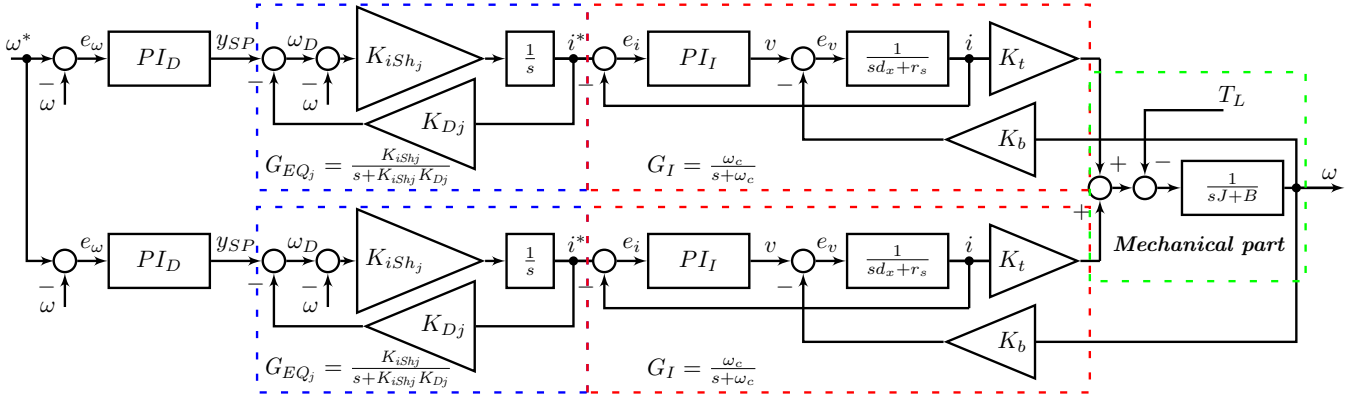
Fig. 2. *q*-axes of the proposed distributed speed control schematic within the synchronous reference frame without axes decoupling.

speeds control loop design is detailed and the Common Speed Reference (CSR) and Torque Follower (TF) configurations are explained [18]. Whilst Section IV introduces power sharing feature based on droop control strategy, Section V details the compensation loop needed to restore the steady state speed drop. In order to clarify the experimental validation shown in Section VII, Section VI summarises the whole control procedure. Conclusions are finally given in Section VIII.

II. CURRENT CONTROL

The current control loops design has been based on the first harmonic inductance obtained from the Vector Space Decomposition (VSD) [12]. Considering switching delays (T_{sw}) and current filter cut-off frequency (ω_{fc}), *d-q* current PI controller proportional (K_{pI}) and the integral (K_{iI}) gains have been designed on the following plant:

$$G_{Ix}(s) = \frac{1}{sT_{sw}+1} \frac{1}{sd_x + r_s} \frac{\omega_{fc}^2}{s^2 + \sqrt{2}\omega_{fc}s + \omega_{fc}^2} \quad (1)$$

where s is the Laplacian operator, x is d or q axis, r_s is the phase stator resistance, and d_x is the first *d-q* harmonic inductance calculated using the VSD [17].

III. SPEED CONTROL

Speed control is achieved either configuring all the drives in speed mode (Common Speed Reference - CSR) or configuring one drive in speed mode and the others in torque mode (Torque Follower - TF). The latter configuration is based on a master/slave approach, hence in case of master drive fault, the system is compromised. On the other hand, the CSR is distributed, guaranteeing system operation in case of fault of any of the drives. From now on, speed control will refer to the CSR configuration. Taking into account the current control loop cut-off frequency (ω_c), speed PI controller proportional (K_{pS}) and integral (K_{iS}) gains have been computed on plant G_S in (2), where J is the inertia, B is the friction, and K_t is the machine constant relating torque and i_q current.

$$G_S(s) = \frac{\omega_c}{s + \omega_c} \frac{K_t}{sJ + B} \quad (2)$$

IV. POWER SHARING

Power sharing in speed controlled multi-three phase motors can be achieved with tunable current set-points [18]. Current set-point step change would lead to discontinuity, hence current distortion and mechanical vibration. The proposed self-balancing sharing controller guarantees smooth transients even with step variations in sharing ratios. The regulator has been derived by the so called droop control strategy - common in power transmission systems - where the basic droop characteristic is a linear function with a negative coefficient on the Frequency/Active-Power plane [19].
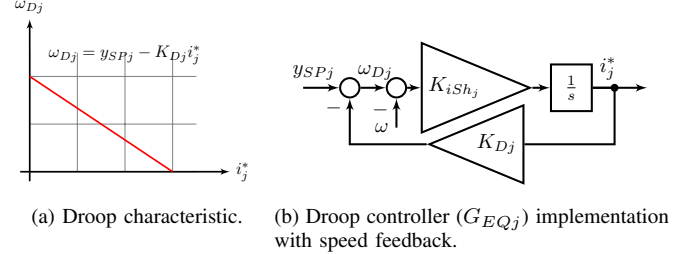

(a) Droop characteristic.   (b) Droop controller (G_{EQj}) implementation with speed feedback.

Fig. 3. *j* denotes the *j-th* set of windings. K_{iShj} is the integral sharing gain.

$$G_{EQj} = \frac{K_{iShj}}{s + K_{iShj}K_{Dj}} \quad (3)$$

Translating the droop concept from power systems to multi-three phase drives, the droop concept is a linear function with a negative coefficient (K_{Dj}) on the Speed/Current set-point plane (Fig. 3a). In Fig. 3b, the droop controller with the droop coefficient K_{Dj} is shown. y_{SPj} and ω_{Dj} are the speed and the internal (or drooped) speed set-point, respectively. The droop controller transfer function is in (3).

For simplicity, a distributed control schematic with just two sets of windings is shown in Fig 2. T_L is the load torque, K_b is the electro-magnetic force constant, and PI_I is the current PI controller. Due to the presence of the droop coefficient, the original speed set-point drop ($y_{SPj} - \omega_{Dj}$) must be restored with an outer loop, here referred as compensation loop. Final speed dynamics is achieved thanks to the PI_D regulator. In the

next sub-sections, design procedures for the droop regulator and the PI_D controller for a given power sharing time constant and a given speed dynamics are detailed.

A. Equivalent model

Current PI_I controllers are tuned on stator resistance and first harmonic inductance values in equation (1). Droop controller parameters design must be carried out considering the whole amount of power produced by all the converters. Assuming balanced condition where all the converters are producing the same torque - denoted with (ES) -, the control schematic in Fig. 2 can be simplified with the equivalent collective control scheme in Fig. 4 where paralleled cur-
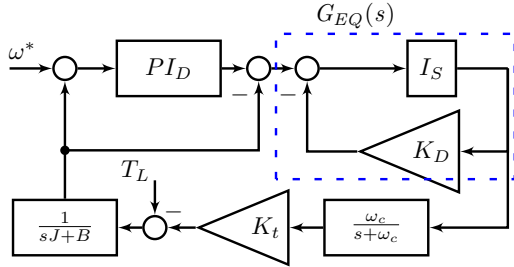

Fig. 4. Equivalent collective control scheme valid when the load power is equally split among the n modules. $I_s = K_{iS}/s$.

rent loops (dashed red squares) have been replaced by the $G_I = \omega_c/((s + \omega_c))$ transfer function. The parallel of droop controllers have been replaced by the $G_{EQ} = nG_{EQj}^{(ES)}$ transfer function valid if and only if [15]:

$$K_{iSh} = K_{iSh_j}^{(ES)}n; \quad K_D = \frac{K_{D_j}^{(ES)}}{n} \quad (4)$$

The K_D and K_{iSh} parameters can be defined as the collective droop and collective integral gain coefficient respectively. Setting different values of K_{Dj} and K_{iShj}, it is possible to set the amount of torque produced by each drive and the power sharing time constant during current transients. Once droop controllers are tuned, outer compensation loop can be designed on the final speed dynamics specifications.

B. Current sharing dynamics

The total power is given by the sum of all the nominal torques produced by each module multiplied by the shaft speed. Since currents and torques are directly proportional ($T = K_t i$), the j-th power in p.u. is $P_j = I_{nom,j}/(\sum_j^n I_{nom,j})$ where $I_{nom,j}$ is the nominal current on the q-axis of the j-th module. From Fig. 2, it can be noticed that the current set-points are the output of the sharing regulators G_{EQj}. Provided that in steady state the magnitude of the droop controllers in Eq. (3) is the reciprocal of the droop coefficient ($|G_{EQj}|_{s\to 0} = 1/K_{Dj}$), the power produced by each module can be re-written like the following:

$$P_j = \frac{1/K_{Dj}}{\sum_j^n (1/K_{Dj})} = \frac{1/K_{Dj}}{\varepsilon} \ [p.u.] \quad (5)$$
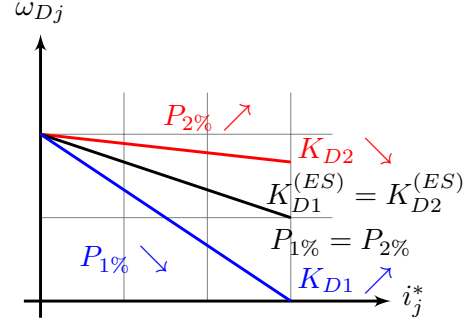

Fig. 5. Power sharing is achieved with different droop coefficients.

Thanks to Eq. 5, as long as ε is kept constant, unbalanced power sharing can be achieved just changing the droop coefficients as shown in Fig. 5. However, constant speed of the shaft is guaranteed if and only if the collective sharing regulator transfer function is kept constant: $\sum_j^n G_{EQj} = nG_{EQj}^{(ES)} = G_{EQ}$. Therefore, constant speed operation is achieved updating both the droop and the integral gain coefficient. Provided that $\sum_j^n P_j = 1$, G_{EQ} is kept constant by dividing the individual equal power droop coefficients $K_{Dj}^{(ES)}$ by a factor $\xi_j = nP_j$ and multiplying the individual integral gain $K_{iShj}^{(ES)}$ by the same factor ξ_j. Since the current sharing dynamics is governed by the droop controller G_{EQj}, its closed loop time constant in Eq. (6) dominates the power sharing transient. Looking at Eq. (6), it is clear that constant sharing transient is obtained only updating both the coefficients.

$$\tau_{sharing,j} = \frac{1}{K_{Dj}K_{iShj}} \quad (6)$$

V. COMPENSATION LOOP

Aim of the compensation loop is to restore the speed drop introduced by the droop controllers and to guarantee adequate speed dynamics performance. Looking at the equivalent collective scheme in Fig. 4, the plant G_D for designing the compensation PI is described by Eq. (7).

$$G_D(s) = \frac{\frac{K_{iSh}}{s+K_{iSh}K_D}\frac{\omega_c}{s+\omega_c}\frac{K_t}{sJ+B}}{1+\frac{K_{iSh}}{s+K_{iSh}K_D}\frac{\omega_c}{s+\omega_c}\frac{K_t}{sJ+B}} \quad (7)$$

VI. CONTROL DESIGN APPROACH

Based on the previous discussion, a possible design procedure for each module with power sharing capability is summarised here. The system has been designed considering a speed set-point $\omega^* = 30rad/s$. Referring to the equivalent collective scheme, d and q current controllers have been assigned the same bandwidth $BW_C = 211rad/s$ and the same phase margin $PM_C = 65°$ whereas the speed and compensation regulator has been designed with a $BW_D = 6rad/s$ and a $PM_D = 60°$. The droop loop, or sharing loop, has been set up with different bandwidth ($BW_D < BW_{Sh} < BW_C$) and phase margin (PM_S) values in order to demonstrate

$\tau_{sharing} = 0.001[s]$					$\tau_{sharing} = 0.030[s]$				
j	P_j	Current	K_{Dj}	K_{iShj}	j	P_j	Current	K_{Dj}	K_{iShj}
$1,2,3(ES)$	$1/3(ES)$	2	$1.5(ES)$	$666.6(ES)$	$1,2,3(ES)$	$1/3(ES)$	2	$1.5(ES)$	$22.2(ES)$
1	2/3	4	0.75	1333.3	1	2/3	4	0.75	44.4
2	1/12	0.5	6	166.6	2	1/12	0.5	6	5.5
3	1/4	1.5	2	500	3	1/4	1.5	2	16.6

how the proposed strategy is able to control current sharing dynamics. Looking at the droop plane in Fig. 3a, the collective droop gain coefficient can be obtained imposing the maximum speed drop with the following Eq.: $K_D = \Delta\omega_{max}/(\sum_j^n I_{nom,j})$. The maximum speed drop would be the steady state output speed of the system without the PI_D in nominal condition. In this particular case, the maximum delta has been set up equal to the 10% of the reference speed ($\Delta\omega_{max} = 3[rad/s]$). Considering a total nominal current of $6[A]$, the collective droop coefficient $K_D^{(ES)} = \Delta\omega_{max}/(\sum_j^n I_{nom,j}) = 0.5[(rad/s)/A]$ has been computed. Therefore, $K_{Dj}^{(ES)} = nK_D^{(ES)} = 1.5[(rad/s)/A]$. Looking at Table I, the integral gain $K_{iShj}^{(ES)} = 1/(K_{Dj}^{(ES)}\tau_{sharing,j})$ has been computed for $1[ms]$ and $30[ms]$, respectively. Finally, dividing the droop gain and multiplying the integral gain by the same factor $\xi_j = nP_j$ (for $P_j = 2/3, 1/12, 1/4$) power unbalancing has been obtained.

VII. EXPERIMENTAL VALIDATION

The proposed droop controller has been validated on a multi-three phase salient pole synchronous generator with one pole pair and nine phases shown in Fig. 7. Machine parameters are as follows: $r_s = 9.1\Omega$, $d_d = 0.045H$, $d_q = 0.114H$, $K_t = 3.06Nm/A$, $J = 0.38Nms^2$, $B = 0.14Nms$. The motor has been connected to three off-the-shelf two-level three-phase converters (IRMD22381Q demo board combined to the FP25R12KE3 power modules from Infineon) controlled by an in-house developed control platform called *uCube* [20]. In order to connect the converters to the *uCube*, three interface
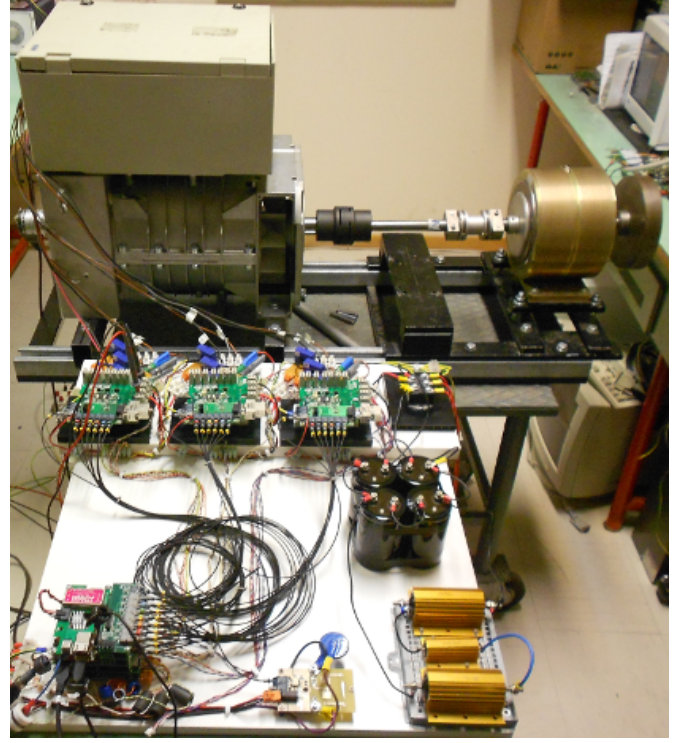

Fig. 7. Multi-three phase trig with motor, brake, *uCube*, rectifier, three two-level converters, nine current sensors, DC-link, bleeding resistors, and brake enable board.
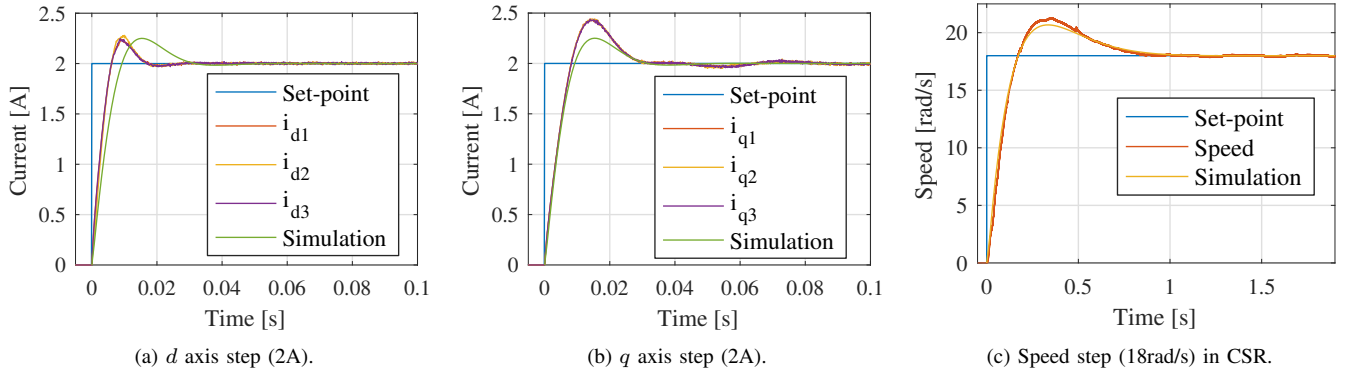

(a) *d* axis step (2A).

(b) *q* axis step (2A).

(c) Speed step (18rad/s) in CSR.

Fig. 6. *d*-current, *q*-current, and speed loop design validation.

(a) Speed variation with load step in CSR.

(b) Speed step (18rad/s) without PI_D.

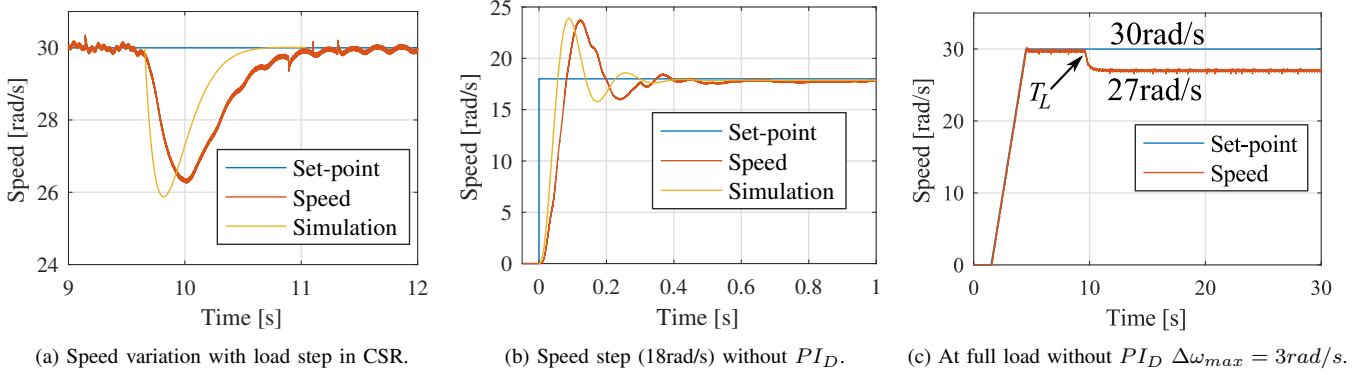(c) At full load without PI_D $\Delta\omega_{max} = 3rad/s$.

Fig. 8. Speed dynamics in Common Speed Reference configuration and speed dynamics as in Fig. 2 but without compensation PI_D.

cards with fibre optic receivers have been designed. The switching frequency and the sampling period have been set to $10kHz$, whilst the DC link voltage was set to $350V$. Current have been measured by nine LEM LA-25P current sensors. In order to produce a load torque, the generator has been coupled to a hysteresis brake enabled by the control platform.

A. Current loop

Current step responses on d and q axes in locked rotor condition are shown in Fig.s 6a, 6b.

B. Speed Loop in Common Speed Reference configuration and without compensation loop

Speed step response and speed variation with load step from zero to full load $T_L = 55.2Nm$ with all the drives in speed mode (CSR) are shown in Fig. 6c, 8a, respectively. Fig. 8b and 8c show the speed step response and the speed variation with load step without compensation loop. In this particular case the sharing time constant has been set to $30ms$. In Fig. 8c, it is possible to appreciate how the speed drop increases with T_L.

C. Compensation loop

In Fig.s 9a, 9b speed step response and speed variation with load step from zero to full load with system configured like in Fig. 2 are shown.
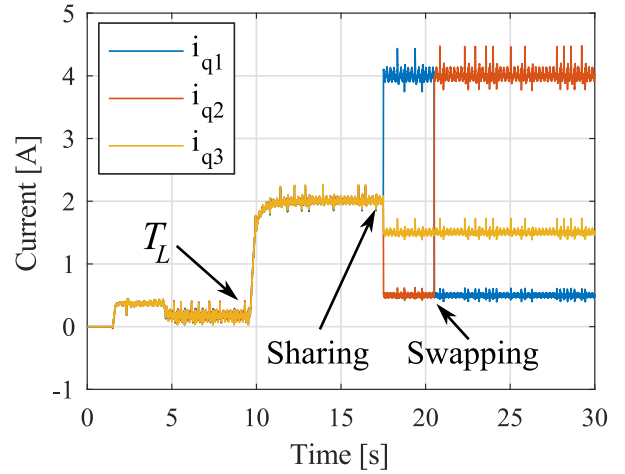
D. Power sharing

Fig. 10. Power sharing.

Looking at Fig. 10, the i_q currents under ramped input from zero to nominal speed are shown. After $10s$ the brake have been enabled. The system was in (ES) condition until second 17.5. At that point, the droop and the integral coefficients

(a) Speed step (18rad/s).

(b) Speed dynamics under load transient.

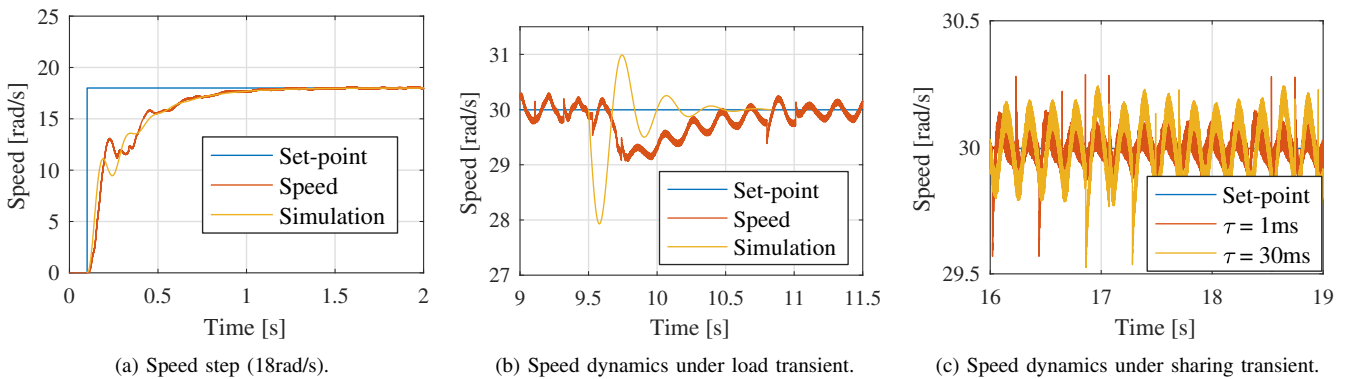(c) Speed dynamics under sharing transient.

Fig. 9. Speed dynamics with compensation PI_D under load and sharing transients for different sharing time constants.

have been programmed for unbalanced sharing like reported in Table I in the previous Section.
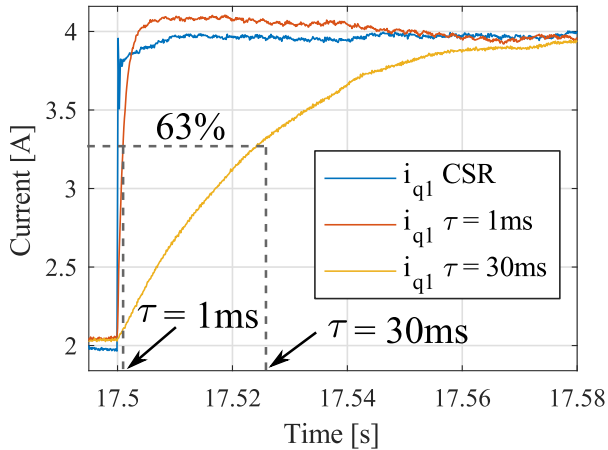

Fig. 11. Sharing time constants comparison.

In Fig. 11, the difference between the current set-point step change - with the system configured in CSR - and the droop controller is highlighted. Whilst current set-point step change leads to current distortion and could potentially excite mechanical resonances, droop controlled transients smoothly reach the steady state with predicted time constants reported in Table I. In Fig. 9c, unaffected speed dynamics under power sharing transient for different time constants is shown.

VIII. CONCLUSIONS

This paper focused on power sharing in multi-three phase speed controlled electrical systems. The so called Speed-Drooped control has been validated on a multi-three phase rig with a salient pole synchronous generator with nine phases. If compared to current steps in Common Speed Reference (CSR) configuration, droop control allows the torque transients to be controlled guaranteeing constant shaft speed. Thanks to the Vector Space Decomposition (VSD), mutual interactions among different sets of windings have been taken into account in the control design. Despite the presence of the electromagnetic couplings within the machine, the droop controller has been successfully validated controlling power sharing transients as predicted.

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