

Research Article

Characterization of the Manufacturing Processes to Grow Triple-Junction Solar Cells

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A number of important but little-investigated problems connected with III-V/Ge heterostructure in the GaInP/GaInAs/Ge multijunction solar cells grown by MOVPE are considered in the paper. The opportunity for successfully applying the combination of reflectance and reflectance anisotropy spectroscopy in situ methods for investigating III-V structure growth on a Ge substrate has been demonstrated. Photovoltaic properties of the III-V/Ge narrow-band subcell of the triple-junction solar cells have been investigated. It has been shown that there are excess currents in the Ge photovoltaic p-n junctions, and they have the tunneling or thermotunneling character. The values of the diode parameters for these current flow mechanisms have been determined. The potential barrier at the III-V/Ge interface was determined and the origin of this barrier formation during MOVPE heterogrowth was suggested.

1. Introduction

Multijunction (MJ) solar cells (SCs) grown by metal-organic vapor phase epitaxy (MOVPE) demonstrate the highest efficiency being one of the most successful trends of photovoltaic development [1–4]. Stacking several junctions with different band gaps allows reducing significantly the fundamental losses due to charge carrier thermalization. Triple-junction cells based on the Ga_{0.52}In_{0.48}P/Ga_{0.99}In_{0.01}As/Ge structure are currently the main part of the mass production of high efficiency multijunction solar cells. Although the material combination in terms of band gap does not give the maximum theoretical efficiency for triple-junction cells, the fact that they are lattice matched allows producing such monolithic heterostructure solar cells with a perfect crystalline quality.

The MJ SCs are widely used both in space and in terrestrial concentrator photovoltaic systems due to high

efficiency under concentrated sunlight and high radiation resistance.

In the topological sense, the MJ SC structure is a series connection of single-junction subcells. Each subcell includes not only the photoactive p-n junction but also the functional heterolayers (e.g., window, back surface field (BSF), etc.). Moreover the subcells are connected by tunnel junctions with additional heterointerfaces. As a result, a MJ SC has a quite complicated structure (see Figure 1), consisting of many layers with different types of interfaces including chemically dissimilar III-V/Ge interface (the GaInP/Ge heterostructure is used as the bottom subcell of the triple-junction GaInP/GaInAs/Ge SC). There are a number of important but little-investigated problems connected with this interface. First of all the electrical properties of the III-V/IV interface have not been enough studied so far, especially taking into account the diffusion processes, which take place during the Ge p-n junction formation during the MOVPE growth of



FIGURE 1: Schematic structure of a GaInP/GaInAs/Ge triple-junction solar cell.

III-V semiconductors on the Ge wafer. The interface phenomena may affect the charge carrier transport in the Ge subcell as well as in the whole multijunction solar cell.

In this paper a review of a number of results devoted to III-V/Ge heterostructure formed during MOVPE is presented. Also new approaches to control the epitaxial heterogrowth of III-V semiconductors on the germanium wafer during the MOVPE growth are shown.

2. Materials and Methods

The growth experiments have been performed in a horizontal low pressure R&D MOVPE reactor. Hydrogen was used as a carrier gas. The following metalorganic compounds were used as the third group element sources: trimethylgallium (TMGa), trimethylaluminum (TMAl), and trimethylindium (TMIn). Arsine (AsH₃) and phosphine (PH₃) were used as the fifth group element sources. Doping precursors for investigated epitaxial layers were disilane (SiH₄) and diethylzinc (DEZn). The experimental layers for in situ investigations as well as photovoltaic structures were grown on 2-inch in diameter substrates.

The MOVPE installation is equipped with a UV-transparent quartz window for optical in situ measurements. A commercially available spectrometer has been used for performing reflectance (R) as well as reflectance anisotropy (RA) signals on rotating samples.

According to Aspnes et al. [5], the reflectance anisotropy for cubic crystals is the difference in reflectance between the two surface symmetry axes along [110] and [-110] directions. For the (100) surface, the RA signal is defined by

$$\frac{\Delta R}{R} = 2\frac{R_{110} - R_{-110}}{R_{110} + R_{-110}},\tag{1}$$

where R_{110} and R_{-110} are the complex reflectance for the polarizations parallel to the [110] and [-110] directions.

The difference in the reflection coefficients for light polarized along the main crystalline axes results mainly from the surface anisotropy [6] (taking into account the bulk symmetry of cubic crystals). This anisotropy is characterized by the spatial configuration of the dimmers formed due to surface dangling bonds. Reflectance anisotropy spectroscopy (RAS) allows picking out weak signals of the surface optical transitions using polarization properties.

Due to sample rotation in MOVPE, the RA signal measurement is more complicated especially when it is accompanied by some degree of wobble [7]. Direct measurements during MOVPE growth processes give the absolute value of the RA signal that should be taken into account in analyzing experimental results.

The in situ measurements are an important part in investigating the semiconductor epitaxial structures and creating different semiconductor devices by the epitaxial method [8]. Investigations by RAS proved to be a useful instrument for determining a number of key parameters of epitaxial layers, semiconductor surfaces, and interfaces [9–12]. The RAS is applied effectively for comprehensive investigation of different semiconductor devices grown by the MOVPE technique, such as heterojunction bipolar transistors [7], laser structures [13], and vertical cavity surface-emitting lasers [14]. However, there is the quite small number of papers on RAS application for investigating the multijunction solar cell.

The approach for studying and controlling parameters of the MJ SC structures during MOVPE growth has been developed [15–17]. There are a number of factors decreasing the surface isotropy, such as surface reconstruction, morphology, atomic ordering, and surface electric field. The RA signal reflects contribution of all these factors. Comparison of the spectra of experimental samples with, for example, similar reconstruction allows obtaining a contribution of ordering and/or doping to their surface anisotropy. It has been demonstrated that the impurity concentration in the GaAs, GaInP, and $Ga_{0.99}In_{0.01}As$ layers grown on both Ge and GaAs substrates may be determined by the RA signal due to the linear electrooptical effect caused by the electric field of the near-surface space charge region [15, 16]. It has been shown that CuPt_B-type ordering effect on the group



FIGURE 2: Approach of the in situ control of MJ SC parameters (layer thicknesses, solid solution composition, doping levels and homointerface detection, heterointerface quality, etc.) based on simultaneous measurements of the time dependencies of short-wavelength (on 3.8 eV) RA and long-wavelength (on 2.1 eV) reflection signals (lower part) and SEM image of cross-section of GaInP/GaInAs/Ge MJ SC.

III sublattice of GaInP [18], which results in narrowing the semiconductor band gap, can be determined by RAS at growth temperature of GaInP on both Ge and GaAs substrates [16].

A general method for the in situ control of MJ SC parameters (layer thicknesses, solid solution composition, doping levels, heterointerface quality, etc.) based on these studies has been proposed (Figure 2). The method is a combination of reflectance with the RAS, and it is based on simultaneously measuring the time dependence of the long-wavelength reflection signal and the short-wavelength anisotropic reflection signal [16, 19]. On the one hand the long-wavelength (on 2.1 eV) measurements characterized by the larger absorption depth allow obtaining the quite large number of Fabry-Perot oscillations on the reflection, which is useful for composition, growth rate, and thickness of a material determination. On the other hand there is direct dependence of the Δ RAS (difference between signals for doped and undoped layers $\Delta RAS = \Delta R/R_{doped} - \Delta R/R_{undoped}$) on the logarithm of the free charge carrier concentration for the short-wavelength range, so 3.8 eV RA signal measurements allow controlling the doping level and observing homointerfaces. Also it is used for determining the quality of interfaces.

There is a wide range of ex situ techniques used for III-V interface characterization. The structural properties of interfaces can be studied by XRD [21]. The values of the recombination rate at interfaces can be measured using time resolved photoluminescence [22]. The values of the valence band offset (ΔE_V) and the interface charge density at interfaces can be obtained by CV profiling [23] among other methods.

However, there are a number of methods which are helpful but not often used for this purpose. First of all they are the above mentioned optical in situ methods, which allow estimating the heterogrowth quality. As an ex situ method, the admittance spectroscopy is helpful enough for studying the interface properties [24]. In general, capture and emission of charge carriers at and from interface states lead to variation of charge at the interface and, therefore, may contribute to the structure capacitance. This fact allows one to use the admittance spectroscopy for defect characterization in the space charge regions [25] and at heterointerfaces [26].

Also the helpful experimental approach is the study of heterointerface influence on the charge carrier transport by means of the dark IV characteristics' measurements in a wide temperature range and analysis of diode parameter temperature dependence.

For detailed study of III-V/Ge heterostructure, the experimental approach consisting in "subcell" fabrication is applied. The p-n junction in the Ge substrate was formed due to the diffusion of P atoms from the growing GaInP nucleation layer. Such grown heterostructure completely reproduces the narrow gap Ge subcell in MJ SC. A n⁺⁺ GaAs contact layer was grown for subsequent fabrication of the test Ge photovoltaic convertor. Then an additional postgrowth thermal treatment which is reproducing the growth conditions (temperature and growth time) of GaInP/GaInAs wide-bandgap tandem was carried out. The structures were annealed in the AsH₃ atmosphere in order to prevent degradation of the GaAs cap layer.

Thus, the experimental simulation of the Ge subcell was performed by an additional thermal treatment of a single Ge cell. Such structures are referred to as "subcell" here.

3. Results and Discussion

To form the bottom Ge subcell, the epitaxial GaInP layers grown on the Ge substrate are used as the source for doping impurities (P) to form Ge p-n junction and as a wideband-gap window for the Ge subcell. On the one hand the III-V/Ge interface is a very critical issue from epitaxial viewpoint, because it is a boundary between chemically dissimilar materials. On the other hand properties of diffusive p-n junction are less controlled and require detailed study.

There is a major problem connected with the growth of defect-free III-V semiconductors on a nonsimilar (group IV) germanium substrate. By using the RAS in situ method, the most optimal conditions for the growth of perfect III-V layers on a Ge were determined. The conditions of deoxidation of Ge substrates and the suppression of autodoping by germanium of the photoactive parts of MJ SC [15] structure have been found due to single-wavelength RA signal temporal dependence measurement during the growth process.

The autodoping process has been suppressed in our experiments by growing a nonphotoactive $n-Ga_{0.99}In_{0.01}As$ buffer at rather low growth temperature. Due to the electrooptical effect, the RAS is sensitive to the doping level (Figure 3).



FIGURE 3: RA spectra (upper part) and Δ RAS spectra ($\Delta R/R_{doped} - \Delta R/R_{undoped}$) for p-type: Zn (blue) and n-type: Si (red) doped Ga_{0.99}In_{0.01}As layers at 600°C growth temperature; energies near the critical points of dielectric function are marked by vertical black lines.



FIGURE 4: The in situ record of the single-wavelength (3.8 eV) RA signal: (a) during GaInAs buffer growth (Ge autodoping suppression is shown) and (b) during GaInP on Ge nucleation (optimization interface quality is shown).

The red dotted line in Figure 4(a) marks the growth time (interval of about 1000 seconds) after which the doping level comes to saturation corresponding to the doping level of the n-Ga_{0.99}In_{0.01}As buffer. Taking into account the Ga_{0.99}In_{0.01}As layer growth rate (8.5 Å/s) one can conclude that the thickness of the layer, when autodoping with germanium influences on RA signal level, is in the range 0.7–0.9 μ m.

The interface quality was determined by analyzing the RA signal temporal dependence during growth. It is known that the RA signal at the interface directly correlates with a rough interface [27]. Three RA transients from GaInP layers grown on Ge after different preepitaxial treatments are shown in Figure 4(b). The maximum RA signal rise was observed at growth on a not completely deoxidized germanium substrate.



FIGURE 5: Bright field TEM image of (1–10) cross-section of a SC structure (a) and dark field TEM image of (1–10) cross-section of the GaInP/Ge interface (b). The TEM images were obtained in twobeam condition with a diffraction vector g = (002).

Further growth of a bulk semiconductor structure resulted in imperfect crystallization of the epitaxial layers. This has been confirmed by comparison of the short-wavelength reflectance signal recorded in the single-wavelength mode with a successful run.

The minimum relative value of the RA signal at the $GaInP_2/Ge$ interface achieved was 8. In this case, the reflection signal level remained unchanged during the following growth of the bulk crystal. A high structural quality of the GaInP layers and a sharpness of the GaInP/Ge interface were confirmed by TEM study (Figure 5). No dislocations or any growth related defects were observed.

Thus, the approach with in situ measurements allows determining the key parameters for successful nucleation on Ge substrates including those which are difficult to obtain by means of ex situ methods.

In spite of such quality a number of new phenomena associated with a GaInP/Ge heterostructure were found during investigation by means of a number of approaches.

In particular, it has been shown [28, 29] that there are excess current components, which, at reverse bias, gave the saturation current density in the range of $(10^{-4}-10^{-3})$ A/cm² for Ge p-n junction formed during the MOVPE diffusion process. Previously, it was believed that, in the Ge p-n junctions, the forward current flow mechanism is diffusion (the ideality factor $A_d = 1$).

For such "subcells" (Ge subcell structure and their growth conditions were experimentally simulated as described above) the dark forward and reverse IV curves were analyzed for the temperature range of 90–330 K. The approximation of Ge subcell IV curves by means of only one-exponential (diffusion) model in the whole temperature range was not absolutely accurate. It was shown that an excess current should be taken into account for Ge subcells and that the appropriate model for approximation of IV curves is twoexponential. As a result of the diode parameter analysis, the nature of the excess current was determined as either tunneling or thermotunneling.



FIGURE 6: Forward and reverse dark IV characteristics of a Ge "subcell" at different temperatures (from 90 to 330 K). Symbols are the experimental data: red line: the calculations by the two-exponential model, blue line: the diffusion components ($A_d = 1$), and blue separated line: the tunneling components ($E_t = 0.17$ V).

A number of samples (especially after high temperature treatment) showed "not classic" IV curves in whole temperature range (Figure 6). For these samples the approximation of the forward branch of the IV curves by the sum of the components of exponential form (2) has shown that, in the operation temperature range of 90–330 K, the forward current consists of the diffusion component and the excess component:

$$J = J_{od} \cdot \left[\exp\left(\frac{V - J \cdot R_s}{E_d}\right) - 1 \right] + J_{ot} \cdot \left[\exp\left(\frac{V - J \cdot R_s}{E_t}\right) \right],$$
(2)

where *J* is the current density in the external circuit, *V* is voltage, R_s is the series resistance of the device, J_{od} is the saturation current of the diffusion component (preexponent), J_{ot} is the preexponent ("saturation" current) of the excess (tunneling) component, E_t is the characteristic potential of the tunneling component, and E_d is the characteristic (thermal) potential of the diffusion component, which may be expressed through the ideality factor $A_d = 1$:

$$E_d = \frac{kT}{q} \cdot A_d = \frac{kT}{q},\tag{3}$$

where q is the electron charge, k is the Boltzmann constant, and T is the absolute temperature.

The analysis of diode parameters of the second term in the expression (2) showed that the excess current is characterized by a thermally independent potential E_t (0.17 V) and weak (in comparison with the thermal activation dependence) exponential temperature dependence of J_{ot} . The temperature



FIGURE 7: Dependencies of the Ge "subcell" diode parameters of diffusion (E_d , J_{od}) and tunneling (E_t , J_{ot}) current components on the value of inverse (a) and forward (b) absolute temperature.

coefficient is $b = \Delta \ln J_{ot} / \Delta T = 3 \div 9 \times 10^{-3} \text{ K}^{-1}$) (Figure 7). That allows characterizing the mechanism as a tunneling current.

However, in most cases, the excess component is not observed in the dark IV characteristics of Ge subcell at RT. However, at lower temperatures (especially at T < 200 K) excess current has been detected. Thus, in this case the dark IV characteristic of a Ge "subcell" can be described by twoexponential model similarly to (2):

$$J = J_{od} \cdot \left[\exp\left(\frac{V - J \cdot R_s}{E_d}\right) - 1 \right] + J_{ott} \cdot \left[\exp\left(\frac{V - J \cdot R_s}{E_{tt}}\right) \right],$$
(4)

where the second term is thermotunneling current, J_{ott} is the preexponent ("saturation" current), and E_{tt} is the characteristic potential of the thermotunneling component.

Temperature dependence of J_{ott} (Figure 8(a)) at low temperatures is rather weak (in comparison with the thermal activation dependence) but at high temperatures can be approximated by an exponential function:

$$J_{ott} = J_{\infty} \cdot \exp\left(\frac{-E_{g0}}{A_{\beta} \cdot kT}\right),\tag{5}$$

where $E_{g0} = 0.78 \text{ eV}$ [20] is the extrapolated value of the Ge bandgap at $T \rightarrow 0$, A_{β} is the coefficient ("ideality factor") independent of temperature and approximately equal to 1.5, and J_{∞} is the constant, which characterizes J_{ott} at $1/T \rightarrow 0$ (experimentally determined value of J_{∞} is 5000 A/cm²). The characteristic potential E_{tt} at high temperatures can be well approximated by a function which increases linearly with temperature (Figure 8(b)):

$$E_{tt} = A_{\beta} \cdot \frac{kT}{q}.$$
 (6)

Thus, in this case the excess current has thermal activation dependence of J_{ot} (with activation energy $E_a = E_g/A_\beta$ and ideality factor $A_\beta = E/(kT/q) \sim 1.5$) at high temperatures $T \ge 200$ K, but the current acquires the tunneling character, when temperature is decreasing ($E_{tt0} = 0,016 \div$ 0,019 V) (Figure 8). The availability of both the tunnel and the thermoinjection parts on $J_{ott}(T)$ dependence allows characterizing the mechanism as thermotunneling.

Although the influence of thermotunneling current on the efficiency at RT and high temperature (concentrator application) can be neglected, the two-exponential model is more accurate for description of current transport in Ge subcell (formed by MOCVD) into the wide temperature range.

The nature of the excess currents cannot be explained by dislocations (as it is known for heterojunctions) because of high quality of the $GaInP_2/Ge$ interface (Figure 5). A possible reason for the excess currents was investigated in detail in our paper [30].

The dark IV curves at 100 K for the Ge "subcells" as well as for the GaInP/GaInAs/Ge MJ SC (Figure 9) clearly demonstrate an S-shape behavior, which is somewhat similar to Shockley's dynistor. The "set-on" voltage of the S-shape



FIGURE 8: Dependencies of the Ge "subcell" diode parameters of thermotunneling (E_{tt}, J_{ott}) current component and thermal potential (E_d) of the diffusion current on the value of inverse (a) and forward (b) absolute temperature.



 $\times 10^{-7}$ ×10⁻ 2 1.5 1 kHz-1 MHz Capacitance (F/cm²) $G/\omega (F/cm^2)$ 1 0.5 0 0 280 320 80 120 160 200 240 Temperature (K)

FIGURE 9: Dark IV curves recorded in a current control mode for the Ge "subcell" structure at 100 K (1) and 110 K (2), GaInP/GaAs/Ge triple-junction solar cell at 100 K (3).

FIGURE 10: *C-T-* ω and *G*/ ω -*T-* ω curves for GaInP/Ge subcell heterostructures [20].

increases with temperature decrease (from 0.78 V at 110 K to 0.9 V at 100 K for Ge "subcells").

The admittance spectroscopy was applied for study of the GaInP/Ge heterointerface. The "subcell" structure exhibits a

The experimental IV curves and the admittance spectroscopy data indicate [30] the presence of the undesirable potential barrier for majority of charge carriers in "subcell" heterostructures. The origin of this barrier formation could be related to the diffusion process at the GaInP/Ge interface. In fact, simultaneously with phosphorous (V group atom) diffusion from GaInP to the Ge wafer, which forms the n-type region of the p-n junction in p-Ge, gallium and indium diffusion to Ge also exists. As Ga and In are acceptor impurities for Ge, they could form a p-type region. The diffusion coefficient of phosphorous (~ 2.4×10^{-11} cm²/s) at growth temperature is much higher compared to that of both gallium ($\sim 3.5 \times 10^{-13} \text{ cm}^2/\text{s}$) and indium ($\sim 5.9 \times 10^{-13} \text{ cm}^2/\text{s}$) [31]. That causes deeper diffusion of phosphorous into the Ge wafer. However, the solubility limit of Ga in Ge (4.5 \times 10^{20} cm^{-3}) is significantly higher than that of phosphorous (5×10^{19}) cm⁻³ [32, 33]. Therefore a local concentration of Ga atoms diffused to Ge at the GaInP/Ge interface could be significantly higher compared to that of P atoms, which leads to a narrow region with a p-type doping domination. The inversion of the conduction type in this region near the interface causes the potential barrier for electrons, which can affect the charge carrier transport properties of a Ge solar cell.

The schematic band diagram with the potential barrier at the n-GaInP/n-Ge interface was proposed for the "subcell" structures (Figure 11). The behavior of the experimental IV curves and admittance spectra was reproduced by the numerical simulations using the proposed model with a 60 nm thick p-type Ge ($3 \times 10^{17} \text{ cm}^{-3}$) layer introduced at the interface between the (n)GaInP and (n)Ge, which forms the potential barrier of about 0.1 eV.

The dark IV curves at 100 K and 300 K for the heterostructures with ("subcell") and without potential barrier at the GaInP/Ge interface were simulated (Figure 12) using the AFORS-HET software [34].

The simulations have shown a good agreement with experimental data meaning that the presence of the potential barrier for electrons at the GaInP/Ge interface can explain the observed behavior at low temperatures for the "subcell" structures.

The undesirable potential barrier for majority of charge carriers at the n-GaInP/n-Ge heterointerface seems to be a general problem of the III-V/IV heteroepitaxial interfaces.

4. Summary

The review of important but little-investigated problems connected with III-V/Ge heterostructure in the GaInP/GaInAs/Ge multijunction solar cells grown by MOVPE is presented. Experimental in situ and ex situ investigation approaches are analyzed.

A combination of reflectance and reflectance anisotropy spectroscopy in situ methods has been applied for investigating III-V semiconductor growth on Ge substrates. By means of the in situ methods, the conditions for highquality material growth on Ge substrates have been found. The growth parameters of III-V buffer layer ($Ga_{0.99}In_{0.01}As$)



FIGURE 11: Schematic band diagram at equilibrium proposed for Ge "subcell" structures.



FIGURE 12: The simulated IV curves for the heterostructures with and without a potential barrier at the GaInP/Ge interface at 300 and 100 K.

for preventing autodoping by Ge of the photoactive MJ SC structure have been established.

Photovoltaic properties of n-GaInP/n-p Ge heterostructure, which is a narrow-band subcell of the triple-junction GaInP/GaInAs/Ge solar cell, have been investigated ex situ. It has been shown (from the temperature dependence of the dark IV curves, 77–330 K) that there is excess current (beside the diffusion one) in the Ge photovoltaic p-n junctions. The excess current has the tunneling or thermotunneling character. The values of the diode parameters for both current flow mechanisms have been determined.

The presence of the potential barrier at the n-GaInP/n-Ge interface has been confirmed by the admittance spectroscopy, low-temperature dark IV curve measurements, and numerical simulations. The origin of this barrier formation could be related to the parasitic diffusion of III group atoms into Ge substrate during heterostructure growth.

Conflict of Interests

The authors declare that there is no conflict of interests regarding the publication of this paper.

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