Copyright is owned by the Author of the thesis. Permission is given for a copy to be downloaded by an individual for the purpose of research and private study only. The thesis may not be reproduced elsewhere without the permission of the Author.

The Design and Construction of an

Electronically Beam Steered Phased Array Antenna.

A Thesis presented in partial fulfilment of the requirements for the degree of

Master of Science in Physics

at

Massey University New Zealand

Christopher James Lee 1996

Abstract.

The design and construction of a simple beam steered phased array antenna was undertaken to demonstrate the operational principles behind such devices. The antenna can be used as a receiver or transmitter, however power requirements dictated that the antenna be tested as a receiver. The design is modular to allow for redevelopment without complete reconstruction. The array is made up of the control module, voltage controlled attenuators and a phase shifting unit. The antenna consists of 16 quarter wave monopoles arranged in a 4X4 square array on an aluminium ground plane. Practical considerations lead to a carrier frequency of 200 MHz.

The heart of a phased array antenna is the phase shifting device. This device controls the direction in which the main radiation lobe propagates. Several phase shifting principles were investigated but time did not allow for an exhaustive investigation of every kind of phase shifter. Initially, a relatively new and novel approach was attempted. When this proved to be unachievable a more traditional (but far less elegant) method was used.

During the phase shifting process, the signal necessarily suffers attenuation as well as the designed phase shift, consequently it is necessary to tailor the signal amplitudes of each array element. The required amplitude control is achieved through the use of 16 voltage controlled attenuators.

A computer package is used to control the phase shifter and attenuators. The design of this package depends on only three factors. The first is the interface between the hardware and the computer (via a serial port in this case). The second factor is the type of control signal the phase shifter and attenuators respond to (in this case a dc voltage). The third factor is the range of voltage required for the phase shifter and attenuators so that their full range can be utilised. This is realised through the use of a microprocessor, a "sample and hold" circuit and several D/A converters.

The antenna and computer control package are essentially independent of each other. If an 8 bit digital phase shifter were to be employed later, the hardware could be used to control this with minimal alteration. In this case the advantage of a modular design is apparent. Various parts of the device can be incrementally improved without alteration to the remaining system. Radical change can be accommodated with minimal adjustments.

Acknowledgements.

I wish to thank my supervisor, Assoc. Prof. Neil Pinder, for his advice, encouragement and interest. I would also like to thank Dr. Anthony Burrell for his assistance in chemistry. Thanks also to other staff who gave advice and interest. Finally, I would like to thank my parents and Donna for their help and encouragement. This work was made possible through the financial assistance of the Telecom Users Association of New Zealand.

Contents

	FRACT.	
ACKI	NOWLEDGEMENTS.	III
	Chapter 1 Introduction.	
1.2.	Introduction to Phased Array Antenna. Elementary theory. Coupling Between Antenna Elements.	3
	Chapter 2 Methods of Phase Shifting.	
2.2. 2.3. 2.4. 2.5.	JINDALEE B PHASED ARRAY ANTENNA. DIGITAL PHASE SHIFTERS. PHASE LOCKED LOOP PHASE SHIFTERS. DISTRIBUTED AMPLFIER PHASE SHIFTER. R - C CIRCUIT PHASE SHIFTERS. WAVE GUIDE PHASE SHIFTER.	.9 10 12 14
	Chapter 3 The Phase Shifter.	
3.2.	Design of a Phase Shifter. Producing a BST Phase Shifter. PIN Diode Phase Shifter.	
	Chapter 4 The Attenuator.	
		32 33

Chapter 5. Control Software.

5.1. INTRODUCTION.	00
5.2. Computer Control Program.	37
5.3. Data Reception Program.	
5.4. HARDWARE INTERFACE PROGRAM.	
O. I. TWILDTWILE INTERNACE THOSITIME III.	
Chapter 6	
Hardware.	
6.1. The Digital Board.	18
6.2. THE ANALOG BOARD.	
6.3. Hardware Performance.	
O.S. MAHDWARE PERFORMANCE.	50
Chapter 7	
*	
Antenna Design.	51
ANTENNA DESIGN.	
Chapter 8	
SIGNAL DETECTION.	56
Chapter 0	
Chapter 9	
Testing Methodology.	
9.1. The Testing Range.	58
9.2. Data Collection.	
Chamter 10	
Chapter 10	
Test Results.	
10.1. Measured Results.	61
10.2. A SIMPLE ARRAY SIMULATION: "THE STANDARD SIMULATION"	
10.3. Uncoupled Half Wave Dipole Simulation.	
10.4. Fully Coupled Dipole Simulation.	
TO. I. I OLLI OGGILLO DII OLL OIMOLATION.	

Chapter 11 Conclusion.

	. Conclusion	
	Appendicies	
	MUTUAL COUPLING. PRODUCTION OF BST POWDER.	
Вівц	IOGRAPHY	82

Table of Figures and Tables

Figure	1.1.	BSB antenna	2
Figure	1.2.	Two isotropic sources	
Figure	1.3.	Coordinate system	
Figure	2.1.	Digital Phase Shifter	
Figure		PLL	
Figure		PLL with variable phase	11
Figure		PLL as a modulator	
Figure		Artificial transmission line	
Figure		Distributed amplifier phase shifter	
Figure		Improved distributed amplifier phase shifter	
Figure		Phase shift of an R-C circuit	
Figure		Amplitude variation of an R-C circuit	
Figure		Cascaded network	16
Figure		Wave guide phase shifter	
Figure		Transmission line	
Figure		Artificial transmission line	
Figure		ATL phase shifter	
Figure		BST production diagram	
Figure		Thermocouple calibration	
Figure		Hot press Mk. 1.	
Figure		Sintering temperature	
Figure		Hot press Mk. 2.	
Figure		Sintering temperature	
Figure		PIN diode	
Figure		Phase shifter Mk. 1	
Figure		Antenna geometry	20
Figure	3.12.	Phase shifte Mk. 2	20
Figure	3.13a.	Predicted phase/amplitude shift for different resistances	20
Г:			
Figure	5.13D.	Predicted phase/amplitude shift for different	
F!	2 1 4	resistances	30
Figure		Measured phase shift	
Figure		Photograph of the phase shifter	
Figure		PIN diode attenuator	
Figure		Practical PIN diode attenuator	
Figure		Attenuator performance curves	
Figure		Reference voltage dependence	
Figure		Photograph of the attenuators	
Figure		Control system overview	
Figure		Computer program flow chart	
Figure		Coordinate system	
Figure		Latch	
Figure		Control hardware	
Figure		D/A converter	
Figure		Typical attenuator curve	
Figure	6.3b.	Phase shift profile	50

Figure 6.4a.	D/A converter performance	51
Figure 6.4b.	D/A converter performance	51
Figure 6.5.	Photograph of digital board	52
Figure 6.6.	Photograph of analog board	52
Figure 7.1.	Ground plane of antenna	53
Figure 7.2.	Monopole attachment to the ground	54
Figure 7.3.	Photograph of the antenna	54
Figure 7.4.	Photograph of monopole attachment	55
Figure 8.1.	AM demodulator	
Figure 8.2.	Signal detection system	
Figure 8.3.	Photograph of the radio	57
Figure 9.1.	The testing range	
Figure 9.2.	The transmitter	
Figure 9.3.	The receiving station	60
Figure 9.4.	Receiving station viewed from the transmitter	.60
Figure 10.1.	Measured results	
Figure 10.2.	Measured results	
Figure 10.3.	Measured results	
Figure 10.4.	Measured results	
Figure 10.5.	Measured results	
Figure 10.6.	Measured results	
Figure 10.7.	Standard simulation	
Figure 10.8.	Standard simulation	
Figure 10.9.	Uncoupled simulation	
Figure 10.10.	Uncoupled simulation	
Figure 10.11.	Uncoupled simulation	
Figure 10.12.	Uncoupled simulation	
Figure 10.13.	Uncoupled simulation	
Figure 10.14.	Uncoupled simulation	
Figure 10.15.	Coupled simulation	
Figure 10.16.	Coupled simulation	71
Figure 10.17.	Coupled simulation	
Figure 10.18.	Coupled simulation	
Figure 10.19.	Coupled simulation	
Figure 10.20.	Coupled simulation	
Figure a.1.	Calculating the mutual coupling	
Figure a.2.	Coordinate system	/9
Table 5.1.	Binary numbers	
Table 10.1.	Phase measurements	01