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III–V nanowire arrays: growth and light interaction

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Abstract

Semiconductor nanowire arrays are reproducible and rational platforms for the realization of high performing designs of light emitting diodes and photovoltaic devices. In this paper we present an overview of the growth challenges of III–V nanowire arrays obtained by molecular beam epitaxy and the design of III–V nanowire arrays on silicon for solar cells. While InAs tends to grow in a relatively straightforward manner on patterned (111)Si substrates, GaAs nanowires remain more challenging; success depends on the cleaning steps, annealing procedure, pattern design and mask thickness. Nanowire arrays might also be used for next generation solar cells. We discuss the photonic effects derived from the vertical configuration of nanowires standing on a substrate and how these are beneficial for photovoltaics. Finally, due to the special interaction of light with standing nanowires we also show that the Raman scattering properties of standing nanowires are modified. This result is important for fundamental studies on the structural and functional properties of nanowires.

(Some figures may appear in colour only in the online journal)

1. Introduction

Semiconductor nanowires have been the object of intensive study in the last few years. Their longitudinal geometry and small footprint provide numerous advantages for optoelectronic and energy harvesting and storage devices [1–5]. Additionally, they can be the base for more complex nanostructures such as axial/radial heterostructures, nanotrees, nanocrosses, nanomembranes and nanocubes/diamonds [6–11]. Recently, it has been shown that the relatively small cross-section of the nanowires provides a path for the integration of mismatched materials within a nanowire [12, 13] and also to grow nanowires of a certain material on mismatched substrates [14]. Novel materials combinations have been achieved such as the growth of Si segments in a III–V nanowire [15–17] and III–V nanowires on silicon for high performance electronics [18, 19].

The integration of III–V nanowires on silicon is particularly attractive in the area of photonics and photovoltaics [20]. For example, a GaAs nanowire-based solar cell could be grown on a silicon pn junction forming a dual-junction solar

cell [21]. Here, nanowires would not only provide the active material for the top cell; they could intrinsically provide an antireflective layer. Several works have shown that the arrangement of nanowires in a nanowire-based solar cell is extremely important for both the collection of light in the wires and for an isotropic reduction of the reflectivity [22, 23]. Recently, studies on single and multiple nanowire-based solar cells have shown photonic effects which lead to a several-fold broadband increased absorption in each nanowire. In particular, the Lund group has obtained a conversion efficiency of 13.8% under 1.5 AM illumination conditions by optimizing the nanowire array configuration [23]. Along the same lines, we have recently shown that light absorption in a single nanowire solar cell can be enhanced more than one order of magnitude due to a self-concentrating effect [24]. To the best of our knowledge, the effect on the nanowire dimensions and density on a substrate have not been investigated in detail yet.

In this work, we present some recent results on the gold-free growth of III–V nanowire arrays and on the interaction of light with the nanowires of the arrays. We highlight some of the existing challenges for the understanding and for the reproducible growth of nanowires in the array form. Next, we turn to the application of nanowire

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arrays. We start presenting finite-difference-time-domain simulations on the light absorption of III–V nanowire arrays as a function of the inter-wire distance and show some results on Raman spectroscopy of nanowires as a function of the nanowire spacing. It is particularly interesting that the self-concentration effect in nanowire results in a variation of the Raman selection rules and opens up possibilities in the area of light scattering with nanowire-based nanostructures.

2. Growth of III–V arrays on silicon

Organized growth of nanostructures on a substrate is the first step towards the rational and parallel fabrication of devices. In addition, by growing nanowires on precise locations and with controlled positioning should be the ideal setting for studying their growth mechanisms. The most common way of organizing growth of nanowires on a substrate is to pattern a substrate either with gold nano-droplets or with nanoscale holes on a silicon dioxide mask. Nanoscale openings in a silicon dioxide mask provide a preferential site for nanowire for catalyst-free growth methods. However, given the same growth conditions, growth dynamics of nanowires depends in a high degree on their dimensions and center-to-center distance [25–29]. The nanoscale features for nanowire growth can be defined on a substrate by electron beam, nanosphere, phase-shift or nano-imprint lithography [30–34].

III–V nanowires grow mostly along the (111)B direction [35, 36]. This represents a challenge for growing on (111) silicon, as the polarity at the interface needs to be type B for the nanowires to grow perpendicularly to the substrate [37–42]. It is interesting to compare the catalyst-free growth of InAs and GaAs on silicon: while GaAs nanowires grow assisted by a Ga droplet at their tips, InAs nanowires seem to grow without the assistance of an In droplet by selective area epitaxy [43–45]. At the same time, InAs nanowire arrays are relatively straightforward to grow, while GaAs encounter many more challenges [46–49].

Nanowire arrays were grown by molecular beam epitaxy (MBE) in a DCA P600 system without the use of a predefined metal particle. In order to localize the growth on the substrate, thermally oxidized silicon wafers were patterned with nanoscale holes. The holes were defined by electron beam lithography and wet-chemical etching based on 7:1 buffered hydrofluoric acid solution. In order to ensure an oxide-free surface in the holes, the wafers were shortly dipped in the same HF solution prior to growth. They were subsequently kept in isopropanol until the loading in the MBE to preserve the cleanliness of the substrate. The patterns consisted in a square arrangement of holes of nominal sizes ranging between 90 and 150 nm. The inter-hole distance or pitch was varied between 200 and 2000 nm on the same substrate. The oxide thickness was 20 nm for the InAs growth and was varied between 5 and 30 nm for the GaAs.

2.1. InAs

InAs nanowires on patterned silicon substrates were grown at a nominal In growth rate of 0.2 \AA s^{-1} , As_4 partial pressure

of 6.0×10^{-6} Torr (BEP: beam equivalent pressure) and substrate temperature of 500°C for 1 h. Scanning electron micrographs (SEM) of InAs nanowire arrays grown on a Si(111) substrate are shown in figures 1(a)–(g). In the context of InAs nanowire growth, it is well known that a tradeoff exists between the growth selectivity and the final length of the nanostructures [46]: this has recently also been observed in the case of InAs V-shaped membranes [50]. The selectivity of growth in the openings of the substrates improves with the temperature until parasitic growth on SiO_2 completely disappears. Indeed, the sticking probability of indium ad-atoms on oxide decreases by increasing the temperature leading to the nucleation of nanowires only in the predefined holes. However, the higher is the temperature the higher is desorption of the deposited material and thus the shorter the structures grown. This means that the ideal growth conditions represent a compromise between optimal selectivity and maximum length, and these are the ones we adopted in this study. Within these growth conditions we could achieve high selectivity and aspect ratio, which were needed for the optical measurements. The nanowire growth yield, here defined as number of openings nucleating nanowires divided by the total number of openings in the pattern, was 92%.

The sample reported in figure 1 shows a significant size variation of the nanowires as a function of the inter-hole distance (pitch). In particular the diameter and length saturates around a value of 155 and 1480 nm for pitch 800 nm. A similar behavior has been observed by other groups in the case of ordered growth of InAs nanowires [44, 46, 51] and nanomembranes [50]. The dependence of the nanowire length as a function of the pitch can be explained by the existence of two separate growth regimes: (i) a competitive growth regime with shorter nanowires for narrow inter-holes distances and (ii) a diffusion-limited or independent growth regime for wider pitches. The indium surface diffusion length on SiO_2 (λ_{SiO_2}) determines the switch between the two regimes. Indeed, λ_{SiO_2} limits the surface collection area i.e. the sample area from which each wire can collect the In species diffusing on the oxide. For a small nanowire spacing (pitch $< 2\lambda_{\text{SiO}_2}$), the indium ad-atoms are shared between the wires. By increasing the spacing, the surface collection area available exclusively to each nanowire increases, resulting in a linear increase of the growth rate. In the opposite limit of a large spacing (pitch $> 2\lambda_{\text{SiO}_2}$) the wires can be treated as independent isolated islands. In this regime the surface collection area cannot increase anymore and the growth rate is no longer dependent on the pitch. The calculated volume of the nanowires depends on the pitch and it can be seen that it saturates when the growth reaches the diffusion-limited regime (see figure 1(e)). For completeness, in figure 1(f) we show the equivalent layer thickness, defined as the thickness of the equivalent layer obtained with the same InAs volume. This will be useful for the analysis of the Raman scattering data in the arrays, as discussed later in this work.

2.2. GaAs

We turn now to the study of Ga-assisted growth of GaAs nanowires on SiO_2/Si patterned substrates. In this

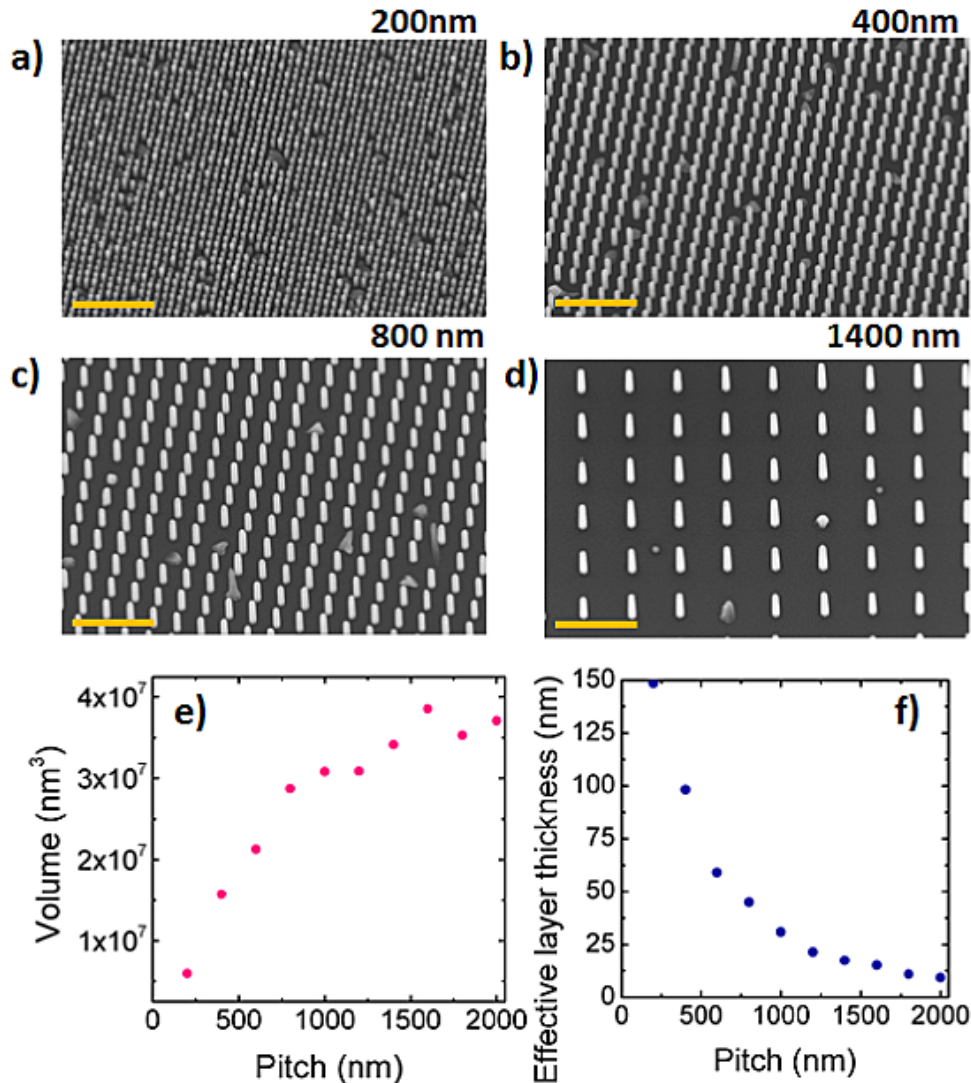


Figure 1. (a)–(d) Scanning electron micrographs of InAs nanowires obtained on a patterned Si substrate for different pitch (scale is 2 μm), (e) nanowire volume (V_{NW}) and (f) effective layer thickness as a function of the pitch.

case, a nanoscale Ga droplet gathers preferentially As_4 molecules to drive the GaAs growth. The Ga droplet is continuously replenished by the diffusion of Ga-adatoms from the facets and surface for ensuring the continuous nanowire growth [52]. Recently, it has been shown that the substrate preparation and the pre-growth conditioning are key to achieve high-yield growth of GaAs nanowire arrays by the Ga-assisted method [49]. We have investigated two of these main factors, namely the degassing of the sample prior to growth and the thickness of the growth mask layer. Figure 2 shows a sequence of 20° tilted SEM images of the nanowire arrays obtained in this study. The GaAs nanowires grown on patterned Si(111) substrates, with a nominal Ga growth rate of 1 \AA s^{-1} , As_4 partial pressure of 2×10^{-6} Torr, substrate temperature of $615 \text{ }^\circ\text{C}$, and with 7 rpm substrate rotation. In all cases the samples have been degassed at $600 \text{ }^\circ\text{C}$ for 2 h and they have been all grown with the same growth recipe. Figure 2(a) shows the results of the first growth: the yield of vertical wires is low and parasitic bulk growth occurs. The sample reported in figure 2(b) has been grown after a further

heating at $770 \text{ }^\circ\text{C}$ for 30 min in order to remove any possible contaminants of the surface: the density of vertical wires is significantly improved, thus demonstrating that the sample preparation and the cleaning of the surface are decisive steps in obtaining high quality nanowire arrays.

Subsequently, we evaluated the influence of thickness of the oxide layer on nanowire growth yield. The thickness of the oxide layer was determined with spectroscopic ellipsometry prior to growth. Growths of GaAs nanowires on arrays with SiO_2 layers of 13, 18 and 21 nm thickness are reported in figures 2(c)–(i). As also found by Plissard *et al* [49], by decreasing the oxide thickness the yield improves to dramatically drop again at thinner thicknesses. We found an optimal oxide thickness of 18 nm, which is not affected by the pitch of the array. The geometry of the pattern is found to affect in itself the yield of vertical wires, an observation which needs further investigation. The maximum yield (65%) is reached for pitch = 400 nm and opening size = 100 nm (figure 2(c)).

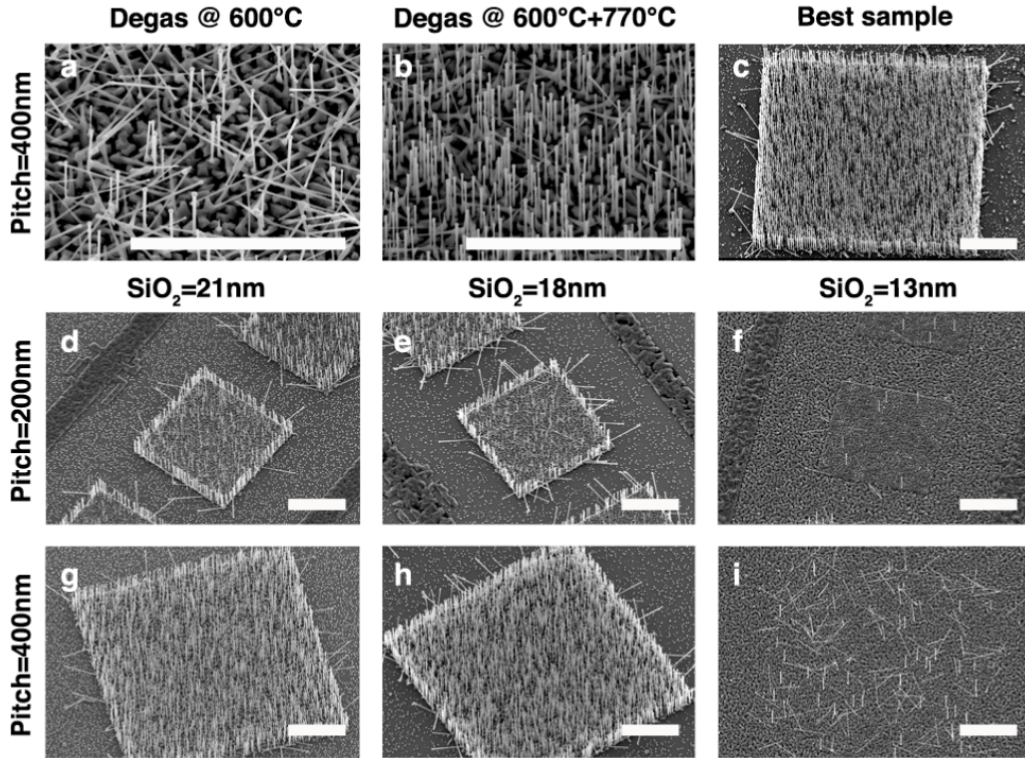


Figure 2. Scanning electron micrographs of GaAs nanowires obtained on patterned (111)Si substrates as a function of the oxide thickness, pitch and substrate preparation conditions (see text).

Further optimization can be achieved by studying several other parameters like the substrate temperature, the V/III ratio and the catalyst pre-deposition. However we have encountered several issues in the long-term reproducibility of the samples. In particular, we have recently observed that small changes in the surface roughness of the substrates results in extremely different nanowire density and orientation on a silicon substrate [53]. We believe this kind of variation from batch to batch might be one of the sources for lack of reproducibility for the Ga-assisted growth of GaAs nanowire arrays.

3. Light interaction with III–V nanowire arrays

We turn now our attention towards understanding of how a planar light wave incident perpendicularly to the substrate interacts with standing nanowires. As mentioned above, we have recently shown a self-concentration effect occurring in single nanowires standing on a substrate [24]. Here below we present calculations on light absorption in standing nanowires, and we show how this changes in an array as a function of the inter-wire distance. Finally, we show how this concentration effect affects the Raman spectra of nanowires.

3.1. Light absorption in standing nanowires

Light absorption in standing GaAs nanowires of different diameters surrounded by vacuum was calculated by solving the wave equation for an incident radiation propagating along

the nanowire axis. For this, we use finite-difference-time-domain simulations [54], similar to what the Witzigmann group has been realized recently [55]. We start by presenting the propagation of a planar wave impinging vertically on a $2 \mu\text{m}$ long GaAs nanowire. We have calculated the spectral and diameter dependence of the absorption rate. The enhancement in absorption for a single standing nanowire is defined as the ratio between the absorbed power and the power of the incident electromagnetic radiation on the nanowire surface ($S = \pi r^2$, where r is the nanowire radius):

$$\text{Absorption enhancement} = \frac{P_{\text{absorbed}}}{P_{\text{inc}}S} \quad (1)$$

where P_{absorbed} is the power absorbed by the nanowire and P_{inc} is the incident power surface density. Clearly, this efficiency will be larger than 100% in the cases where the absorption cross-section is larger than the *apparent* one defined by the physical limits of the nanowire. The spectral dependence of the absorption enhancement as a function of the nanowire diameter is plotted in figure 3(a). The color scale denotes the value of the enhancement. The graph is composed of two main branches of enhanced absorption. The first one, defined by an enhancement of the spectral absorption close to 70 fold, corresponds to nanowires between 50 and 150 nm. It is important to note that the absorption enhancement is always higher than 10 in the 300–900 nm range, where GaAs absorbs at room temperature. The second branch corresponds to an enhancement of 10 fold in the absorption for diameters above 150 nm. These both branches correspond to resonances for which the light is better trapped in the nanowire structure.

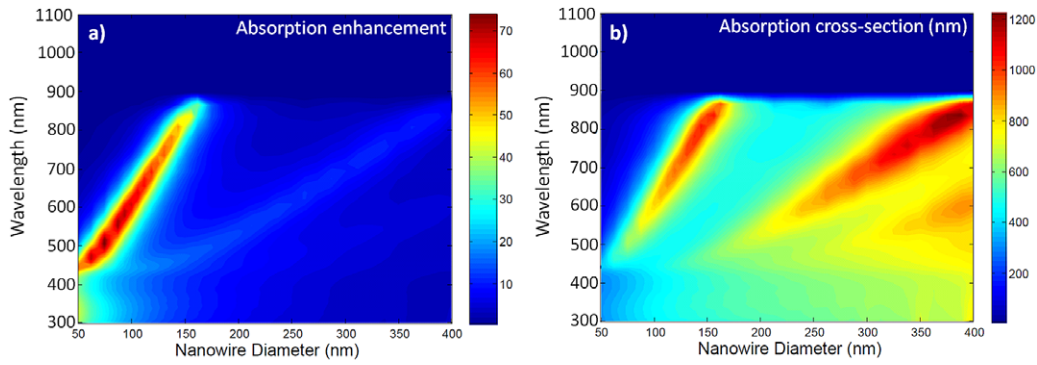


Figure 3. (a) Absorption enhancement and (b) diameter of the absorption cross-section of single vertical GaAs nanowires standing on a Si substrate as a function of the diameter.

Along these branches, the resonant wavelength increases with the nanowire diameter. It is interesting to note that there is at least a four-fold increase of the absorption in the whole spectrum even for larger nanowires (diameter above 100 nm). This represents a significant broadband absorption increase, difficult to observe in lying nanowires [56–59].

A way of capturing the origin of the dramatic increase in light absorption, we proceed by calculating the absorption cross-section, defined by the disk of radius r^* corresponding to the equivalent area under which the absorbed power density $P_{\text{abs}}/\pi r^{*2}$ is equal to the incident surface power density, P_{inc} :

$$r^* = \sqrt{\frac{P_{\text{abs}}}{\pi P_{\text{inc}}}}. \quad (2)$$

In figure 3(b) we plot the spectral dependence of the capture cross-section diameter ($2r^*$) as a function of the nanowire diameter. In a thin film the absorption cross-section has identical dimensions to its size, while in the case of nanoscale objects the cross-section can be quite larger [60, 61]. We find cross-sections as large as 1200 nm for the longest wavelengths (830 nm) and largest nanowires. Interestingly, the plot of the cross-sections diameter amplifies the branches found in the absorption efficiency mapping. One of the consequences of the large absorption cross-section in standing nanowires is that in a solar cell they should be disposed at an optimized distance. At the same time, the self-concentrating effect should result in an increase of the device efficiency and provides a path for surpassing the efficiency limits posed by Shockley and Queisser in 1961 as explained in the [24, 62].

We direct now our discussion towards the interaction of light in nanowire arrays as a function of the inter-wire distance or pitch. We have extended our calculations to nanowire forming square arrays on silicon and exhibiting a diameter of 150 nm. For simplicity in the calculations we have assumed a square array. In a real nanowire-based solar cell device, the arrangement should be optimized so that the nanowires are separated by the optimum pitch and avoid any possible diffraction effects [63, 64]. Figure 4(a) depicts the square of the electric field of light propagating in the nanowire (which is proportional to the light intensity), weighted by the solar spectrum 1.5 AM as a function of the pitch. The smallest pitch is 150 nm, which corresponds to the thin film case.

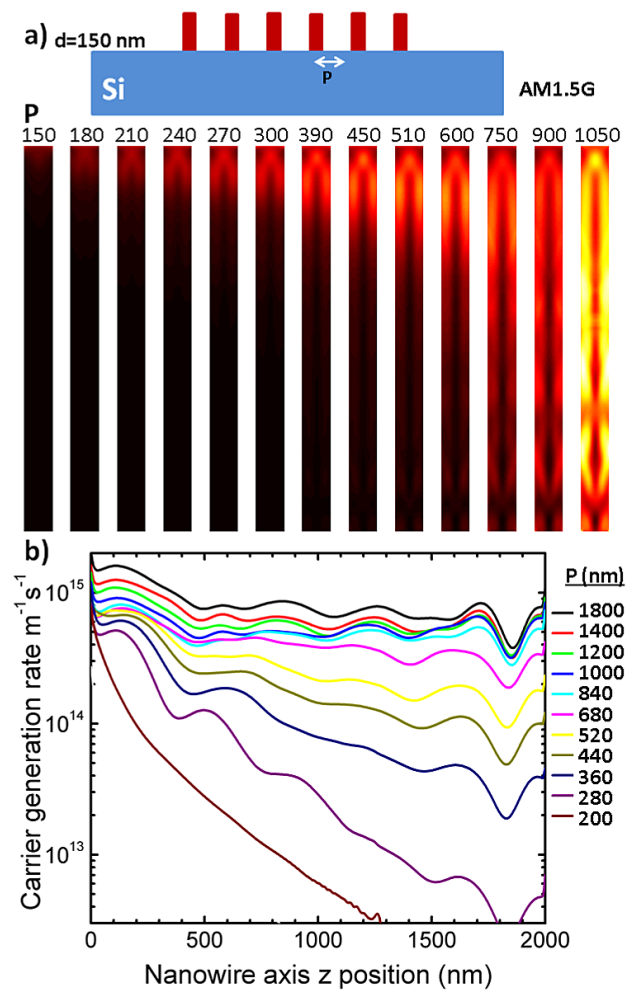


Figure 4. Calculations of light absorption in GaAs nanowires in an array in AM 1.5 conditions. (a) Average of the square of the electric field in the nanowire for different pitch, (b) profile of the carrier generation along the nanowire axis as a function of the pitch.

In this case, the light intensity decays exponentially at the top of the nanowire/thin film. By separating the nanowires the light starts to be present to a deeper length within the nanowire. For a pitch of 750 nm a significant amount of light is able to propagate till the bottom of the nanowire.

The intensity of light along the nanowire axis increases by further separating the nanowires. One should note that the distribution of the intensity along the nanowire seems to exhibit some nodes, showing that the absorption enhancement corresponds to a resonance and that the nanowire acts as a sort of cavity. From the distribution of the square of the electric field along the nanowire we calculate the overall carrier generation rate along the nanowire axis. The results are shown in figure 4(b). In agreement with the distribution of the light intensity, the generation of carriers increases and homogenizes along the nanowire axis when the pitch of the nanowire array increases. Interestingly, the carrier generation does not follow the exponential decay from Lambert–Beer law but a relatively homogeneous absorption along the nanowire axis. This distribution of light absorption is at the same time more beneficial for carrier extraction in the radial pn junction design. Along with the self-concentration effect, the generation of carriers all along the nanowire axis could lead to devices with a higher V_{OC} and a smaller series resistance. Overall this would result in a higher photo-conversion efficiency.

3.2. Raman scattering in standing InAs nanowire arrays

Backscattering Raman measurements were conducted using the 488 nm line of an Ar/Kr⁺ gas laser focused on the InAs arrays, described in section 2.1, through a 0.75 N.A. microscope objective. The total laser power for the experiments was kept at 300 μ W. The Raman scattered light was projected on the entrance slit of a triple grating spectrometer and collected by means of a charge coupled device. Experiments were conducted on a series of as-grown nanowires obtained during a single MBE growth run on a sample with patterned fields of varying inter-wire spacing. Figure 5(a) shows a typical Raman spectrum of the InAs nanowires obtained under such conditions. One can observe two modes at 211 and 236 cm^{-1} , reported respectively as TO and LO for InAs. Still, the exact position of these modes depends on the existence of zinc-blende, wurtzite and/or twinning defects [65–68]. We also observe a mode at 520 cm^{-1} resulting from the silicon substrate. Figure 5(b) shows the absolute intensities of these modes in dependence of nanowire array spacing. One can see that while the absolute intensities of InAs related modes decrease with nanowire spacing the relative intensity of the silicon mode is increasing.

Figure 5(c) depicts the top InAs surface coverage and equivalent thin film volume normalized to the values extrapolated for 0 nm pitch (thin film case). As seen in figure 5(b), for small pitch distances, both TO and LO decrease in intensity, which is explained by the loss of InAs scattering cross-section because of the lower nanowire density in the probing area. Interestingly, for pitches larger than the laser spot diameter (around 800 nm), the LO intensity reaches a plateau and even increases slightly with pitch distance, thus indicating that an effect beyond the purely geometric material decrease plays a role. Actually, the signal seems to correlate with the volume of a single nanowire from figure 5(c), fact that suggests a light coupling effect that allows the light to probe

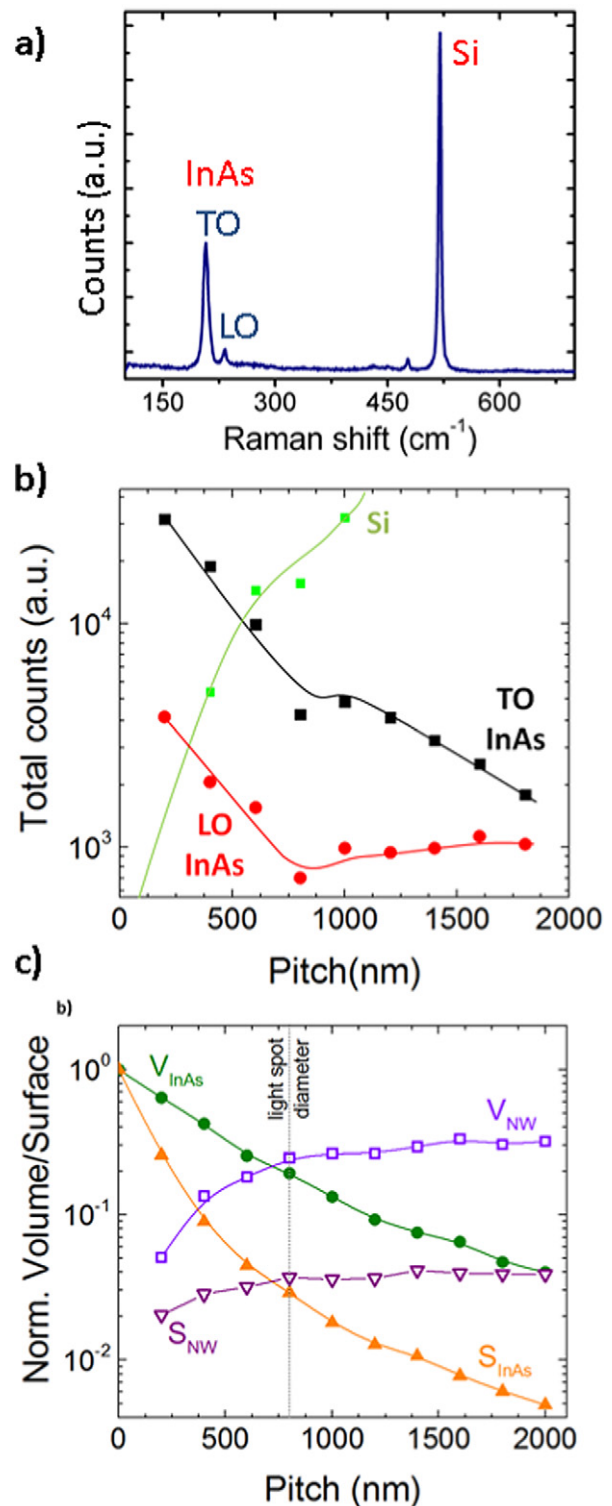


Figure 5. Raman spectroscopy in InAs nanowire arrays. (a) Typical spectrum of the InAs nanowires on Si, (b) evolution of the intensities of the peaks corresponding to InAs and Si and (c) normalized values of the volume of a single nanowire (V_{NW}), equivalent thin film thickness of the array (V_{InAs}) as well as the surface covering of the nanowire array (S_{InAs}) and single nanowire (S_{NW}).

the whole nanowire volume even though the strong absorption of InAs at 488 nm (absorption length is about 8 nm). In Raman spectroscopy, not only enhancement for incident light

is important, but also how the scattered light interacts with the nanowire and should be taken into account [69–71]. It has been shown in previous works that light extraction efficiency in an array can be explained by the Maxwell–Garnett effective medium approximation [72]. Under such framework, arrays with smaller nanowire density present an effective refractive index closer to that of air, thus enabling a better light extraction. However, the light resonances at the nanoscale should be also taken into account to have a full understanding of the Raman signal.

Similarly, the TO intensity also shows a change in the intensity decay as a function of the pitch for pitch distances around 800 nm. However, contrary to the LO mode, the TO intensity decreases back again for large pitch distances (larger than 1200 nm). In wurtzite material, it has been observed by Raman scattering that light coupling enables light entering the nanowires to a considerable extent through their side facets even for normal incidence and small bandgap material [73, 74]. Because the Raman signal is directly related to the electric field direction through the symmetry of the vibrational mode in question, the change in the field direction at the nanoscale can completely modify the Raman selection rules for different modes from the macroscopic point of view. Our results thus seem to indicate a similar effect, where the electromagnetic resonances in the nanowire favor symmetry conditions for extraction efficiency of the LO scattered light. This opens the way for fundamental studies in the area of LO phonon–electron/hole scattering in doped nanowires [75, 76].

4. Conclusions

In conclusion we have demonstrated growth of self-catalyzed InAs and GaAs nanowire arrays on a (111)Si substrate and explained some of the challenges associated with obtaining high yield. We have also explored the concept of ideal nanowire array configuration for optimal light absorption. In particular, we have shown how planar light waves incident perpendicularly to the substrate can exhibit resonances for certain nanowire diameters. This results in a self-concentration effect and a several-fold enhancement in the absorption rate. This phenomenon can be very useful in photovoltaic and light emitting devices. Finally, we show how the occurrence of absorption resonances in nanowires can lead to an enhancement of the LO phonon scattering, which can be of fundamental importance for the study of structural and functional properties of nanowires by Raman spectroscopy.

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