

# On the Interplay Between Microstructure and Interfaces in High-Efficiency Microcrystalline Silicon Solar Cells

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**Abstract**—This paper gives new insights into the role of both the microstructure and the interfaces in microcrystalline silicon ( $\mu\text{c-Si}$ ) single-junction solar cells. A 3-D tomographic reconstruction of a  $\mu\text{c-Si}$  solar cell reveals the 2-D nature of the porous zones, which can be present within the absorber layer. Tomography thus appears as a valuable technique to provide insights into the  $\mu\text{c-Si}$  microstructure. Variable illumination measurements enable to study the negative impact of such porous zones on solar cells performance. The influence of such defective material can be mitigated by suitable cell design, as discussed here. Finally, a hydrogen plasma cell post-deposition treatment is demonstrated to improve solar cells performance, especially on rough superstrates, enabling us to reach an outstanding 10.9% efficiency microcrystalline single-junction solar cell.

**Index Terms**—High efficiency, microcrystalline silicon ( $\mu\text{c-Si}$ ), thin-film solar cells, tomography.

## I. INTRODUCTION

MICROCRYSTALLINE silicon ( $\mu\text{c-Si}$ ) is a material which is now commonly used in thin-film photovoltaics and which could lead, with amorphous silicon in the tandem configuration, to module production costs as low as 0.5  $\$/W_p$  in a close future, even at moderate production volume [1]. Indeed, its good stability against light soaking and its extended absorption in the 800–1100-nm range of the solar spectrum com-

pared with amorphous silicon makes it an excellent candidate for silicon-based multijunctions [2], [3]. Moreover, as a consequence of the abundance and nontoxicity of silicon, scaling up to the terawatt level will be possible in the near future. However, while initial efficiencies as high as 16.3% have been reported for thin-film silicon triple-junctions cells [4], the stabilized efficiency of modules and thus of the  $\mu\text{c-Si}$  subcell has to be further improved for this technology to play a major role in clean and sustainable energy production.

Here, we focus on single-junction p-i-n  $\mu\text{c-Si}$  solar cells, where the intrinsic absorber layer quality is known to be crucial to obtain high-efficiency devices [5]–[7]. We notably investigate the role of porous material areas (often called cracks) that appear when challenging superstrate morphologies and/or inappropriate deposition conditions are used [8], [9]. We present an innovative 3-D tomographical reconstruction of a solar cell to evidence the 2-D network of these porous areas of defective material within the absorber layer when grown on highly textured superstrates.

Silicon-suboxide-doped layers have been proposed as excellent candidates to mitigate the effect of those localized porous zones [10]–[12]. We study here the role of these novel interfaces through variable illumination measurements (VIM) [13] and demonstrate the quenching of these defective areas when adequate interfaces are used. In addition, we present a post-deposition hydrogen plasma treatment allowing a significant improvement of the conversion efficiency, particularly when the solar cells are deposited on rough superstrates, and even when the best cell design is used. These new findings led us to outstanding single-junction microcrystalline devices, with a very high conversion efficiency of 10.9% (0.25  $\text{cm}^2$ ) and 10.4% (1  $\text{cm}^2$ ).

## II. CELL FABRICATION AND CHARACTERIZATION

Boron-doped zinc oxide (ZnO) obtained by low-pressure chemical vapor deposition (LPCVD) was used as front electrode directly deposited on top of AF45 Schott glass. The self-textured pyramidal surface morphology obtained with this LPCVD ZnO allows an efficient light trapping and, thus, a reduced thickness of the absorber layer [14]. The nominal thickness of the front ZnO was 4.5  $\mu\text{m}$  (Z5), with a typical sheet resistance of 9  $\Omega/\square$ . The as-grown surface texture of LPCVD ZnO is excellent for efficient light harvesting but can be extremely challenging for the growth of high quality  $\mu\text{c-Si}$ , resulting in decreased

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electrical performance [15]–[17] and long-term stability issues when porous zones are present within the bulk material [18]–[20]. In order to overcome these effects, an argon-based plasma surface treatment can be used to improve the electrical performance ( $V_{oc}$  and FF) by smoothening the front electrode morphology, however, at the cost of current density loss [21]. Optimum treatment time on Z5 for single-junction microcrystalline silicon solar cell was determined to be about 45 min.

Single-junction solar cells were deposited by plasma-enhanced chemical vapor deposition (PECVD) at a superstrate temperature of 200 °C and a pressure of 0.7 mbar. We varied both silane dilution ( $[SiH_4]/[H_2] = 4\text{--}5\%$ ) and input power ( $0.04\text{--}0.06\text{ W/cm}^2$ ) during the deposition of the intrinsic layer, in order to control the crystallinity (as measured by Raman spectroscopy) at the doped layers interfaces and within the absorber layer (typically 55–60%). The excitation frequency was set at 70 MHz and the deposition rate was  $1.5\text{--}1.8\text{ \AA/s}$ , leading to typical initial cell efficiency of 9.5–10% for adequate cell design.

LPCVD ZnO films with thicknesses ranging between 2.5 and  $5\text{ }\mu\text{m}$  were used as back contact, depending on the experiment. The cells were patterned by liftoff and dry etching into  $0.25\text{--}1\text{-cm}^2$  areas. A dual-lamp sun simulator from Wacom under standard operating conditions (AM1.5g,  $1000\text{ W/m}^2$ , 25 °C) was used for the measurements of the  $I\text{--}V$  curves of solar cells, using gray filters for the VIM [13]. From these measurements, the open-circuit voltage ( $V_{oc}$ ) and the fill factor (FF) were deduced. The short-circuit density  $J_{sc}$  was calculated from external quantum efficiency (EQE) measurement, effectuated with a white dielectric back reflector. No antireflection coating was applied at the air–glass interface, except for the record devices in Fig. 6.

### III. TOMOGRAPHIC RECONSTRUCTION OF SOLAR CELLS

A solar cell deposited on an as-grown rough superstrate (Z5) was investigated with focused ion beam (FIB) “slice and view” tomography [22], in order to further investigate  $\mu\text{c-Si}$  microstructure on such a challenging superstrate and, more particularly, the creation of porous zones, which are commonly called “cracks.” By milling to obtain a smooth cross section of the sample with an FIB column, then imaging the milled face with a scanning electron microscope (SEM) column, and after successively FIB milling back in steps of some nanometers, followed each time by SEM imaging, a 3-D dataset was created. A voxel size (i.e., 3-D pixel size) of  $7 \times 7 \times 7\text{ nm}$  was used, recording the SEM images using an in-lens backscattered electron detector that gives mass contrast for the different layers (voids as dark regions), and grain contrast in the ZnO, as seen in Fig. 1(a). It also minimizes the effect of “curtaining” from nanometer-scale undulations on the milled surface.

When considering a plane at the Z5/Si interface [see the green arrow in Fig. 1(a)], perpendicular to the  $i$ -layer growth direction, the 2-D network formed by the porous material can be seen directly.

Pseudocolor tomograms in Fig. 1(b) reveal the microstructure of cells codeposited on rough superstrate (Z5) and on a smooth

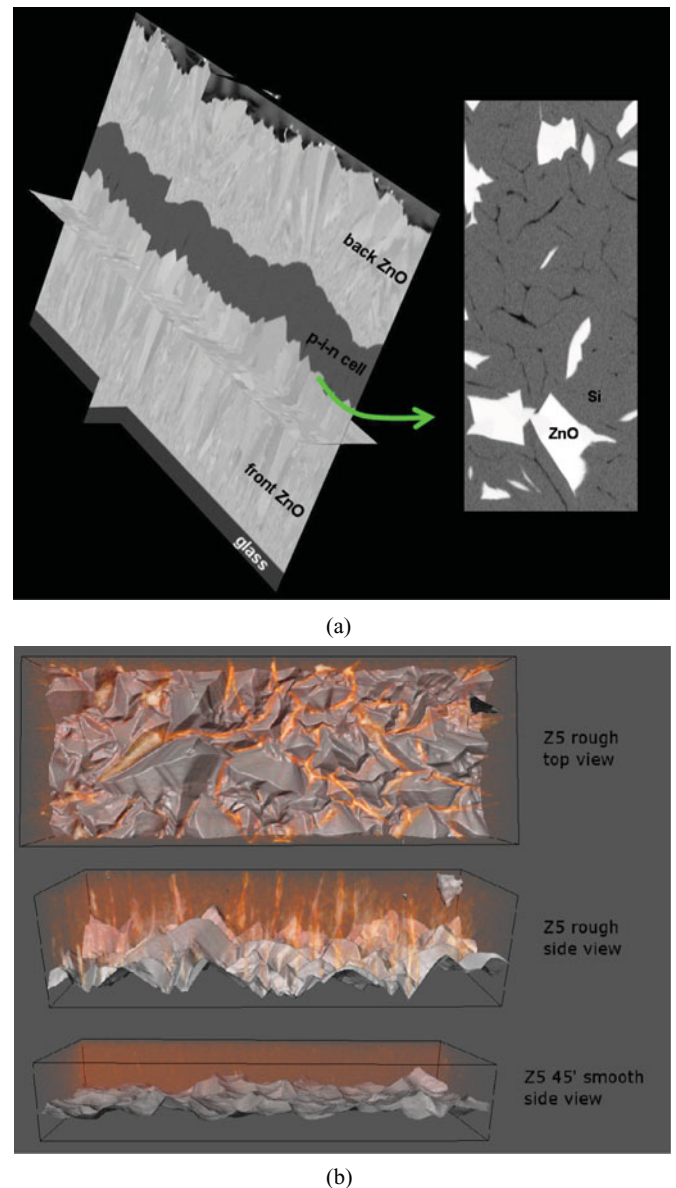


Fig. 1. (a) FIB tomography of a  $\mu\text{c-Si}$  solar cell. A top view of the growth front in the reconstructed image at the ZnO/Si interface (plane at the level of the green arrow, along the growth direction) reveals the 2-D porous network (black) in dense silicon (gray). The white areas are the top of the front ZnO pyramids. (b) Pseudocolor tomograms of the reconstructed network of porous zones (bright orange) in cells deposited on rough (Z5) and smooth (Z5 45') superstrates, respectively.

Z5 45' (current optimum superstrate). As expected from the standard 1-D image, cracks form a continuous porous network (bright orange) following the bottom of the valleys formed by the ZnO pyramids on rough superstrates. On the rough superstrate (Z5), this network is present throughout the absorber layer. In contrast with the smooth Z5 45', the porous zones are much smaller and no longer form a continuous network but are limited to a few zones of the upper part of the absorber layer.

Tomography thus appears as a valuable innovative technique to provide further insights into details of the  $\mu\text{c-Si}$  microstructure, which are recognized to be an essential parameter for high-efficiency devices [9]. A high resilience to challenging

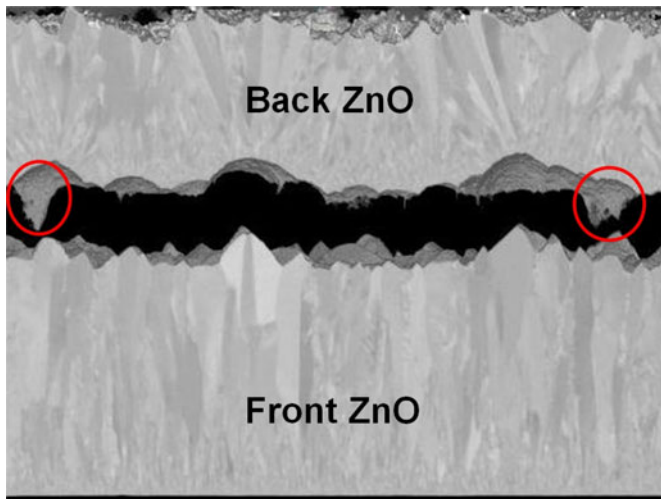


Fig. 2. Tomographic 3-D reconstruction of a single-junction  $\mu c$ -Si solar cell deposited on a rough Z5 superstrate, where the silicon layer was filtered out. The potential pinching points can be seen, here filled with the ZnO back contact.

superstrate morphologies is of paramount importance when developing multijunctions devices, as additional phenomena such as pinching (defined as the creation of an increase of sharpness by the top cell grown by PECVD) can significantly change the properties of the  $\mu c$ -Si bottom cell [23]. In that view, new insights could be gained by means of 3-D tomography, especially the localization and formation of potential pinching points related to the growth of thin-film silicon (see Fig. 2).

Tomographic reconstruction of complete solar cells thus opens new perspectives for detailed analysis of superstrate features on the growth of microcrystalline silicon material, even if instrumental and acquisition time demands render it inappropriate for routine diagnostics or high sample throughput analysis for materials development.

In the following section, we will demonstrate how the detrimental effect of such porous zones can further be reduced on rough superstrate, and especially the important role played by interfaces, well known to be one of the key parts in the development of high-efficiency microcrystalline solar cells [24]. We will then give a quantification of the beneficial effect of using silicon-oxide-doped layers ( $\text{SiO}_x$  design).

#### IV. IMPROVEMENT OF CELL PERFORMANCE USING SILICON OXIDE DESIGN

In Table I, a comparison of high-efficiency cells with standard design ( $\mu c$ -Si-doped layers) and silicon oxide design (nc- $\text{SiO}_x$  doped layers) is shown. The cells are  $1.6 \mu\text{m}$  thick and were deposited on superstrates with decreasing roughness; going from Z5 treated 5' to the Z5 treated 45' that is currently our optimum superstrate for high-efficiency  $\mu c$ -Si single-junction solar cells. Using silicon-oxide-doped layers,  $V_{oc}$  and FF were enhanced on all superstrates, and more remarkably on the roughest one.

At low illumination conditions (see Fig. 3), the benefit of the  $\text{SiO}_x$  design is obvious and consistent with [11], where quenching of local current drains was proposed as an explanation for the improved performance on rough superstrates. In-

TABLE I  
PERFORMANCE OF SOLAR CELLS AS A FUNCTION OF SUPERSTRATE TREATMENT TIME AND DOPED LAYERS DESIGN, SHOWING THE BENEFIT OF THE  $\text{SiO}_x$  DESIGN

Superstrate	$J_{sc}$ [mA/cm <sup>2</sup> ]	FF [%]	$V_{oc}$ [mV]	$\eta$ [%]	$n$ [-]	
Std	Z5 5'	23.9	67.2	493	7.9	1.8
	Z5 20'	23.8	71.8	518	8.9	1.2
	Z5 45'	23.1	74.4	529	9.1	1.1
$\text{SiO}_x$	Z5 5'	25.5	69.0	512	<b>9.0</b>	1.4
	Z5 20'	25.3	72.7	528	<b>9.7</b>	1.2
	Z5 45'	24.6	74.5	543	<b>10.0</b>	1.1

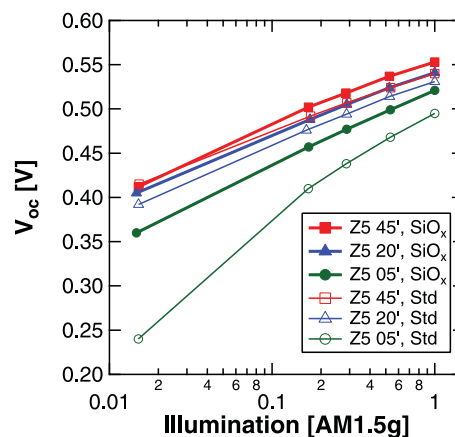


Fig. 3.  $V_{oc}$  losses as function of illumination. The  $\text{SiO}_x$  design mitigates the detrimental effect coming from the porous zones. Similar behavior was observed in FF (not shown here).

TABLE II  
INCREASE OF THE SOLAR CELL PERFORMANCE AFTER A HYDROGEN PLASMA POSTTREATMENT

		$J_{sc}$ [mA/cm <sup>2</sup> ]	FF [%]	$V_{oc}$ [mV]	$\eta$ [%]
Z5 20'	i	24.1	71.7	536	9.3
	pH <sub>2</sub>	24.3	75.3	554	<b>10.1</b>
Z5 45'	i	23.5	73.6	552	9.5
	pH <sub>2</sub>	23.7	75.4	561	<b>10.0</b>

deed, the value of the diode ideality factor  $n$ , as obtained by logarithmically fitting the  $V_{oc}$  versus illumination curves in Fig. 3 with a one-diode model, is decreased with  $\text{SiO}_x$  design and superstrate roughness, as can be seen in Table I.

The tomograms of Fig. 1 were performed on cells containing  $\text{SiO}_x$ -doped layers. Those doped layers are, therefore, unlikely to have a significant effect on the appearance of porous zones during the deposition of the intrinsic layer when a rough superstrate is used. However, as described in [9], PECVD can indeed impact the porous zones density, especially by varying the hydrogen flow when going to higher deposition rates. Here, VIM on our samples thus demonstrate that a suitable choice of interfaces indeed mitigates the detrimental effect of porous zones on rough superstrates and permitted us to reach an efficiency of 9% on a very rough superstrate and 10% on our current optimal superstrate.

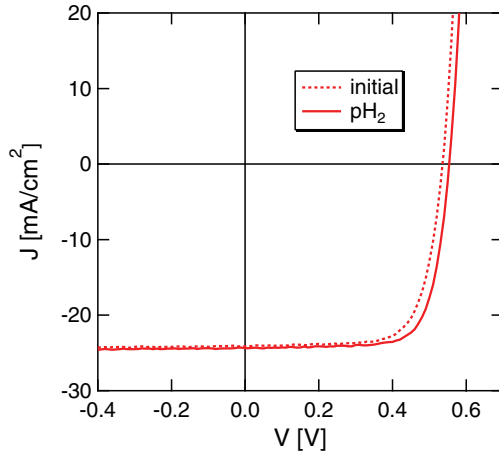
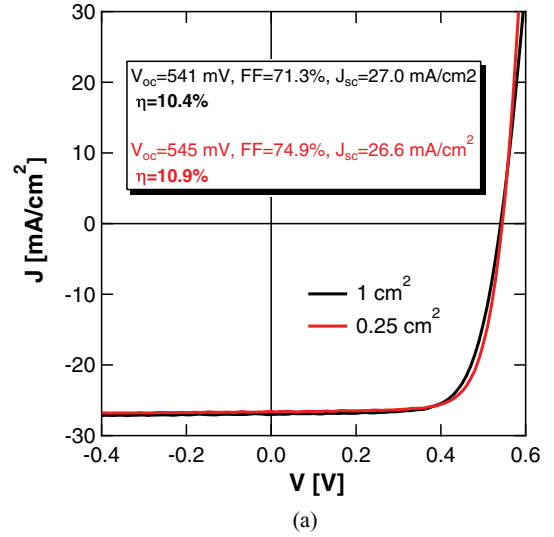


Fig. 4.  $J$ - $V$  curve of a cell deposited on medium-rough Z5 20', showing the beneficial effect of the  $\text{pH}_2$ .



(a)

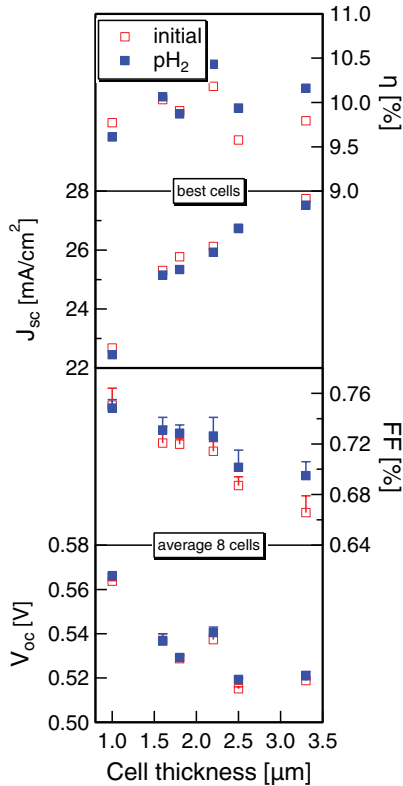
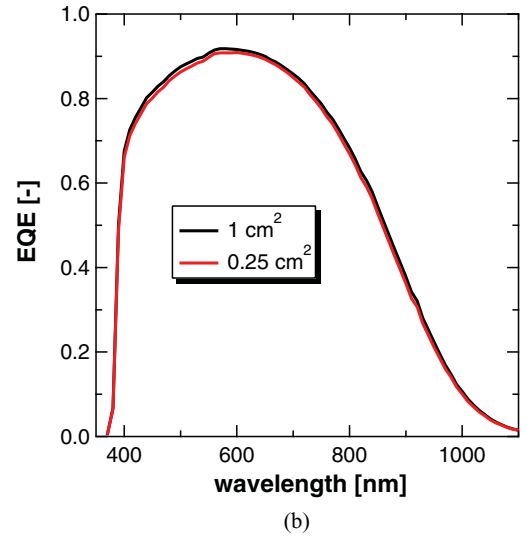


Fig. 5. Properties of solar cells deposited on smooth Z5 45' in initial state (red open squares) and after  $\text{pH}_2$  (blue solid squares), as a function of the cell total thickness. For  $V_{oc}$  and FF, the values of the eight best cells on each superstrate are given (ranked by  $V_{oc}$ ·FF), while the error bars represent the value of the best cell for each thickness. The values of the best  $V_{oc}$  are not visible as they overlap with the average value. For the current densities and the efficiencies, only the best cells are given.

## V. POST-DEPOSITION TREATMENT OF THE SOLAR CELLS

Recently, a hydrogen plasma treatment ( $\text{pH}_2$ ) for the ZnO electrodes has been introduced in our laboratory [25]. Whereas such treatment increases the mobility and free carrier density of ZnO (leading to a decrease in its sheet resistance), to our knowledge, its effect had not yet been investigated on complete



(b)

Fig. 6.  $J$ - $V$  curves and EQE of the best cells, with a total silicon thickness of  $2 \mu\text{m}$ .

high-efficiency p-i-n microcrystalline solar cells. We applied the  $\text{pH}_2$  on finished (nonencapsulated) cells, in a PECVD reactor, at  $200^\circ\text{C}$ , 0.5 mbar, with a power density of  $0.15 \text{ W/cm}^2$ , for 20 min.

Table II demonstrates the typical effect of the  $\text{pH}_2$  on  $1.1\text{-}\mu\text{m}$ -thick solar cells ( $0.25 \text{ cm}^2$ ) deposited on medium-rough Z5 20' and smooth Z5 45', with  $\text{SiO}_x$  design.

We observe, on the one hand, a reproducible improvement of up to 10 mV in  $V_{oc}$  and 1–2% (absolute) in FF for cells deposited on smooth Z5 45'. On the other hand, the effect on  $J_{sc}$  is small ( $\pm 0.2 \text{ mA/cm}^2$ ). The gain is, moreover, more pronounced on rougher superstrates (up to 20 mV in  $V_{oc}$  and 3.6% absolute in FF) such as the Z5 20' (see Fig. 4).

Whereas the gain in FF can partially be explained by a reduction of the sheet resistance of ZnO, the strongly superstrate-dependent gain in  $V_{oc}$  is attributed to a change in the silicon material of the p-i-n device. We indeed suggest that the improvements of  $V_{oc}$  and FF upon hydrogen plasma treatment are linked to a curing effect of porous zones, whose exact nature is

still under investigation. Further experiments are now required to decouple thermal effects (e.g., by annealing under various atmospheres/vacuum) from those effects arising from the presence of hydrogen and of a plasma.

## VI. VERY HIGH EFFICIENCY DEVICES

The cell design concepts just discussed were then applied to various *i*-layer thicknesses of single-junction microcrystalline solar cells, ranging between 1 and 3.3  $\mu\text{m}$  (typical bottom cell thickness range used in tandems), while other layers were kept constant. The Raman crystalline fraction yielded  $57\pm 5\%$  for all cells. The results of this thickness series on Z5 45' are shown in Fig. 5. Remarkably high conversion efficiencies between 9.5% and 10.5% were obtained, without a clear trend on the cell thickness: The decrease in  $V_{oc}$  and FF observed when increasing the thickness of the cell is compensated by an equivalent increase of  $J_{sc}$ .

With a recently developed antireflective texture at the air-glass interface [26], we further obtained a 0.25-cm<sup>2</sup> cell with 10.9% efficiency, as well as a 1-cm<sup>2</sup> cell with 10.4% efficiency, lying amongst the highest efficiencies reported so far [27]. *J-V* and EQE curves of these two cells are shown in Fig. 6.

Furthermore, the presently developed concepts also contributed to high-efficiency micromorph tandems, reaching 14.1% initial efficiency with a 290-nm-thick top cell [28].

## VII. CONCLUSION

We have demonstrated here the importance of both the cell design and the absorber layer microstructure in reaching high-efficiency microcrystalline solar cells. Three-dimensional tomography appears as an innovative and valuable technique to further probe details of microcrystalline silicon microstructure, directly revealing the 2-D nature of the porous zones within the absorber layer. With VIM, we could show that their detrimental effect on solar cell performance could be reduced with appropriate cell design, such as the use of silicon suboxide layers. Furthermore, a hydrogen plasma post-deposition treatment allowed relative efficiency gains up to 5% on our best cells. All these developments permitted us to reach outstanding efficiencies for both 0.25- and 1-cm<sup>2</sup> single-junction (10.9% and 10.4%) and micromorph configurations (14.1%).

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Authors’ photographs and biographies not available at the time of publication.