An Average-Case Analysis for Rate-Monotonic Multiprocessor Real-time Scheduling

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Abstract

We introduce the *First Fit Matching Periods* algorithm for rate-monotonic multiprocessor scheduling of periodic tasks with implicit deadlines and show that it yields asymptotically optimal processor assignments if utilization values are chosen uniformly at random. More precisely we prove that the *expected waste* is upper bounded by $O(n^{3/4}(\log n)^{3/8})$. Here the waste denotes the ratio of idle times, cumulated over all processors and n gives the number of tasks.

The algorithm can be implemented to run in time $O(n \log n)$ and even in the worst case, an asymptotic approximation ratio of 2 is guaranteed. Experiments yield an average waste proportional to $n^{0.70}$, indicating that the above upper bound on the expected waste is almost tight.

While such average-case analyses are a classical topic of Bin Packing, to the best of our knowledge, this is the first result dealing with a theoretical average-case analysis for this scheduling problem, which was described by Liu and Layland more than 35 years ago and has received a lot of attention, especially in the real-time and embedded-systems community.

1 Introduction

In this paper, we are concerned with a scheduling problem introduced by Liu and Layland [21], which is of fundamental importance in the real-time and embedded-systems community. Here one is given a set of *tasks* $S = \{\tau_1, \ldots, \tau_n\}$, where each task τ is characterized by two positive values, its *period* $p(\tau)$ and its *running time* $c(\tau)$. The task τ releases a *job* requiring running time $c(\tau)$ at each integer multiple of its period. Each job has a relative deadline of $p(\tau)$, thus we have *implicit deadlines*. The *utilization* of a task τ is defined as $u(\tau) = c(\tau)/p(\tau)$, thus it gives the average fraction of processor cycles, which are consumed by τ . More general for a set S, we denote $u(S) = \sum_{\tau \in S} u(\tau)$.

We consider *fixed-priority, preemptive* scheduling, i.e. priorities are assigned to the tasks and the arrival of a job of a higher priority task, preempts the execution of lower priority tasks. Liu and Layland [21] have proven that the *rate-monotonic* (RM) scheduling policy is optimal, meaning that if there is a feasible priority assignment, then the one in which the priority of a task τ equals $1/p(\tau)$ is also feasible (i.e. larger periods imply lower priorities). Therefore we only consider rate-monotonic priorities.

If several tasks $S' \subseteq S$ are assigned to one processor, then we call this assignment *feasible* (or RM-schedulable) if in the rate-monotonic schedule all jobs of all tasks always meet their deadlines. See Figure 1 for an example.

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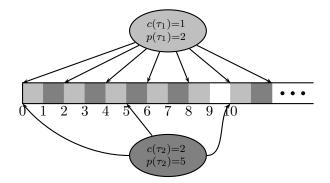


Figure 1: The picture shows a set $S = \{\tau_1, \tau_2\}$ of tasks. The arrows indicate the points in time, where the two tasks τ_1 and τ_2 release jobs. At time 0, the first job of τ_1 as well as the first job of τ_2 are released. Since the period of τ_1 is smaller than the period of τ_2 , the first job of τ_1 is executed, until it is finished at time 1. Now the first job of τ_2 is executed, but interrupted by the second job of τ_1 at time 2. The execution of the first job of τ_2 is resumed at time 3 and finished at time 4. Notice that the processor is idle for one time unit at time 9 and that the schedule repeats at the least common multiple of the periods which is 10. All jobs finish in time. The set S is feasible.

In a multiprocessor environment, the algorithmic challenge is to determine a partition of a task-set S into S_1, \ldots, S_k , such that each S_i is a feasible set of tasks for one processor and the number k of processors is minimized. The minimum possible value for k is denoted by *OPT*. The *rate-monotonic multiprocessor* scheduling problem has received considerable attention in the real-time and embedded-systems community [18, 17, 16, 1, 24, 5, 20, 15, 23, 2, 9, 19]. This popularity is due to the fact that more and more safety-critical control applications are carried out by microprocessors and in particular by multiprocessor environments. Such scheduling problems are for example relevant in the automotive and aviation industry.

A measure for the quality of a solution is the so-called *waste*, which is frequently used concerning the related *Bin Packing* problem. That is, the waste of a solution with k processors is the ratio of idles times, cumulated over all processors, i.e. k - u(S). Clearly, minimizing the waste is equivalent to minimization of the number of partitions.

For the rate-monotonic single-processor scheduling Lehoczky et al. [18] gave a probabilistic analysis, indicating that the reachable processor utilization on average is much better, than the worst-case value of $\ln(2) \approx 69\%$. For example, if periods are drawn from [1, 100] and the running times are scaled by the largest value, such that the system is barely schedulable, then the utilization tends to 88% for $n \to \infty$.

This motivates us to study also the average-case behavior in the multiprocessor case. Our analysis will work for an arbitrary distribution of the periods, as long as the utilization values are drawn independently and uniformly from [0, 1].

Related work

For the famous *Bin Packing* problem a list of items $a_1, \ldots, a_n \in [0, 1]$ is given. The goal is to assign these items to a minimal number of bins such that the total sizes of items, assigned to each bin does not exceed 1.

We will see that if for the considered scheduling problem all periods $p(\tau)$ were multiples of each other, then the problem would be exactly Bin Packing, where the utilization values correspond to the item sizes. This is because a set of tasks $S' \subseteq S$ would be feasible on one processor in this case if and only if the sum of their utilization is bounded by one.

Successful heuristics for Bin Packing are *First Fit*, *Next Fit* and *Best Fit*. In all variants the items are assigned in a consecutive manner to a bin, which has enough space (or a new one is opened). For First Fit the current item is put in the bin with the smallest index, in Best Fit it is assigned to the bin, whose item sum is maximal. For Next Fit an active bin is maintained. If the current item does not fit into it, a new bin is opened, now being the active one; old bins are never considered again. In *First Fit Decreasing* the items are first sorted by decreasing sizes and then distributed via First Fit. In the worst case Next Fit produces a 2-approximation, while First Fit needs $\lceil \frac{17}{10}OPT_{BinPacking} \rceil + 1$ [11] many bins. Asymptotically both, Best and First Fit Decreasing have an approximation ratio of 11/9 [12].

If the items are generated randomly, the heuristics perform much better, than in the worst-case scenarios. For item sizes drawn uniformly at random from [0, 1] the Best Fit algorithm yields an expected waste of $\Theta(\sqrt{n}\log^{3/4} n)$ [25], while for First Fit this value is lower bounded by $\Omega(n^{2/3})$ and upper bounded by $\mathcal{O}(n^{2/3}\sqrt{\log n})$ [25]. The upper bound even holds if First Fit is restricted to never assign more than 2 items per bin. Later we will refer to this algorithm as *Matching First Fit* (MFF). First Fit Decreasing yields an even smaller waste of $\Theta(\sqrt{n})$ [10, 14, 22]. If item sizes are drawn uniformly from $[0, \alpha]$, for any constant $\alpha \leq 1/2$, the waste of First Fit Decreasing is even constant with high probability.

Note that here the waste is defined similar to multiprocessor scheduling, namely as the number of bins minus the sum of all item sizes. But for Bin Packing also in the worst-case nearly optimal solutions can be computed, for example there is an asymptotic PTAS [8] and even an asymptotic FPTAS exists [13]. More on Bin Packing can be found in the excellent survey of Coffman et al. [3].

One major difference between rate-monotonic scheduling and Bin Packing is that for the latter it can be checked easily whether given items fit into one bin, whereas it is conjectured that this does not hold for a set of tasks and one processor. If a set S of implicit-deadline tasks is feasible (i.e. RM-schedulable), then the utilization u(S) is at most 1. However, S can be infeasible, even if u(S) < 1. Consider, for example, again the task system S in Figure 1. If we increase the running time of τ_2 by any $\varepsilon > 0$, then the set S is no longer feasible and its utilization is $u(S) = (9 + 2\varepsilon)/10$. Liu and Layland [21] have shown that S is feasible, if u(S) is bounded by $n(2^{1/n} - 1)$, where n = |S|. This bound tends to $\ln(2) \approx 0.69$ and the condition is not necessary for feasibility, as the example in Figure 1 shows. Stronger, but still not necessary conditions for feasibility are given in [20, 5, 24]. Note that the first job of each task is the *critical instance* [21], thus if $p(\tau_1) \leq \ldots \leq p(\tau_n)$ then response times for τ_i in a rate-monotonic, single-processor schedule are given by the smallest value $r(\tau_i) \geq 0$ with

$$r(\tau_i) = c(\tau_i) + \sum_{j < i} \left\lceil \frac{r(\tau_i)}{p(\tau_j)} \right\rceil c(\tau_j).$$

Of course τ_1, \ldots, τ_n are feasible if and only if $r(\tau_i) \le p(\tau_i)$ for $i = 1, \ldots, n$ [18]. But it was proven in [7] that such response times cannot even be approximated in polynomial time within a factor of $n^{c/\log\log n}$ for a fixed constant c > 0, unless $\mathbf{NP} = \mathbf{P}$. Nevertheless in practice response times can be efficiently computed using a fix-point iteration approach [1]. Furthermore Baruah and Fisher [9] showed that there is an FPTAS for computing the minimum processor speed, which is needed to make a task system RM-schedulable.

Oh and Baker [23] showed that if m processors are needed to schedule S, one must have $u(S) \ge m \cdot (\sqrt{2}-1) \approx 0.41m$. This quantity was later improved by Liebeherr et al. [20] to $m \cdot \frac{1}{1+\frac{m}{\sqrt{2}}} \approx 0.5m$ (for large m).

Most popular algorithms for rate-monotonic periodic multiprocessor scheduling first sort the tasks in a suitable way and then distribute them in a First Fit or Next Fit manner using a sufficient feasibility criterion. See the following table for an overview (with our algorithm in the last row, for the sake of comparability).

Name	sorting	distribution	ratio	run time
RMNF	inc. $p(\tau)$	Next Fit	2.67	$\mathcal{O}(n\log n)$
RMFF	inc. $p(\tau)$	First Fit	2.00	$\mathcal{O}(n\log n)$
FFDU	dec. $u(\tau)$	First Fit	2.00	$\mathcal{O}(n\log n)$
RMST	inc. $\alpha(\tau)$	Next Fit	$\frac{1}{1-u_{\max}}$	$\mathcal{O}(n\log n)$
RMGT	-	First Fit + RMST	1.75	$\mathcal{O}(n^2)$
FFMP	inc. $\alpha(\tau)$	First Fit	2.00	$\mathcal{O}(n\log n)$

Here $\alpha(\tau) = \log_2 p(\tau) - \lfloor \log_2 p(\tau) \rfloor$ and $u_{\max} = \max_{\tau \in S} u(\tau)$. In the table, the column "ratio" gives the best known upper bounds on the asymptotic approximation ratio. The *rate-monotonic general task* algorithm (RMGT) [20] distributes tasks with utilization at most 1/3 using RMST and the rest separately with First Fit. A more detailed description can be found in [19].

Furthermore there is an asymptotic PTAS under resource augmentation, computing for any fixed $\varepsilon > 0$ a solution with $(1 + \varepsilon)OPT + 1$ processors, where the tasks on each processor can be feasibly scheduled after increasing the processor speed by a factor of $1 + \varepsilon$ [6]. In the same paper it was proven that unless $\mathbf{P} \neq \mathbf{NP}$ no asymptotic FPTAS can exist for this multiprocessor scheduling problem. But it is still an open question whether there might be an asymptotic PTAS and thus an algorithm that is asymptotically optimal and does not depend on any assumption about the input. Here we call an algorithm asymptotically optimal, if the approximation ratio tends to 1 for $OPT \rightarrow \infty$. We refer to the article of Baruah and Goossens [2] for an overview on complexity issues of real-time scheduling.

Our contribution

We introduce an efficient and easy to implement algorithm for the multiprocessor rate-monotonic scheduling problem called *First Fit Matching Periods* (FFMP). We proof that it is asymptotically optimal for arbitrary periods provided that the utilizations follow a uniform distribution¹. To this end, we show that our algorithm produces a solution with expected waste of $O(n^{3/4}(\log n)^{3/8})$. Since the expected approximation ratio of $1 + O(n^{-1/4}(\log n)^{3/8})$ tends to 1 for $n \to \infty$, the solution is asymptotically optimal on average. To the best of our knowledge this is the first proof that any algorithm for this problem admits this property w.r.t. a reasonable probability distribution.

To achieve our results, we use the following technique: We introduce an auxiliary algorithm FFMP^{*} and prove that for any task set it needs at least as many processors as FFMP. Thus it suffices to derive an upper bound on the waste of this easier algorithm. We then point out that for suitable subsets of the input tasks, FFMP^{*} behaves like a well studied Bin Packing algorithm MFF. Eventually this allows to bound the waste for FFMP^{*} in terms of the waste of MFF.

In addition to the proof of the asymptotic optimality of our algorithm, we present experimental results showing that FFMP outperforms the algorithms known from literature already on random instances with a small number of tasks. We thereby provide an example of an algorithm that has been designed for asymptotic optimality and which is, in addition, competitive on reasonably small instances. Moreover, we present a family of instances where the average waste scales with $n^{0.70}$, which is almost tight to our theoretical upper bound and thus showing that our technique is suitable for sharp analyses.

¹To be exact, we assume that <u>first</u> arbitrary periods may be given and <u>then</u> the utilizations are chosen randomly.

Algorithm 1 FFMP

Input: Set τ_1, \ldots, τ_n of implicit-deadline tasks

- (1) Sort tasks such that $0 \le \alpha(\tau_1) \le \alpha(\tau_2) \le \ldots \le \alpha(\tau_n) < 1$
- (2) FOR i = 1, ..., n DO

(3) Assign τ_i to the processor P_j with least index j such that $u(P_j \cup \{\tau_i\}) \le 1 - \beta(P_j \cup \{\tau_i\}) \cdot \ln(2)$

2 Preliminaries

For our algorithm we need the following sufficient (but still not necessary) schedulability condition of Burchard et al.

Lemma 1. [20] For tasks $S = \{\tau_1, \ldots, \tau_n\}$ define

$$\alpha(\tau_i) = \log_2 p(\tau_i) - \lfloor \log_2 p(\tau_i) \rfloor \quad \text{and} \quad \beta(\mathcal{S}) := \max_{i=1,\dots,n} \alpha(\tau_i) - \min_{i=1,\dots,n} \alpha(\tau_i)$$

Then the tasks can be RM-scheduled on a single processor if $u(S) \leq 1 - \beta(S) \ln(2)$.

The intuition behind this is that a small value of $\beta(S)$ indicates that the periods of tasks in S are nearly multiples of each other and consequently the tasks are guaranteed to "harmonize".

The idea for our heuristic is now as follows: Sort the tasks w.r.t. their α -values. Then assign them in a First Fit manner using the sufficient feasibility test from Lemma 1. See Algorithm 1 for a formal description. Note that the *Rate-monotonic small tasks* algorithm (RMST) of Burchard et al. [20] is similar, just that a Next Fit assignment is used instead of First Fit. But already from average case analysis of Bin Packing, it is well know that Next Fit approaches generate linear waste for uniformly distributed item sizes [4].

3 The result of Shor

It is our aim to convey known bounds on the waste of Bin Packing algorithms to the waste of our algorithm. To this end we consider the following auxiliary algorithm *Matching First Fit* (MFF) of Shor [25], which distributes a list $L = (a_1, \ldots, a_n)$ of items to bins B_j . Denote size $(B_j) = \sum_{i \in B_i} a_i$.

Algorithm	2	Matching	First	Fit	(MFF)
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Input: Set a_1, \ldots, a_n of items

(1) FOR i = 1, ..., n DO

- (2) Assign item a_i to the bin B_j with the least index j such that either B_j is empty or both of the following conditions hold
 - B_j contains one item and this item has size at least 1/2
 - size $(B_j) + a_i \leq 1$

Shor [25] proved that MFF is monotonic, i.e. for all Bin Packing instances I and all items $a_i \in I$ one has

 $MFF(I) \ge MFF(I \setminus \{a_i\}) \ge MFF(I) - 1$

where MFF(*I*) denotes the number of bins used by MFF if applied to instance *I*. Furthermore MFF is never better than the pure First Fit algorithm and it has an expected waste of $\mathcal{O}(n^{2/3}\sqrt{\log n})$ for Bin Packing instances, whose item sizes are taken uniformly from [0, 1].

Like MFF is a restriction to First Fit, we now state a restricted version of FFMP.

4 An auxiliary algorithm

Let $\gamma := \gamma(n)$ be an integer value, which we are going to choose later. We now define a simplified version FFMP* of FFMP which can be analyzed more easily. First the tasks are partitioned into groups S_1, \ldots, S_γ with $S_j = \{\tau_i \in S \mid \frac{j-1}{\gamma} \le \alpha(\tau_i) < \frac{j}{\gamma}\}$, thus the α -values of tasks from the same group differ only slightly. Next, FFMP* never assigns more than 2 tasks to each processor and tasks from different periods are never mixed. Here we say that an algorithm *mixes* two tasks τ_1, τ_2 , if they are assigned to the same processor. The algorithm even considers a processor to be full if the first assigned task has a utilization of at most $\approx 1/2$. Note that this algorithm is precisely tailored for the used probability distribution. A formal definition of FFMP* now follows

Algorithm 3 FFMP*

Input: Set τ_1, \ldots, τ_n of implicit-deadline tasks

- (1) Sort tasks such that $0 \le \alpha(\tau_1) \le \ldots \le \alpha(\tau_n) < 1$
- (2) Partition tasks into groups S_1, \ldots, S_γ with $S_j = \{\tau_i \in S \mid \frac{j-1}{\gamma} \le \alpha(\tau_i) < \frac{j}{\gamma}\}$.
- (3) FOR i = 1, ..., n DO
 - (4) Assign τ_i to the processor P_j with the least index j such that either P_j is empty or all following conditions are satisfied
 - (a) P_i contains only one item and this item is from the same group as τ_i
 - (b) the item on P_j has utilization $\geq (1 \frac{\ln(2)}{\gamma})/2$
 - (c) $u(P_j \cup \{\tau_i\}) \le 1 \frac{\ln(2)}{\gamma}$

Note that $1 - \frac{\ln(2)}{\gamma}$ is just slightly below 1. Observe that FFMP* assigns either 1 or 2 tasks to each processor. Let FFMP*(S) be the number of processors, needed when scheduling tasks S with algorithm FFMP*. As a slight abuse of notation FFMP*(S) means as well the schedule, obtained when applying FFMP* to S, however the meaning will be clear from the context. From Lemma 1 we see that the produced solution is always feasible since either a single task is assigned to a processor or in case that two tasks are assigned, their α -values differ by at most $1/\gamma$ and their cumulated utilization is upper bounded by $1 - \ln(2)/\gamma$.

The following observation is crucial for our analysis and allows to link the expected waste of FFMP* to MFF.

Observation 2. Consider tasks τ_1, \ldots, τ_m such that one has $\frac{j-1}{\gamma} \leq \alpha(\tau_i) < \frac{j}{\gamma}$ (i.e. all tasks fall into the same group) and $0 \leq u(\tau_i) \leq 1 - \frac{\ln(2)}{\gamma}$ for all $i = 1, \ldots, m$. Create m Bin Packing items a_1, \ldots, a_m with item sizes $a_i := u(\tau_i) \cdot /(1 - \ln(2)/\gamma)$, i.e. $a_i \in [0, 1]$. Then FFMP* schedules τ_1, \ldots, τ_m in exactly the same way, that MFF distributes a_1, \ldots, a_m , i.e. task τ_i is assigned to the ℓ th processor if and only if item a_i is assigned to the ℓ th bin. Especially FFMP* $(\{\tau_1, \ldots, \tau_m\}) = MFF(\{a_1, \ldots, a_m\})$.

The main result of this section will be to show that $FFMP^*(S) \ge FFMP(S)$ for any set of tasks S. The

simplicity of FFMP^{*} will enable us to prove *monotonicity* for it, meaning that removing tasks from S can only lower the value of $FFMP^*(S)$. Although this is trivially true for algorithms yielding optimal solutions, for approximation algorithms with a complex behavior this does not necessarily hold.

Lemma 3. For any set of tasks S and $\tau^* \in S$ one has

$$\operatorname{FFMP}^*(\mathcal{S}) \ge \operatorname{FFMP}^*(\mathcal{S} \setminus \{\tau^*\}) \ge \operatorname{FFMP}^*(\mathcal{S}) - 1$$

Proof. Denote $S' = S \setminus \{\tau^*\}$ and let $S_1, \ldots, S_\gamma [S'_1, \ldots, S'_\gamma]$ be the groups of S [S', resp.]. Let i^* be the index such that $\tau^* \in S_{i^*}$. Since the algorithm never mixes tasks from different groups one has $\text{FFMP}^*(S'_i) =$ $FFMP^*(S_i)$ for all $i \neq i^*$ and $FFMP^*(S) = \sum_{i=1}^{\gamma} FFMP^*(S_i)$. Thus we may assume that all groups but S_{i^*} are empty. Furthermore tasks with utilization larger than $1 - \frac{\ln(2)}{\gamma}$ are never mixed with other tasks, thus their removal does not change the claim. Due to this we may assume that such tasks are not contained in $S = S_{i^*}$, hence S contains just tasks from the same group, all with utilization at most $1 - \frac{\ln(2)}{\gamma}$. Sticking together Observation 2 and the monotonicity of MFF [25] yields the claim.

By iteratively applying Lemma 3 we obtain

Corollary 4. For all task sets S and $S' \subseteq S$ one has

$$\operatorname{FFMP}^*(\mathcal{S}) \geq \operatorname{FFMP}^*(\mathcal{S}').$$

We may now conclude that the restricted variant of FFMP never produces better solutions than FFMP itself.

Theorem 5. For all task sets S one has

$$\operatorname{FFMP}^*(\mathcal{S}) \geq \operatorname{FFMP}(\mathcal{S}).$$

Proof. Let $P_1 \dot{\cup} \dots \dot{\cup} P_m = S$ be the solution computed by FFMP and denote the groups of S by S_1, \dots, S_{γ} . Consider an arbitrary processor P_j and after renaming let τ_1, \ldots, τ_p be the tasks on P_j in incoming order $(p \ge 1)$. Remove τ_3, \ldots, τ_p . Given that $p \ge 2$, remove τ_2 if at least one of the following conditions is true

• τ_1 and τ_2 stem from different groups

•
$$u(\tau_1) < \frac{1}{2}(1 - \frac{\ln(2)}{2})$$

• $u(\tau_1) < \frac{1}{2}(1 - \frac{\ln(2)}{\gamma})$ • $u(\{\tau_1, \tau_2\}) > 1 - \frac{\ln(2)}{\gamma}$

Let $S' \subseteq S$ the remaining tasks. Clearly FFMP* schedules S' in exactly the same way that FFMP schedules them in the solution leading to FFMP(S). Thus $FFMP^*(S') = FFMP(S)$. From Corollary 4 we gain $FFMP^*(\mathcal{S}) \ge FFMP^*(\mathcal{S}')$. Plugging both equations/inequalities together, yields the claim.

An upper bound for FFMP* 5

In this section we will give an upper bound on the expected waste of FFMP*, by exploiting the bound on the waste of MFF. Again Observation 2 will be crucial.

Theorem 6. Let $f : \mathbb{R}_{>1} \to \mathbb{R}$ be a concave and monotonic increasing function, such that f(n) yields an upper bound on the expected waste of MFF applied to n items drawn uniformly at random from [0, 1]. Then the expected waste of FFMP* is bounded by $\frac{n}{2} + \gamma \cdot f(n/\gamma)$ for n tasks with arbitrary periods, but utilization values drawn uniformly at random from [0, 1].

Proof. Let S_1, \ldots, S_{γ} be the partition of the tasks S into groups. Denote n = |S| and $n_i = |S_i|$. FFMP* never mixes tasks from different groups, thus

$$\mathtt{FFMP}^*(\mathcal{S}) = \sum_{i=1}^{\gamma} \mathtt{FFMP}^*(\mathcal{S}_i).$$

Consider an arbitrary group S_i . Call tasks τ with a utilization of $u(\tau) > 1 - \frac{\ln(2)}{\gamma}$ full tasks and ordinary tasks otherwise. Let S_i^{full} be the set of full tasks from S_i and let $S'_i = S_i \setminus S_i^{\text{full}}$ be the ordinary tasks. Condition that $|S'_i| = n_i^o$. Clearly the algorithm FFMP* does not mix ordinary and full tasks, thus

$$\mathtt{FFMP}^*(\mathcal{S}_i) = \mathtt{FFMP}^*(\mathcal{S}_i^{\mathrm{full}}) + \mathtt{FFMP}^*(\mathcal{S}_i')$$

A full task has a utilization of at least $1 - \frac{\ln(2)}{\gamma}$, thus for each full task it suffices to account a waste of $\frac{\ln(2)}{\gamma} \leq \frac{1}{\gamma}$. The expected waste stemming from the processors, owning the full tasks of group *i* is then

$$E[\texttt{FFMP}^*(\mathcal{S}_i^{\text{full}}) - u(\mathcal{S}_i^{\text{full}})] \le \frac{n_i - n_i^o}{\gamma}.$$

It remains to bound the waste from the ordinary tasks. The utilization values of tasks in S'_i are conditioned to be in $[0, 1 - \frac{\ln(2)}{\gamma}]$. It is not difficult to see that the distribution of $u(\tau)$ for $\tau \in S'_i$ is uniformly w.r.t. $[0, 1 - \frac{\ln(2)}{\gamma}]$. If we define a Bin Packing instance I'_i with an item of size $u(\tau)/(1 - \frac{\ln(2)}{\gamma})$ for each $\tau \in S'_i$, then the item sizes in I'_i are distributed uniformly w.r.t. [0, 1]. By Observation 2

$$E[\mathtt{FFMP}^*(\mathcal{S}'_i)] = E[\mathtt{MFF}(I'_i)] \leq \frac{n^o_i}{2} + f(n^o_i).$$

The rest of the proof simply consists of summing up the achieved bounds on the waste. We can express the expected waste, stemming from the processors owning ordinary tasks from the *i*th group as

$$\begin{split} E[\texttt{FFMP}^*(\mathcal{S}'_i) - u(\mathcal{S}'_i)] &\leq (\frac{n_i^o}{2} + f(n_i^o)) - E[u(\mathcal{S}'_i)] &= (\frac{n_i^o}{2} + f(n_i^o)) - n_i^o \frac{1 - \ln(2)/\gamma}{2} \\ &\leq f(n_i^o) + \frac{n_i^o}{\gamma} \end{split}$$

Combining ordinary and full tasks yields

$$E[\texttt{FFMP}^*(\mathcal{S}_i) - u(\mathcal{S}_i)] \le \frac{n_i - n_i^o}{\gamma} + (f(n_i^o) + \frac{n_i^o}{\gamma}) \le f(n_i) + \frac{n_i}{\gamma}$$

using monotonicity of f. Hence the total expected waste for solution $FFMP^*(S)$ can be written as

$$E[\mathtt{FFMP}^*(\mathcal{S}) - u(\mathcal{S})] \stackrel{(*)}{=} \sum_{i=1}^{\gamma} E[\mathtt{FFMP}^*(\mathcal{S}_i) - u(\mathcal{S}_i)] \le \sum_{i=1}^{\gamma} (f(n_i) + \frac{n_i}{\gamma}) \stackrel{(**)}{\le} \frac{n}{\gamma} + \gamma \cdot f(n/\gamma)$$

For (*) we used linearity of expectation and (**) follows by Jensen's inequality and concaveness of f.

Applying the best known bound on f(n) we obtain

Theorem 7. For the expected waste of FFMP one has

$$E[\mathsf{FFMP}(\mathcal{S}) - u(\mathcal{S})] = \mathcal{O}(n^{3/4} (\log n)^{3/8})$$

if S consists of n tasks, whose utilization values are drawn uniformly at random from [0, 1].

Proof. Theorem 5 provides that bounding the waste of FFMP^{*} is sufficient. Choosing $\gamma(n) = \lceil n^{1/4}/(\log n)^{3/8} \rceil$ and using the bound of $f(n) = \mathcal{O}(n^{2/3}(\log n)^{1/2})$ [25] together with Theorem 6 yields the claim (observe that $c \cdot n^{2/3} \cdot (\log n)^{1/2}$ is concave and monotonic).

Observing that $OPT(S) = \Omega(n)$ with very high probability, we conclude that

Corollary 8. Let S consist of n tasks, whose utilization values are drawn uniformly at random from [0, 1]. Then the expected approximation ratio of FFMP is

$$E\left[\frac{\mathtt{FFMP}(\mathcal{S})}{OPT(\mathcal{S})}
ight] \le 1 + \mathcal{O}(n^{-1/4}(\log n)^{3/8})$$

Using essentially the same proof as [20] (see also Leung et al. [19]) one can easily show that even in the worst-case one has $FFMP(S) \le 2u(S) + 4$, i.e. the asymptotic worst-case approximation ratio of FFMP is 2. For the sake of completeness the proof of this fact can be found in the appendix.

6 Experimental Results

We have performed simulations of our FFMP algorithm and compared it with RMFF, FFDU, and RMGT. The experimental setting is as follows. We choose the periods $p(\tau_i) \in [0, 500]$ and the utilizations $u(\tau_i) \in [0, 1]$ uniformly at random. We create random instances in the range of 10 to 100000 tasks. For each given n, we generate 100 random samples to get a good estimate of the expected value of the waste. We use the same instances to test each algorithm to allow also a direct comparison of their performance.

The log-log-plot in Figure 2 shows the power law behavior of the average waste of FFMP as predicted by Theorem 7. The regression yields an exponent of 0.70 which is close to $\frac{3}{4}$ from the Theorem showing that the theoretical analysis is almost tight, i.e. that we do not loose much by analyzing the dominated algorithm FFMP*. In contrast to that, the average waste produced by the other algorithms shows an almost linear dependence on the number of tasks. In fact, we believe that the dependence is linear since the measurements of their average waste show a slight curvature to the left, indicating that the averages are actually growing faster than the fitted straight lines.

The simulated average processor load shown in Figure 3 supports this claim. By *average processor load*, we mean the expected value of the mean utilization of the processors. The closer this value is to 1 the less processor cycles are wasted. Hence, it comes to no surprise that the average load for FFMP tends to 1 with increasing n. For the other algorithms, there is strong evidence that they converge to respective constants strictly smaller than 1 and likely even not more than 0.9.

Interestingly, the quadratic running time of RMGT, which is due to the exact feasibility test for the large tasks, does not pay off in comparison with FFMP, which runs in $O(n \log n)$ time. This does not only hold for the average waste, but also on a per instance basis: FFMP performs better than RMGT for 94 out of 100 random instances with 10 tasks and always better on our random test instances with a larger number of tasks. This is due to the splitting of the tasks into small tasks (i.e. utilization at most 1/3) and large tasks. Thus all tasks with utilization at least 2/3 are deterministically scheduled alone on a processor. For example in

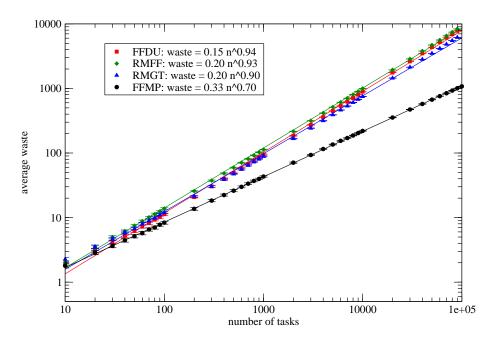


Figure 2: The average waste depending on the number of tasks are shown with 3σ error bars for FFMP and three algorithms from literature. For each algorithm, we fit a power function (straight lines in the log-log plot) and display the results in the legend box.

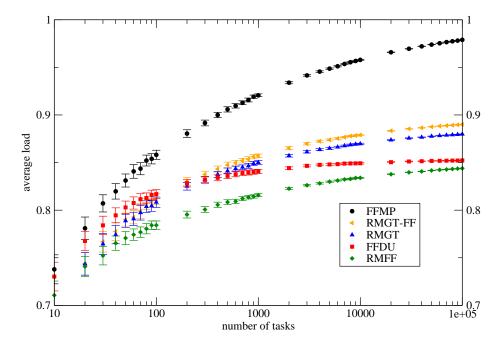


Figure 3: The average load of the processors is shown with 3σ error bars. In the algorithm RMGT-FF, the Next Fit distribution for the small tasks is replaced by First Fit.

expectation 10% of all tasks have a utilization between 0.7 and 0.8. Each of those tasks contributes at least 0.2 to the total waste. Therefore the expected waste of RMGT must be at least $0.1 \cdot 0.2 \cdot n = \Omega(n)$, even if after splitting, the algorithm would find an optimum solution for both parts. FFMP can be implemented in $\mathcal{O}(n \log n)$ using a heap data structure. For the sake of completeness a detailed description can be found in the appendix.

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Appendix

Implementation

In the following, we explain how to implement the FFMP algorithm with a complexity of $\mathcal{O}(n \log n)$ using a heap data-structure. To this end, we rewrite the predicate of the feasibility test as follows. Consider the current task τ_i and a processor P. Recall that the tasks are ordered by increasing α -values. Thus, the value of β always depends on the current task and the task already on P that defines the minimum α -value, say $\alpha(P) = \min{\{\alpha(\tau_j) \mid \tau_j \in P\}}$. Hence, task τ_i can be scheduled on processor P if

$$u(P) + u(\tau_i) \le 1 - (\alpha(\tau_i) - \alpha(P)) \cdot \ln(2)$$

which is equivalent to

$$\underbrace{u(\tau_i) + \alpha(\tau_i)\ln(2)}_{v_i} \le \underbrace{1 - u(P) + \alpha(P)\ln(2)}_{\ell_P}.$$

Note that the left-hand side (called v_i in the following) only depends on the current task τ_i , and that the right-hand side (called ℓ_P in the following) only depends on the tasks which are already scheduled on the processor P. For $P = \emptyset$, we define $\ell_P = \infty$. Hence, we may maintain a binary heap data-structure to find in logarithmic time the processor with the least index that passes the feasibility test. Viewed as a binary tree, we have n leaves corresponding to processors. They are labeled with the right-hand sides ℓ_P depending on their current utilization and the α -value of the first task which was scheduled on each one. Initially, they are empty and their labels are ∞ . The other nodes of the tree are labeled with the maximum label of their respective children. See Figure 4 for a visualisation. Starting from the root, we proceed with the left child if the v_i value for the current task is not greater than the label of the left child. Otherwise, we turn to the right child, which then has a sufficiently large label by construction. The leaf that we eventually reach determines the processor on which we schedule the current task in $\mathcal{O}(\log n)$. Moreover, the update of the data-structure also takes $\mathcal{O}(\log n)$ since we only need to traverse the tree back to the root and update the labels on this path; any other label remains invariant.

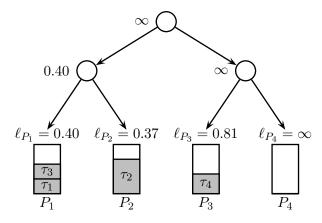


Figure 4: Example for the heap structure. We use notation $\tau_i = (u(\tau_i), \alpha(\tau_i))$. Then FFMP schedules tasks $\tau_1 = (0.3, 0.0), \tau_2 = (0.7, 0.1), \tau_3 = (0.3, 0.2), \tau_4 = (0.4, 0.3)$ as depicted.

Worst-case behaviour

In this section we outline, why the asymptotic worst-case approximation ratio is bounded by 2.

Lemma 9. One has

$$FFMP(\mathcal{S}) \le 2 \cdot u(\mathcal{S}) + 4.$$

Proof. Let P_1, \ldots, P_m be the used processors. By $\alpha(P_i) = \min\{\alpha(\tau_j) \mid \tau_j \in P\}$ we denote the α -value of the first task, assigned to P_i . Clearly $0 \le \alpha(P_1) \le \ldots \le \alpha(P_m) < 1$. Let τ_j be the first task, assigned to P_{i+1} for $i \in \{1, \ldots, m-1\}$, thus $\alpha(\tau_j) = \alpha(P_{i+1})$. But the (i + 1)th processor was only opened because τ_j did not fit on a prior processor. Especially it did not fit on P_i , thus

$$u(P_i) + u(P_{i+1}) \geq u(P_i) + u(\tau_j)$$

>
$$1 - \beta(P_i \cup \{\tau_j\}) \cdot \ln(2)$$

$$\geq 1 - \ln(2) \cdot (\alpha(P_{i+1}) - \alpha(P_i))$$

Hence

$$u(\mathcal{S}) \geq \sum_{i=1}^{\lfloor m/2 \rfloor} (u(P_{2i-1}) + u(P_{2i}))$$

$$\geq \sum_{i=1}^{\lfloor m/2 \rfloor} (1 - \ln(2) \cdot (\alpha(P_{2i}) - \alpha(P_{2i-1})))$$

$$\geq \lfloor m/2 \rfloor - \ln(2)$$

$$\geq m/2 - 2$$

using that the differences of the α -values sum up to at most 1. We conclude that

$$m \le 2u(\mathcal{S}) + 4.$$