

# HPC-UGENT USER MEETING

## MULTISCALE MODELING IN CHEMICAL ENGINEERING ON TIER-2 AND TIER-1

Pieter A. Reyniers, Kevin M. Van Geem, Guy B. Marin

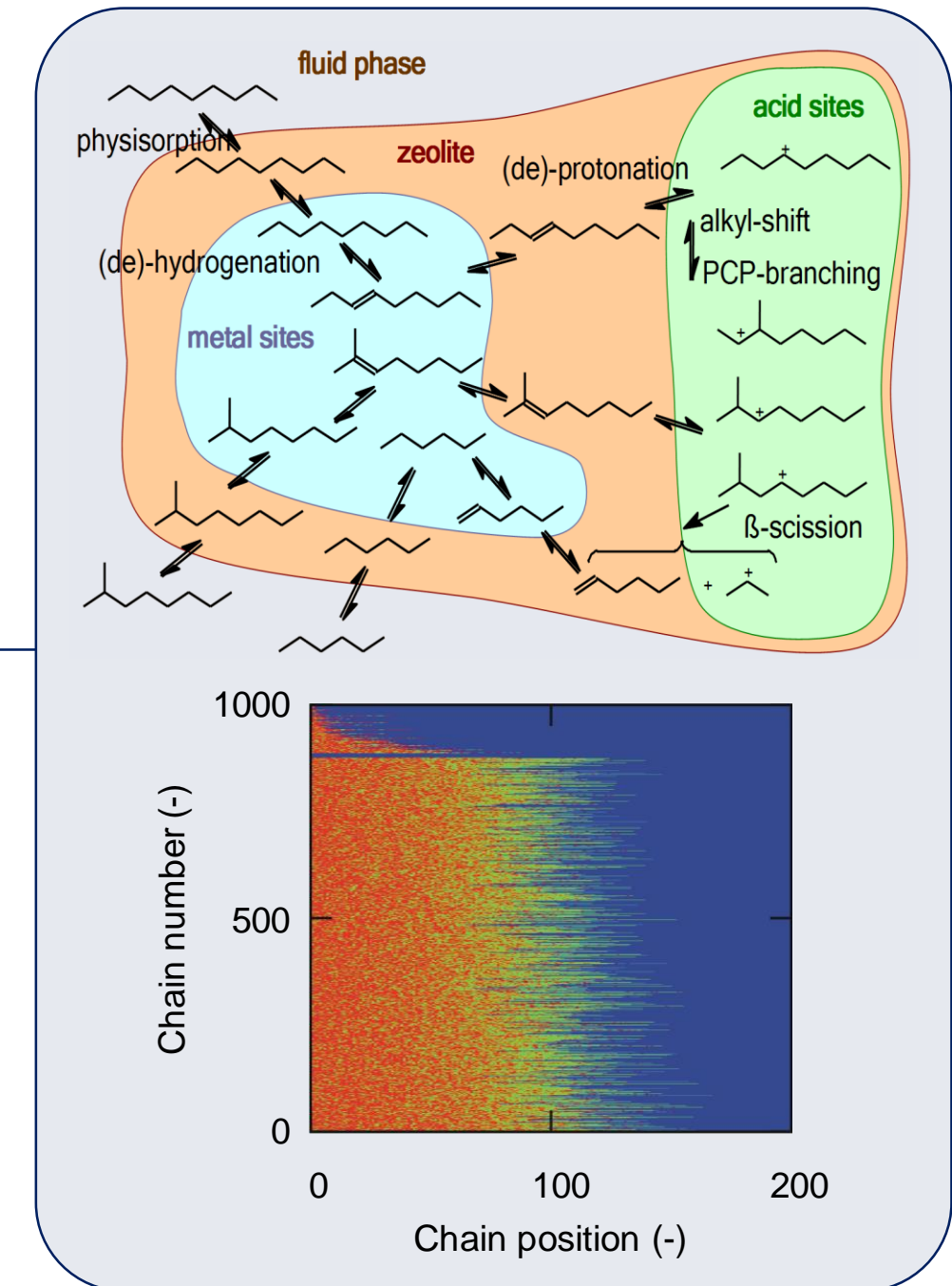
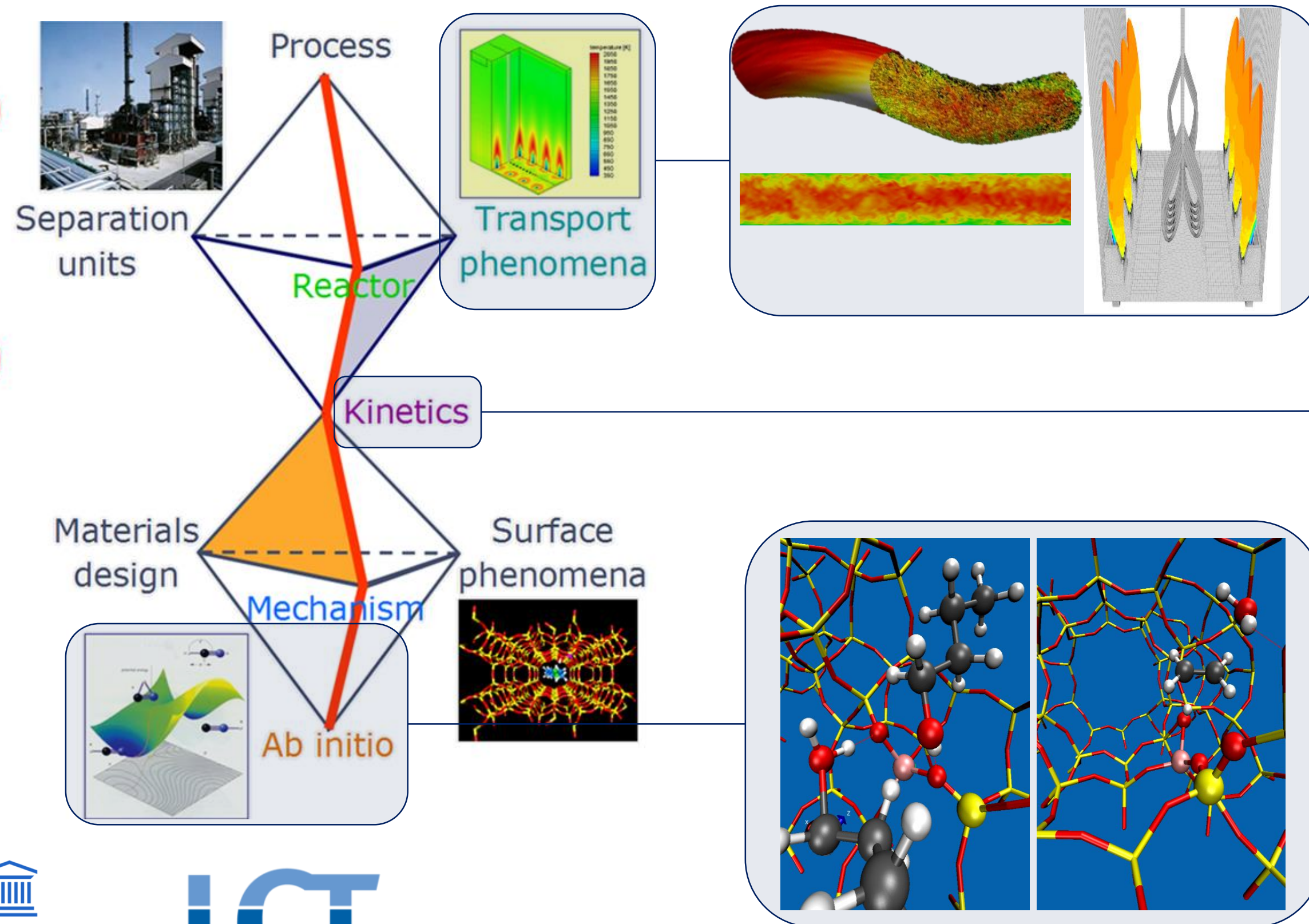
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# MULTISCALE MODELING IN CHEMICAL ENGINEERING

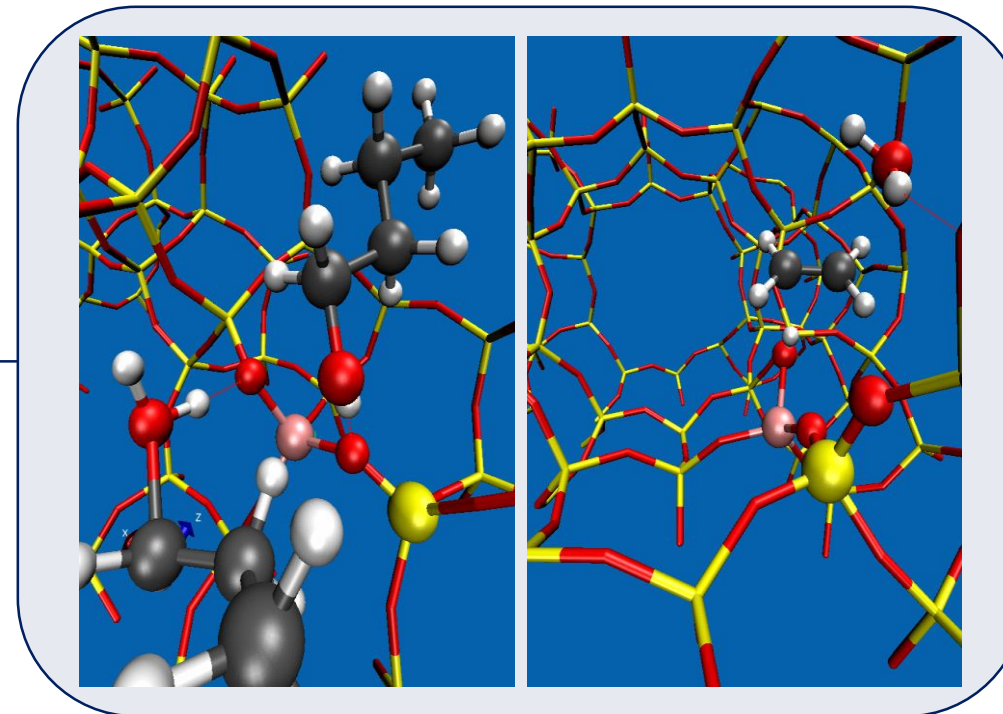
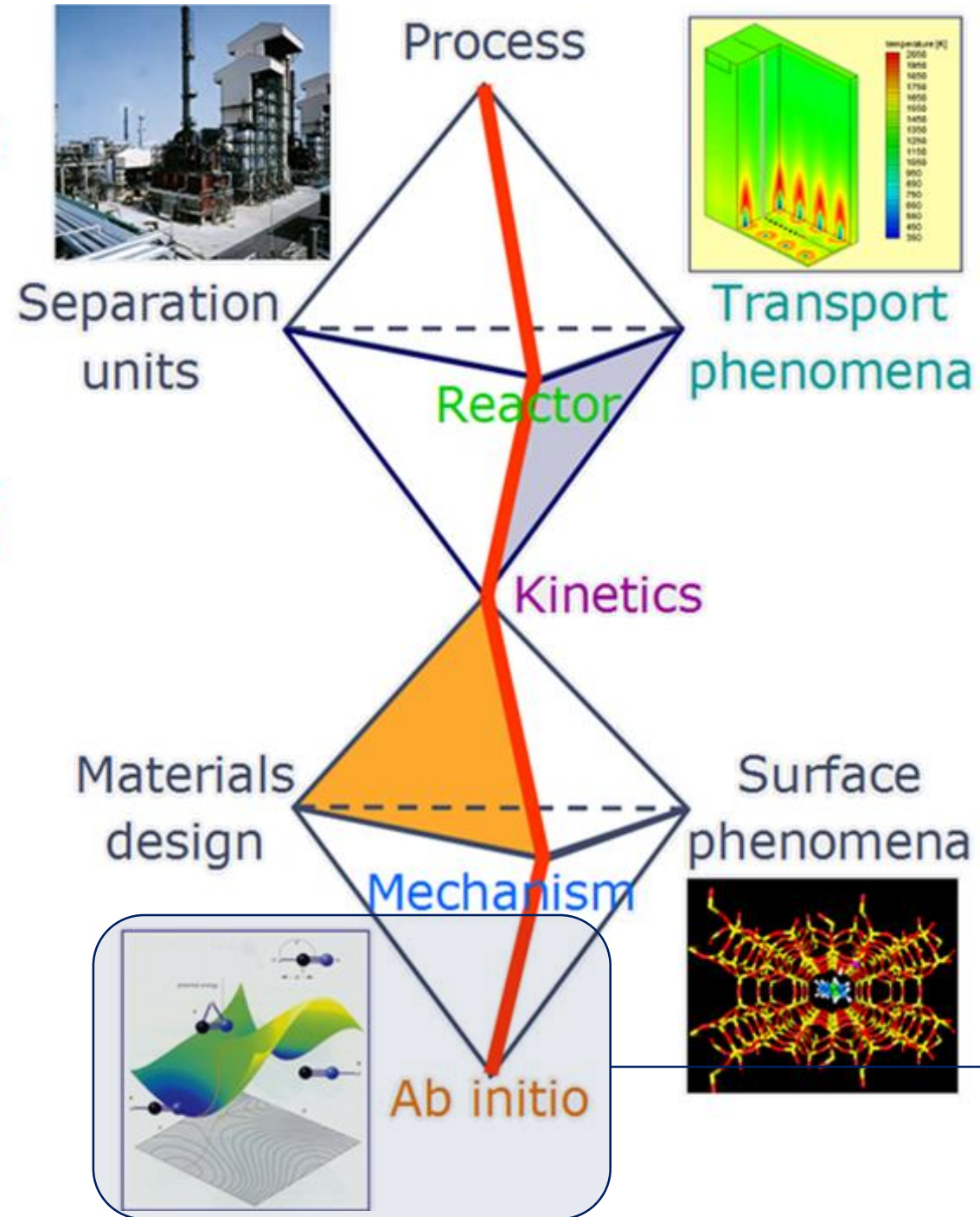
Reaction engineering





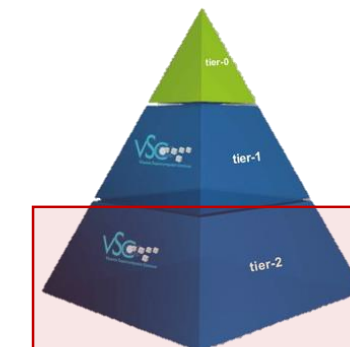
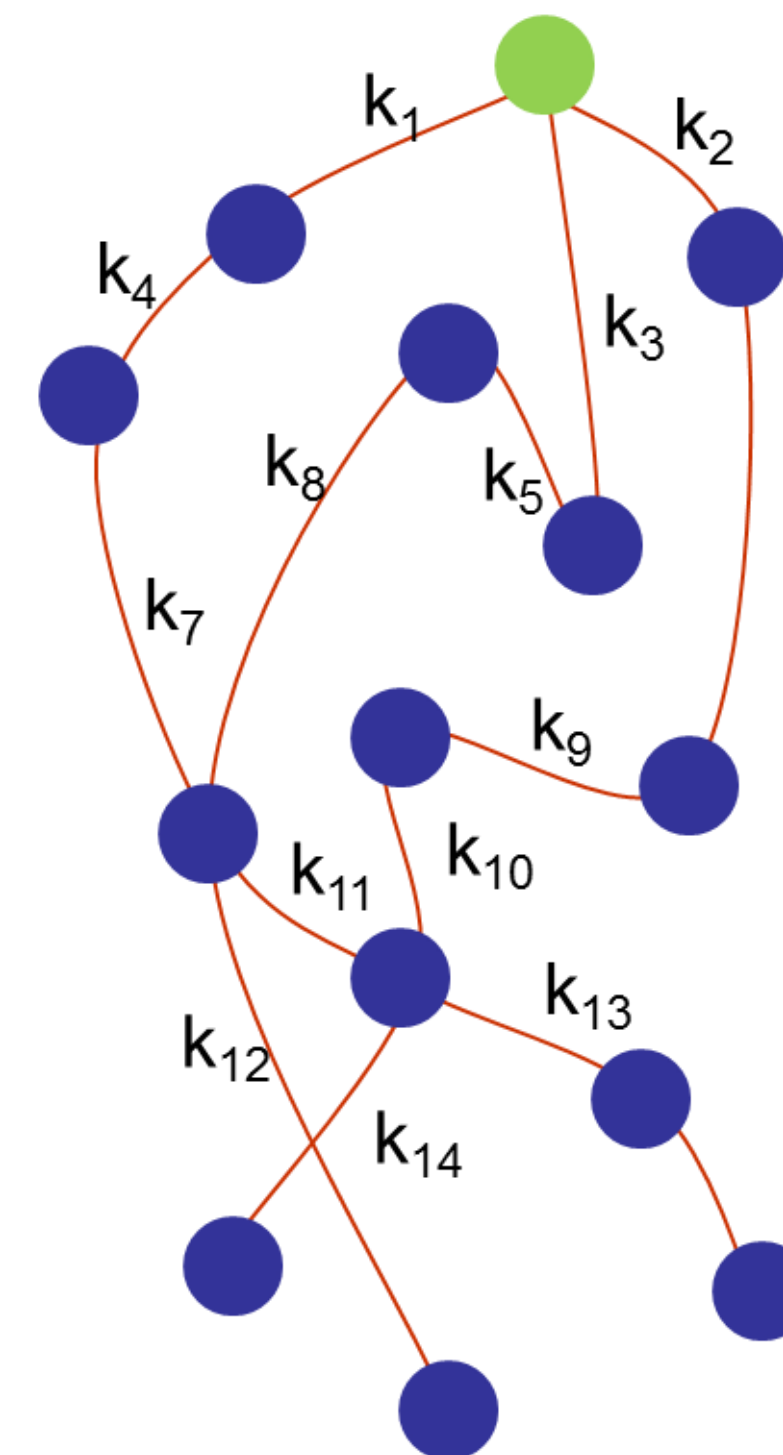
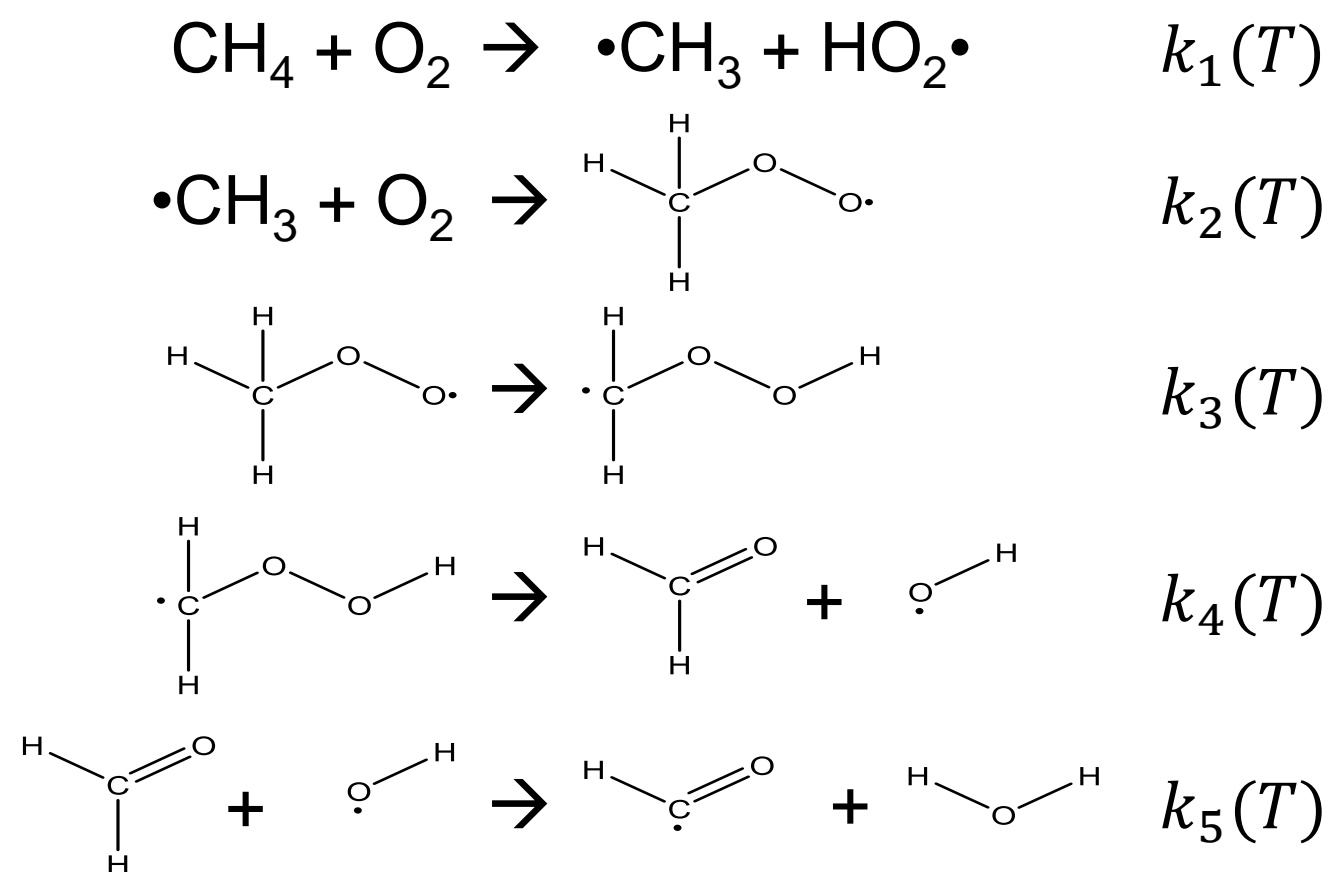
# MULTISCALE MODELING IN CHEMICAL ENGINEERING

Reaction engineering



# AUTOMATIC AB INITIO CALCULATIONS

**Kinetic model:** List of chemical reactions that are important for a chemical process, including a **rate coefficient** for each reaction.



# AUTOMATIC AB INITIO CALCULATIONS

## Genesys: Use of chemoinformatics

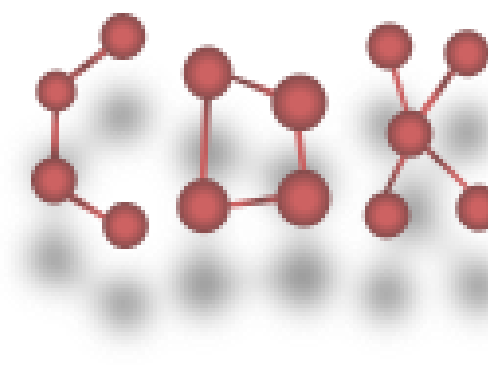
- Molecular representation
- Graph and group theory
- Not tailored to specific applications



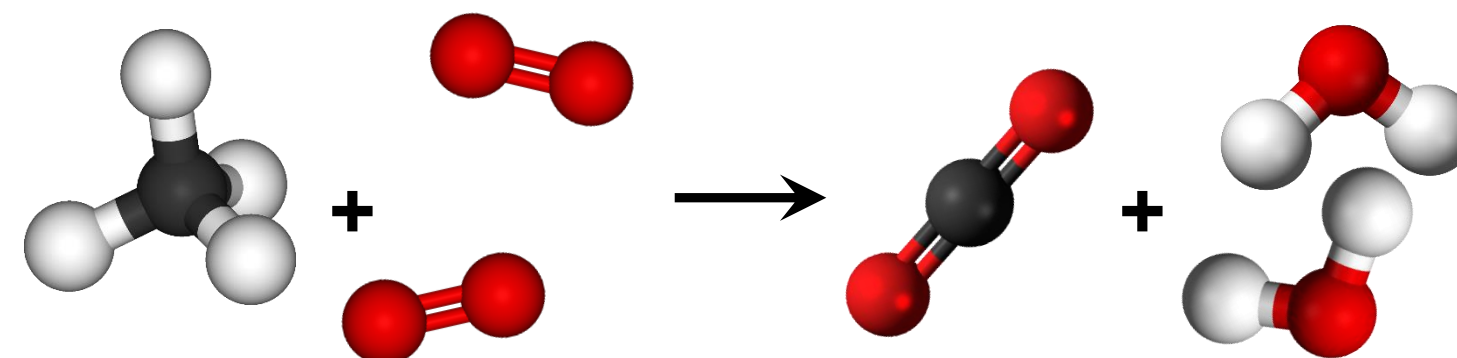
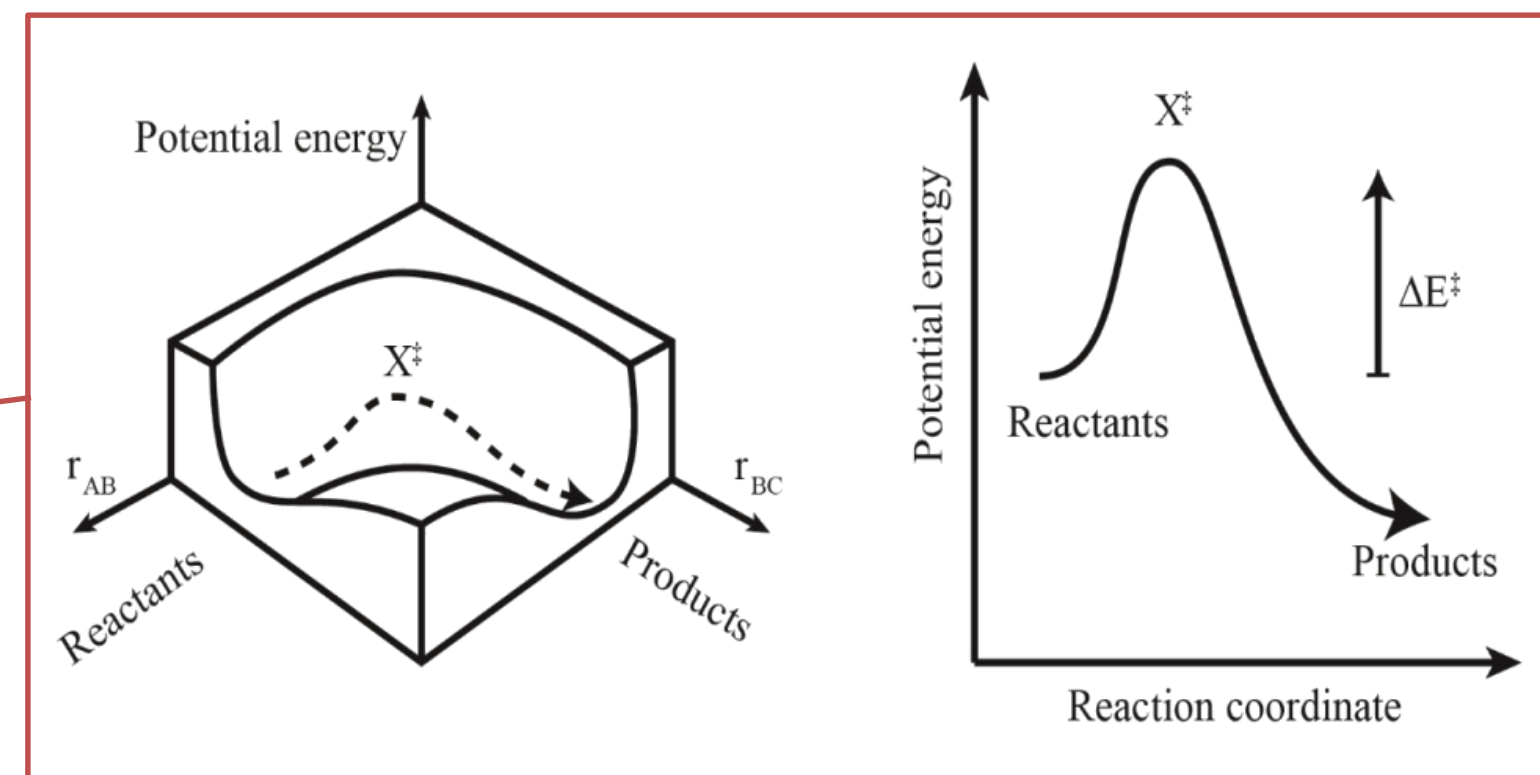
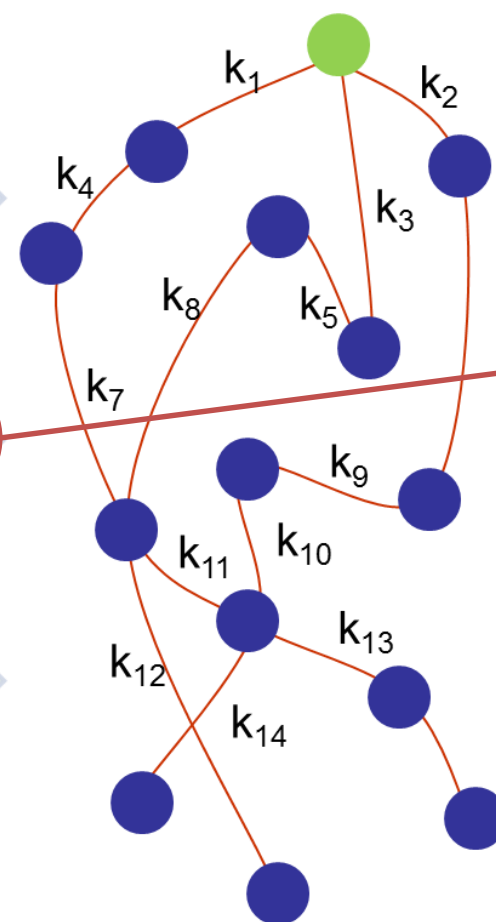
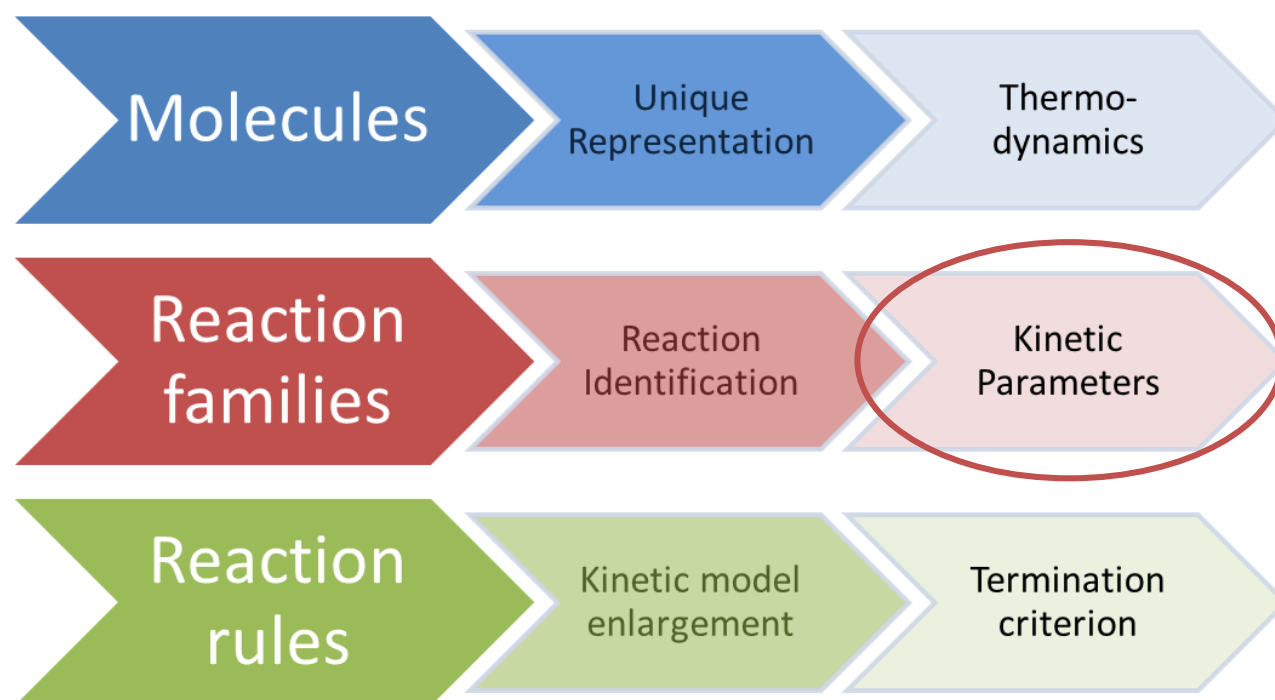
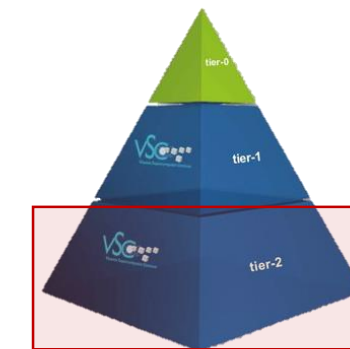
Open-Source Cheminformatics  
and Machine Learning



OpenBabel



CDK

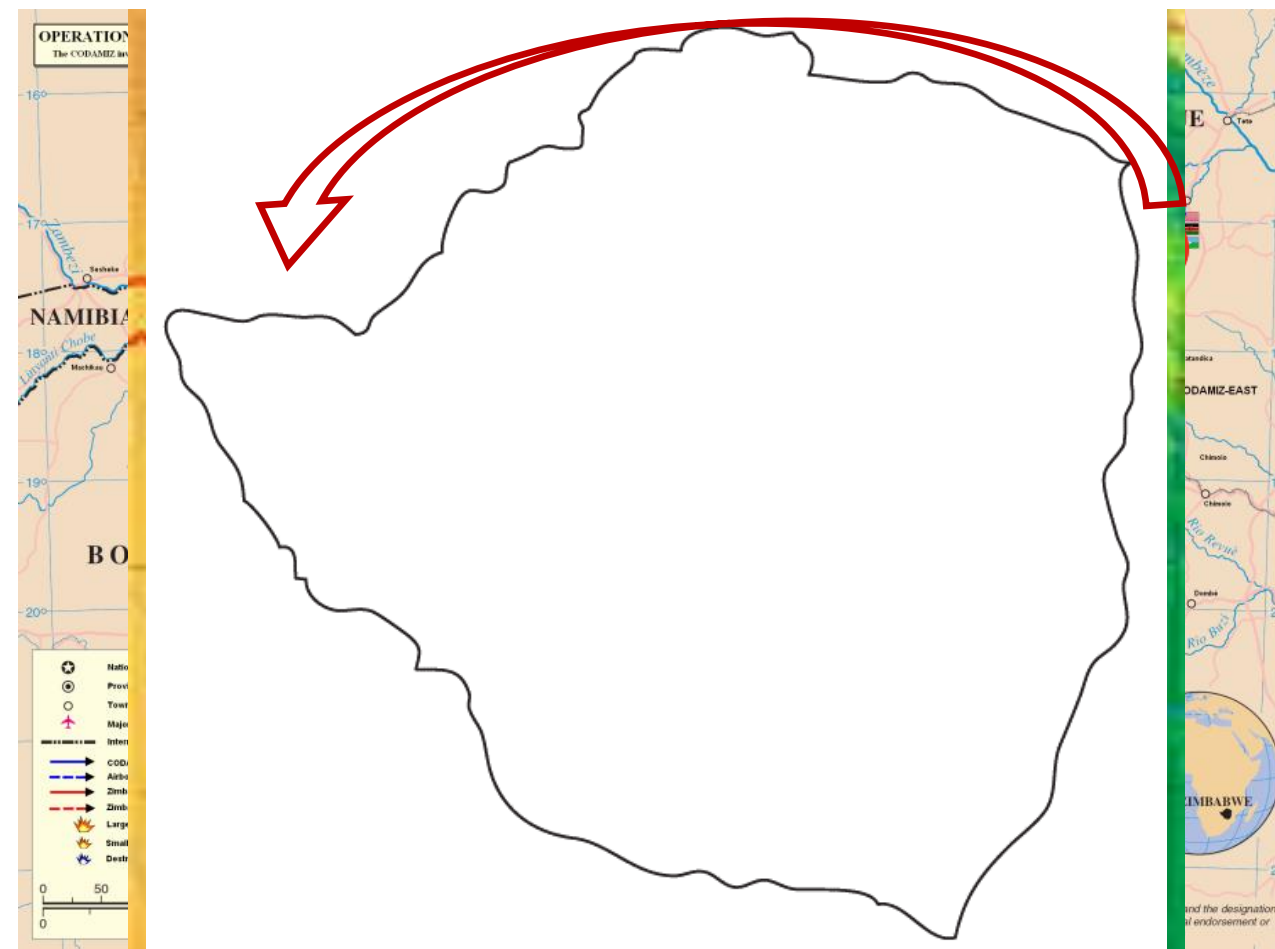
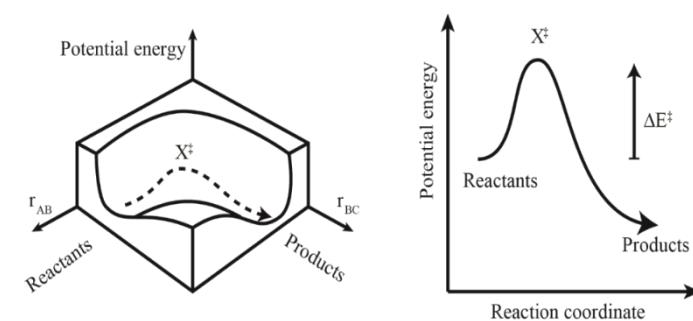
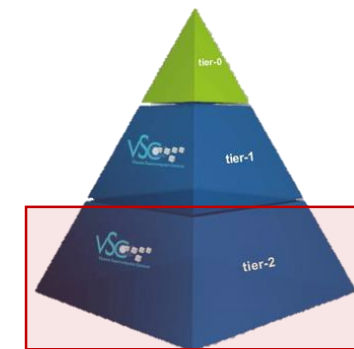


15/12/2017

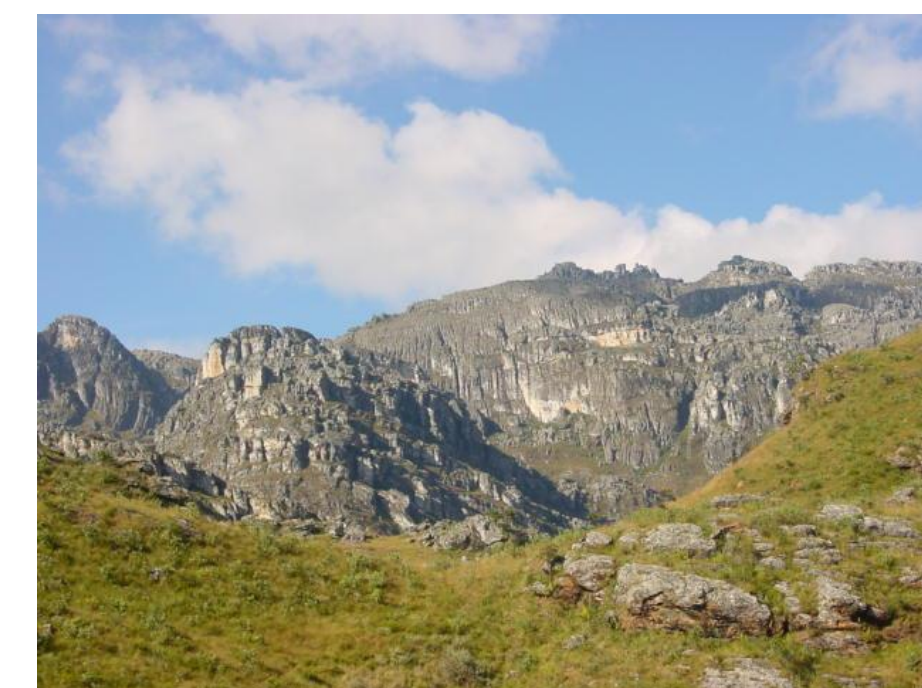
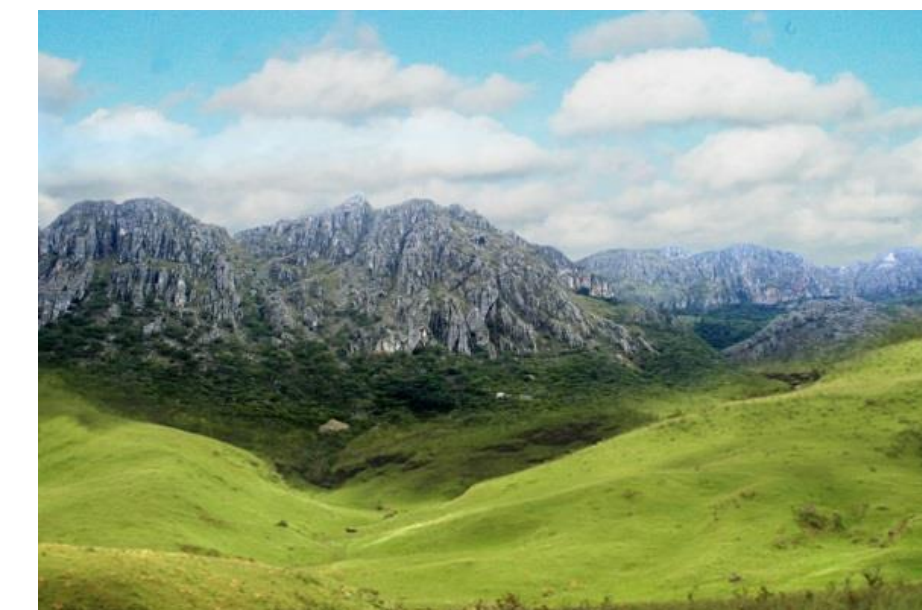
5/32



# AUTOMATIC AB INITIO CALCULATIONS



**Automatic** ab initio calculations via **Genesys** to complete missing thermodynamic and kinetic data in chemical networks

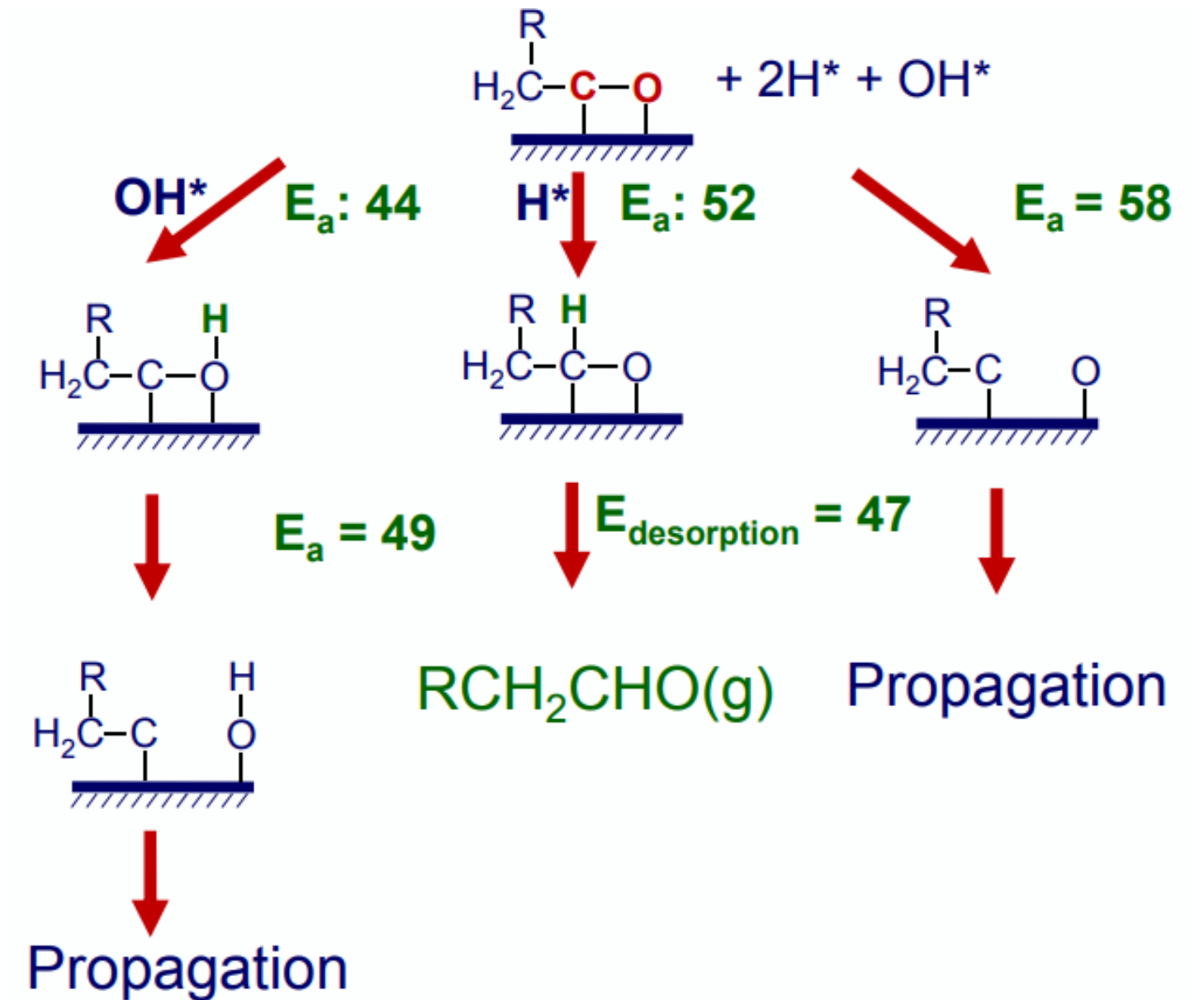
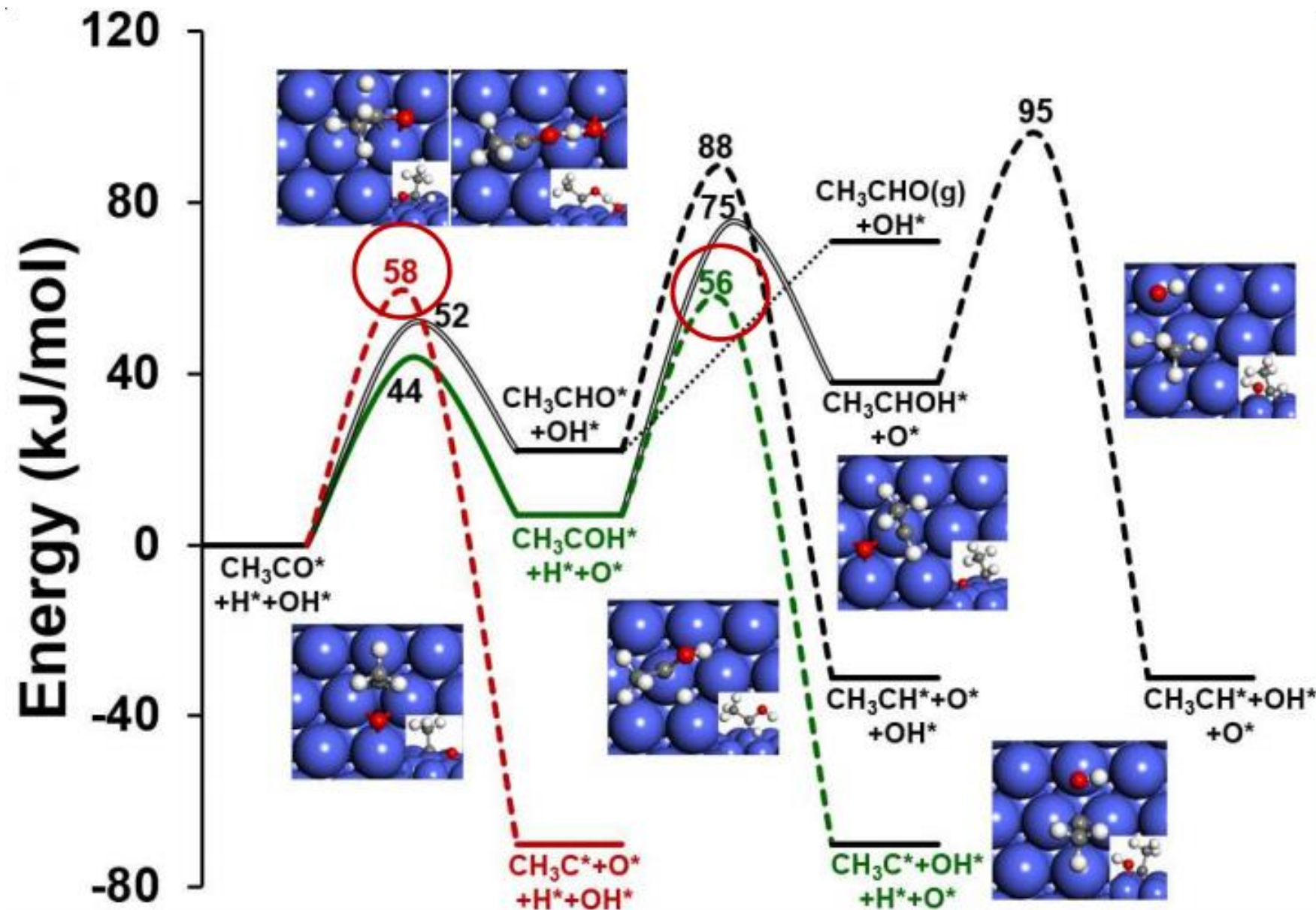




# AB INITIO CALCULATIONS

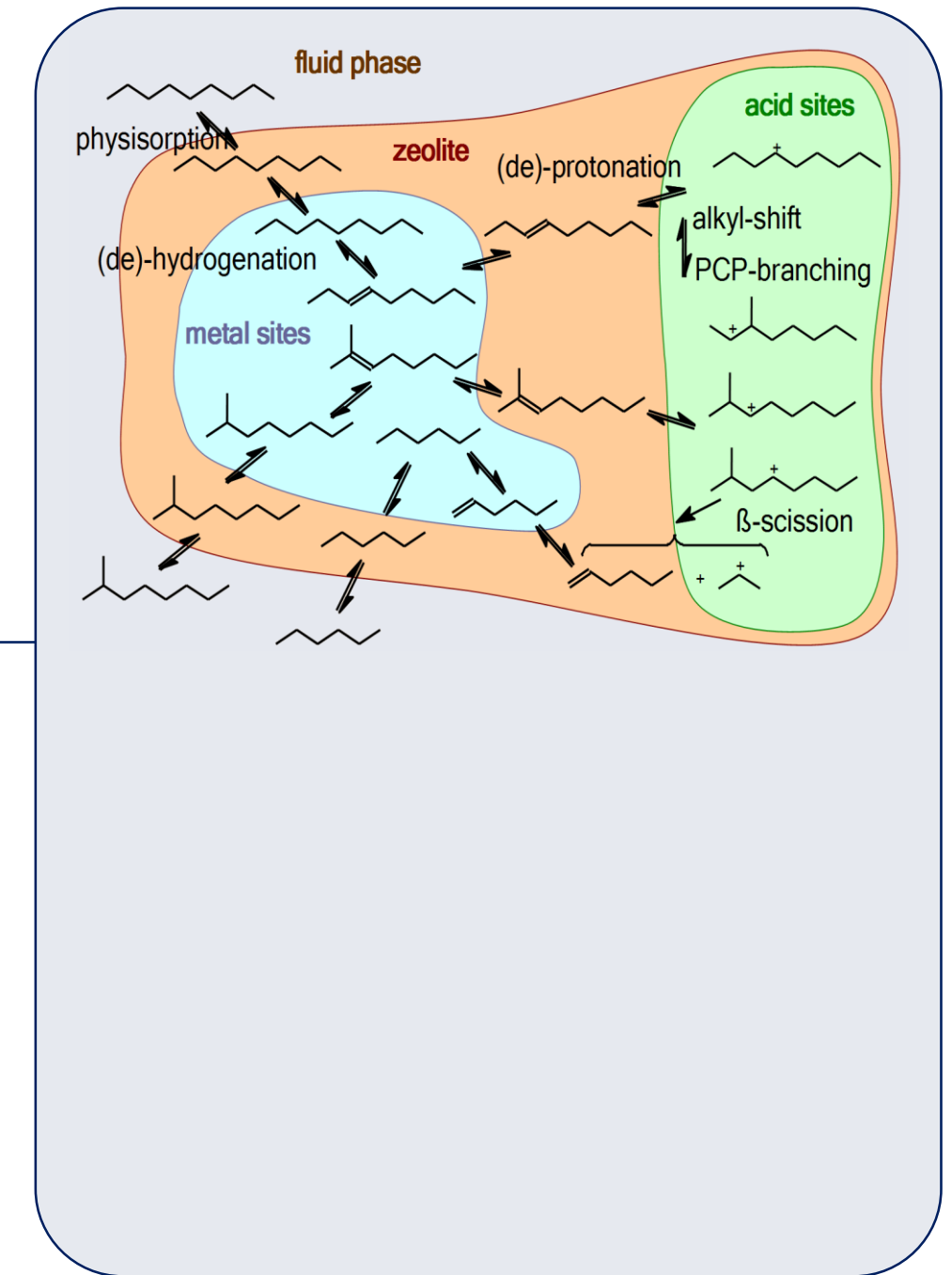
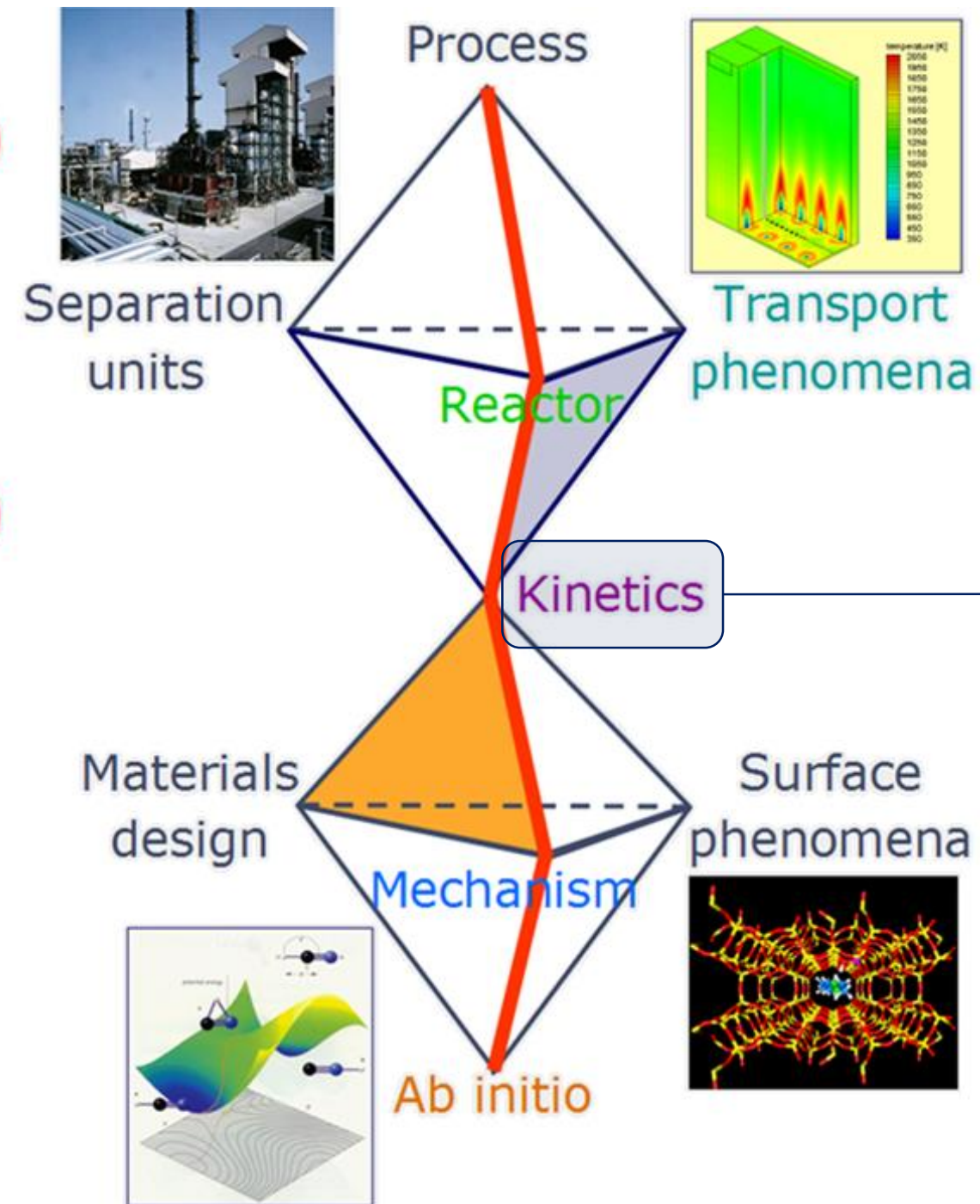
## Elucidate reaction mechanisms

Fischer-Tropsch synthesis: natural gas to liquid fuels



# MULTISCALE MODELING IN CHEMICAL ENGINEERING

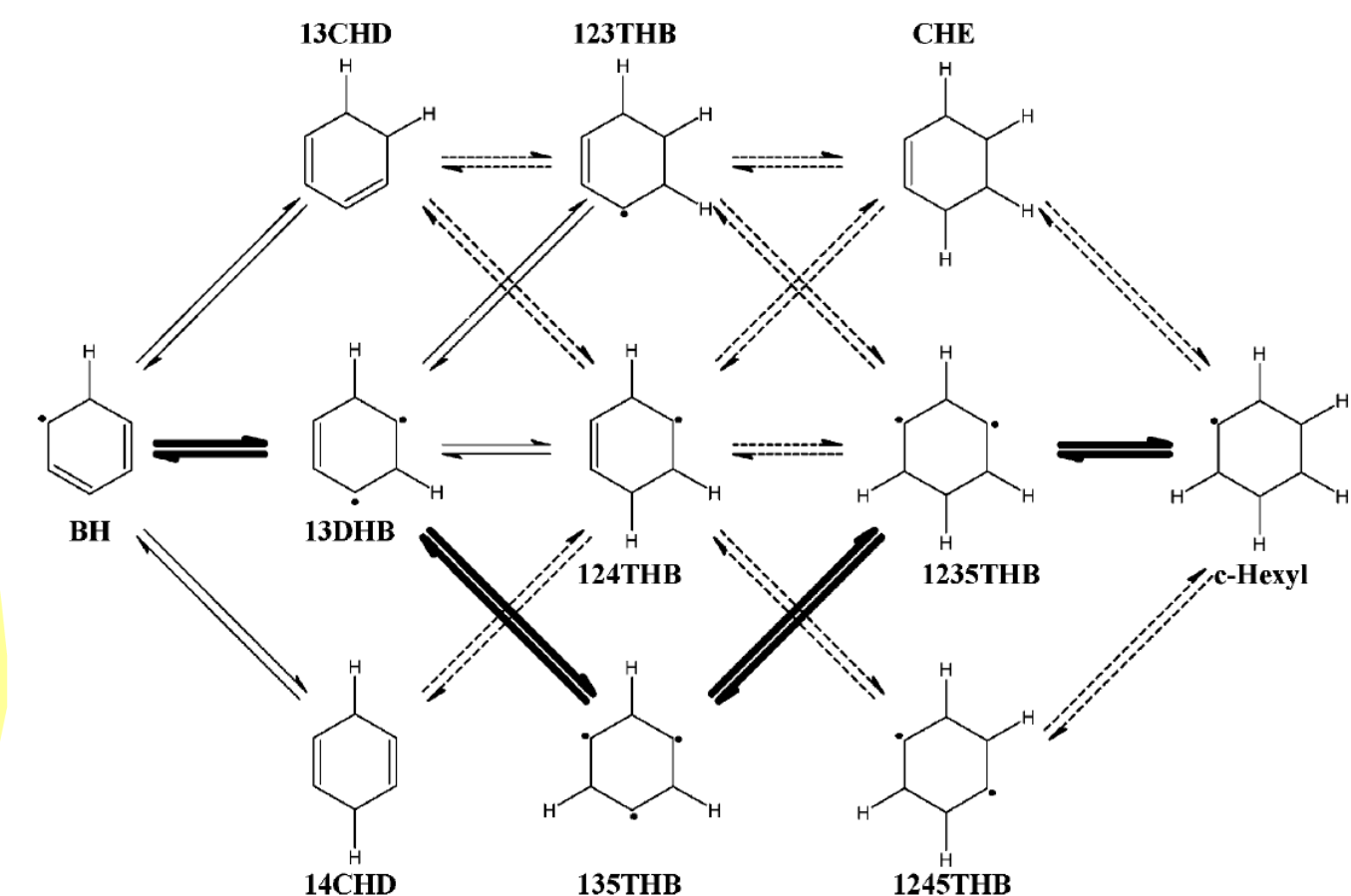
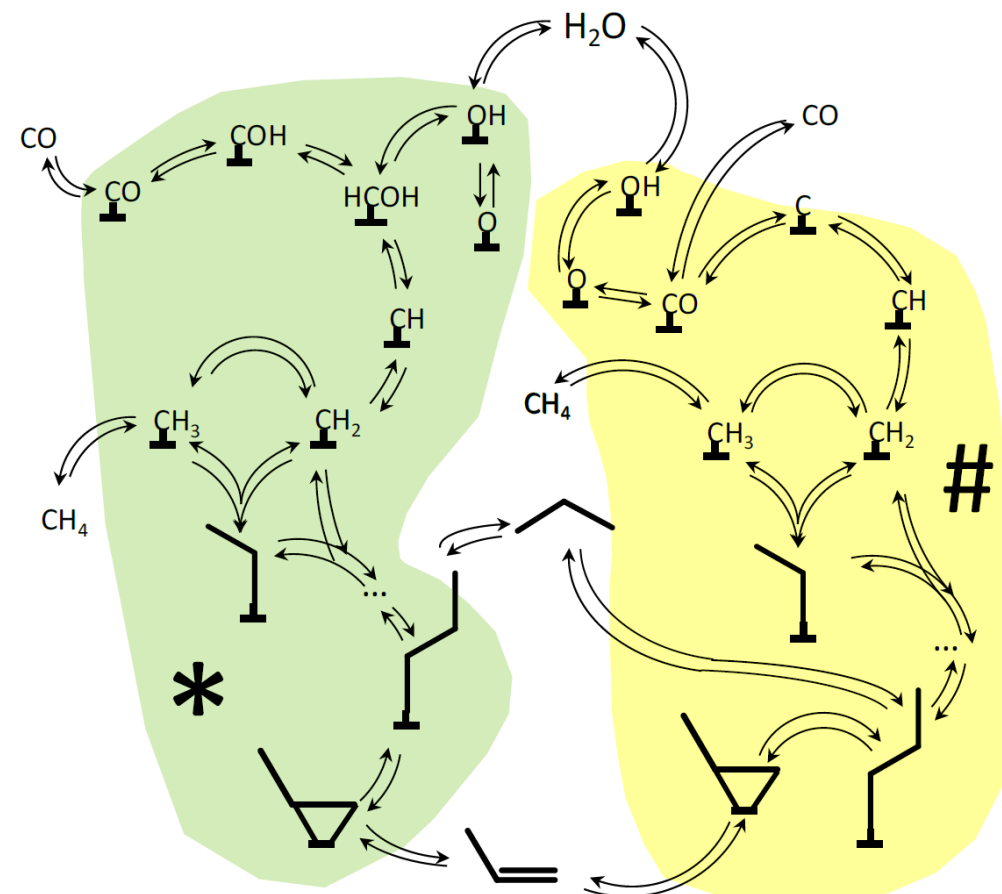
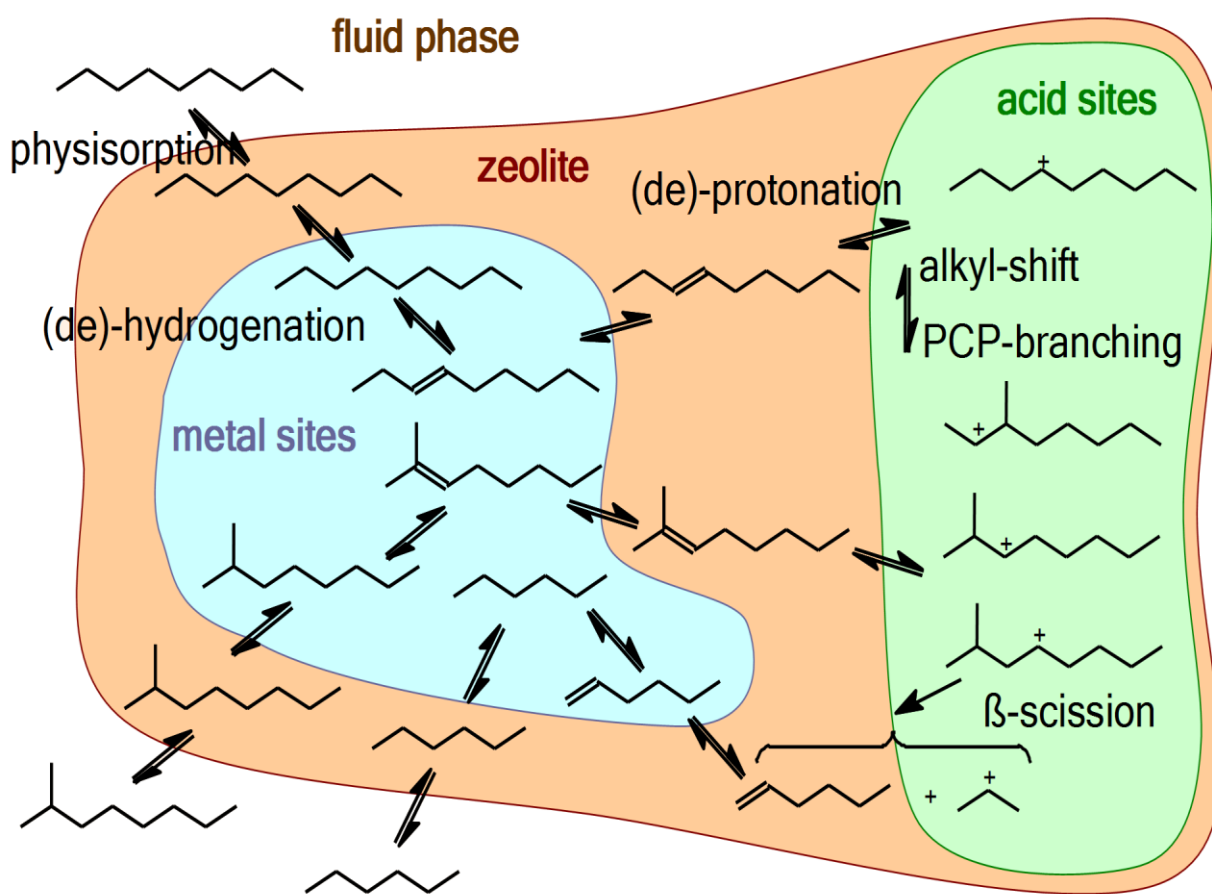
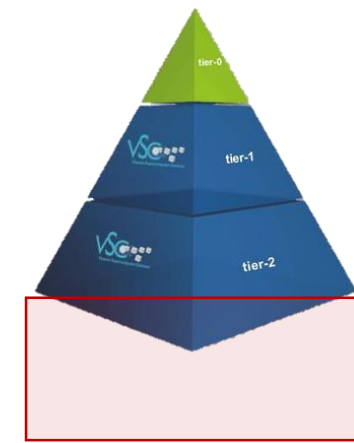
Reaction engineering





# KINETIC MECHANISM DEVELOPMENT - CONTEXT

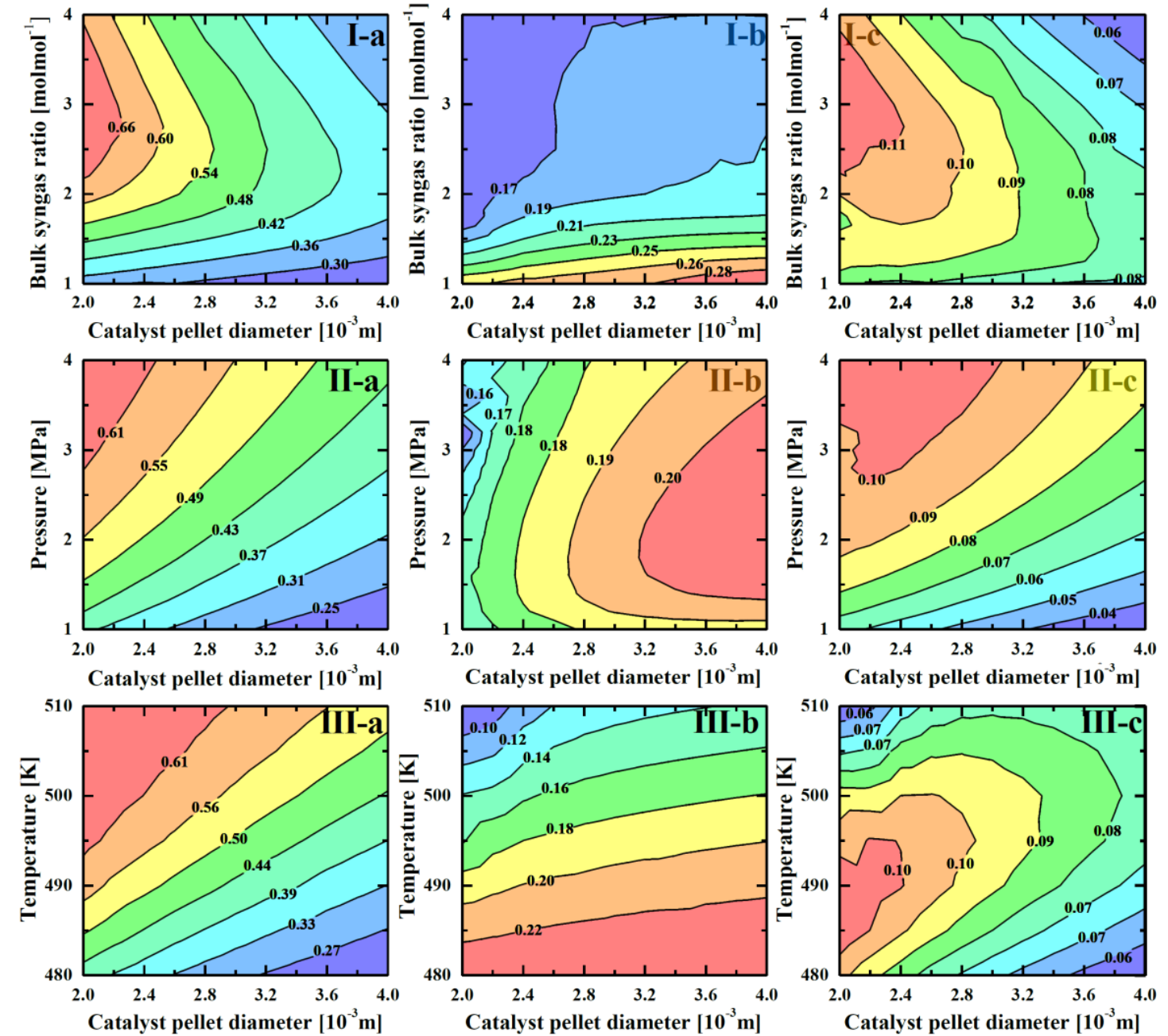
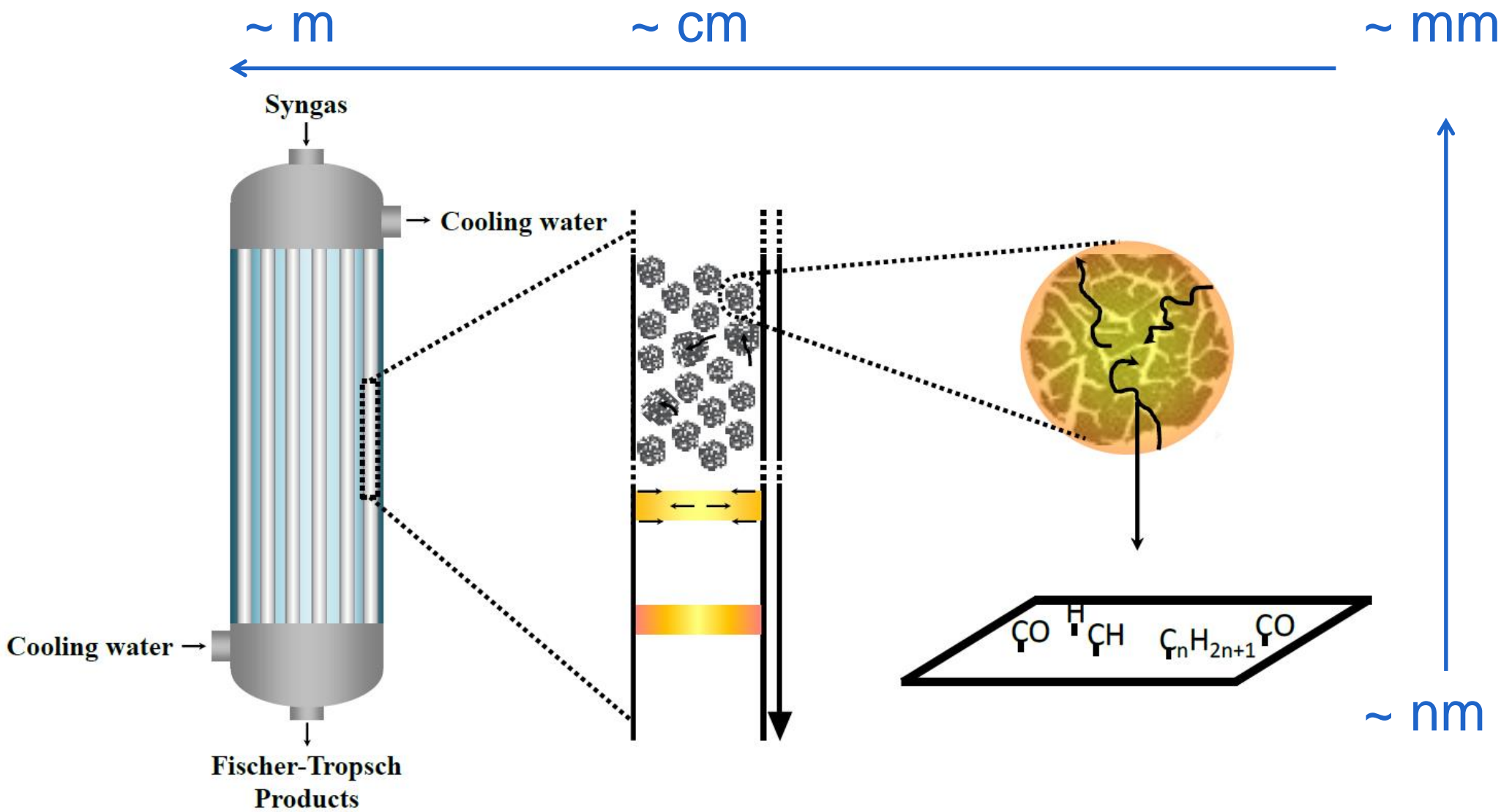
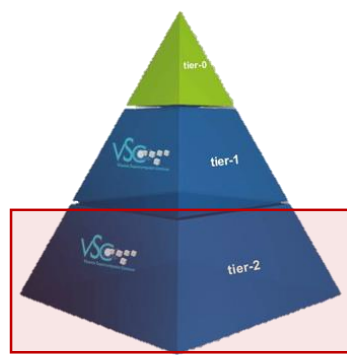
Kinetic networks form the bridge between the molecule scale and the reactor scale  
 Combine information from experiments and theoretical calculations



# KINETIC MECHANISM DEVELOPMENT

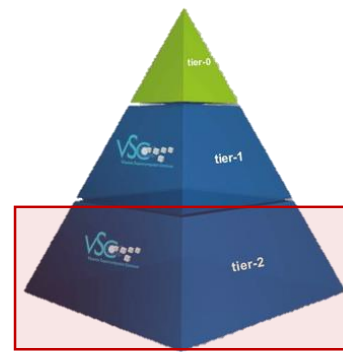
Multiscale modelling of chemical reactors

Goal: *in silico* optimization of full-scale industrial reactor

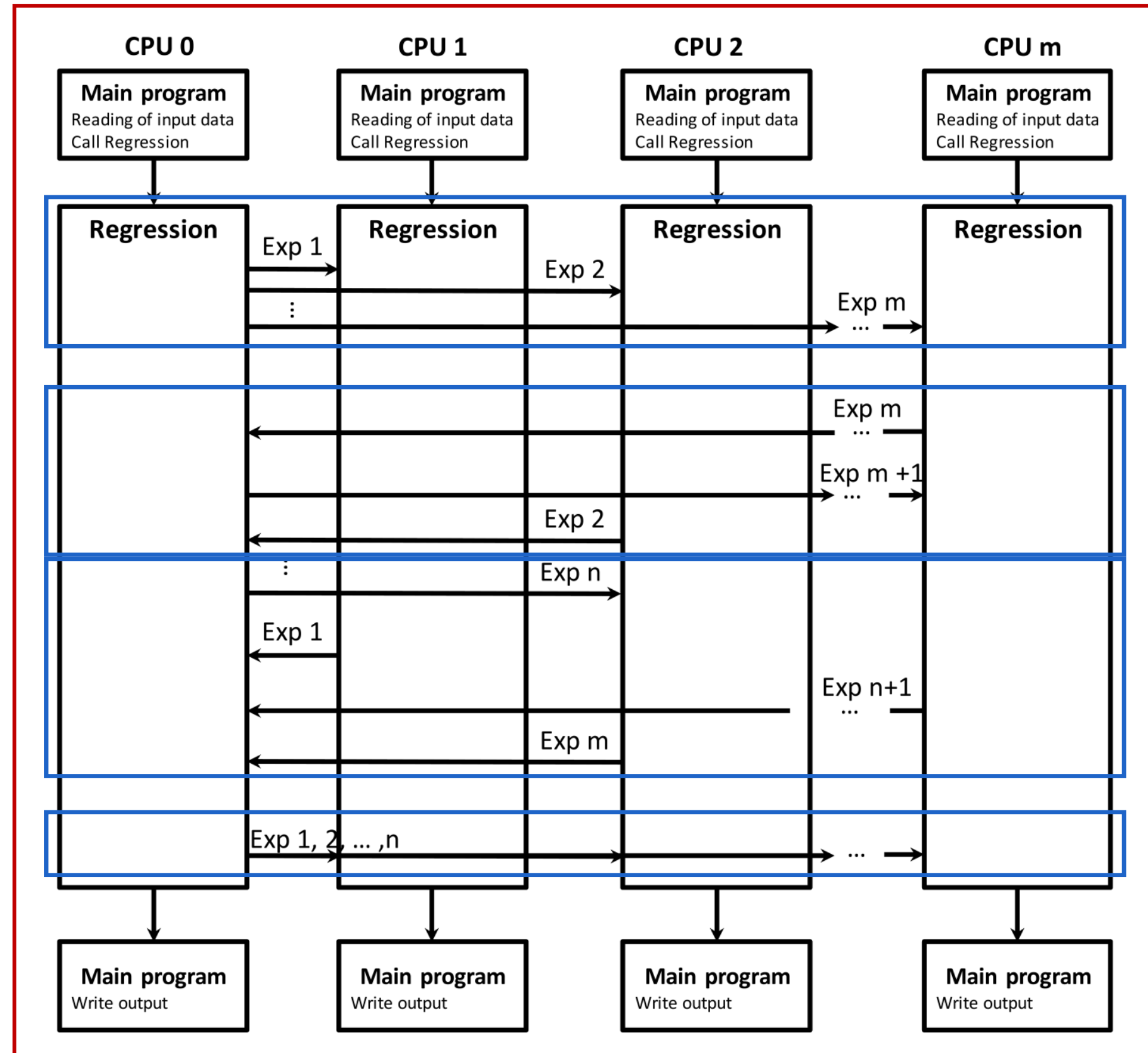
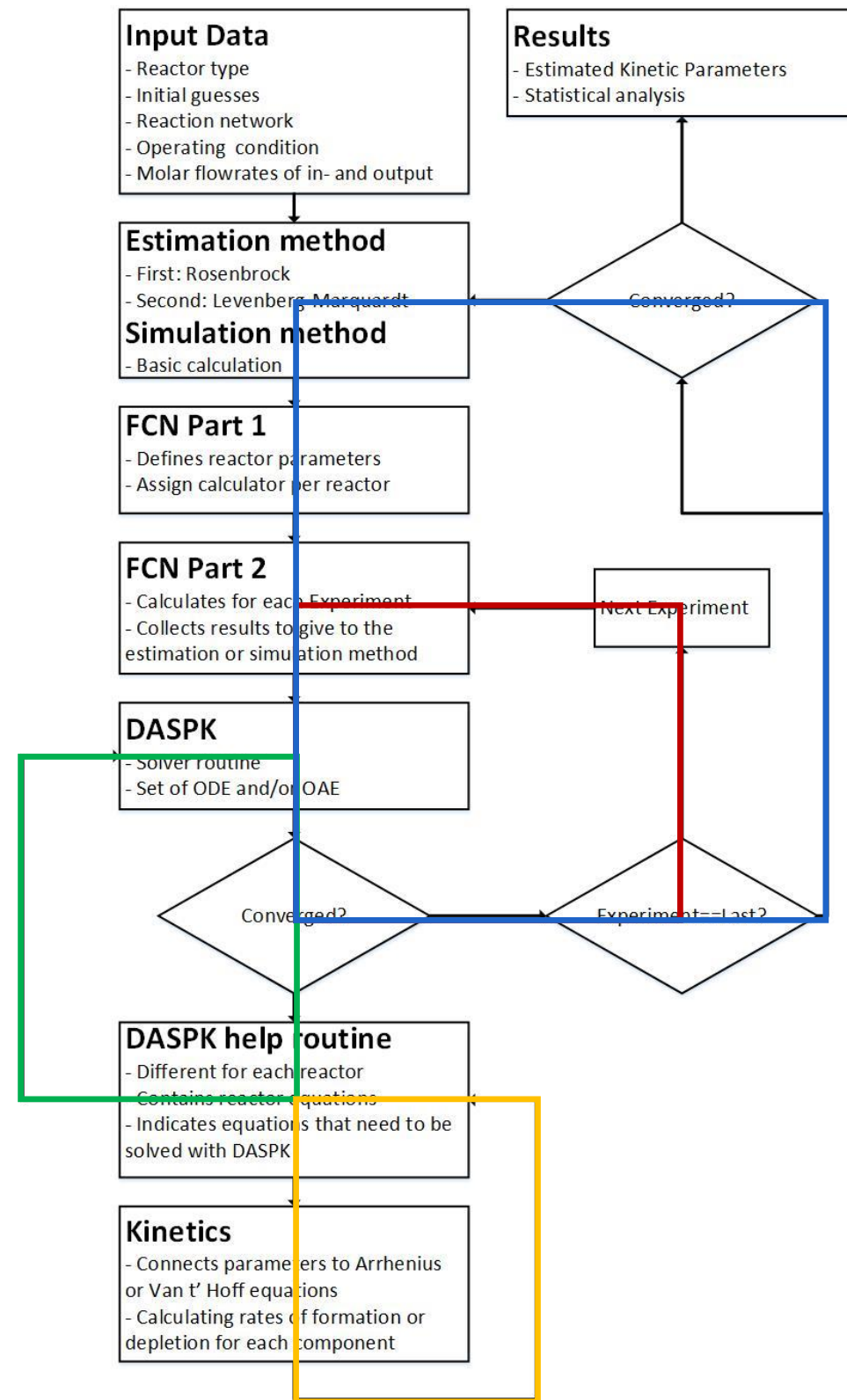




# KINETIC MECHANISM DEVELOPMENT

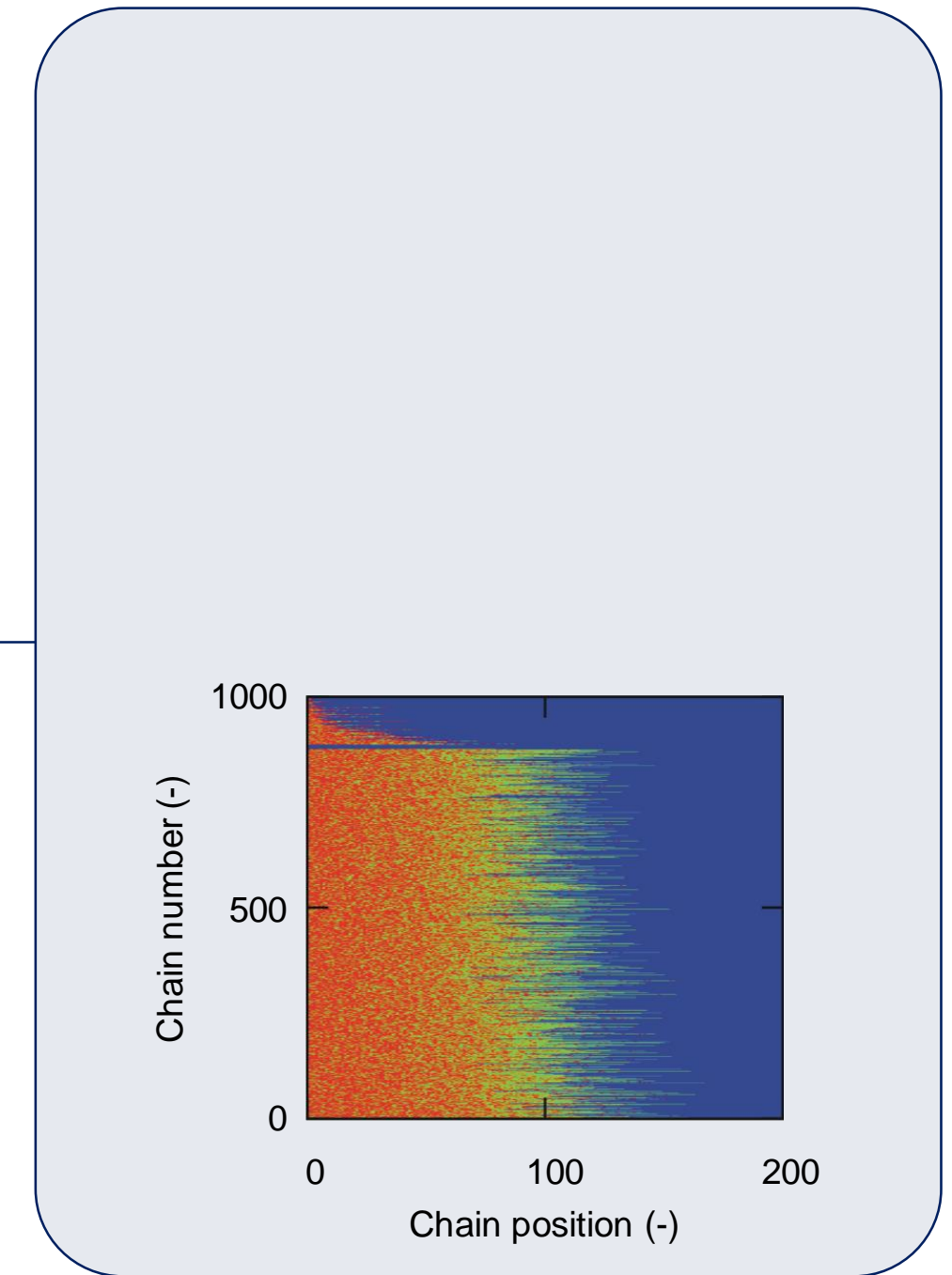
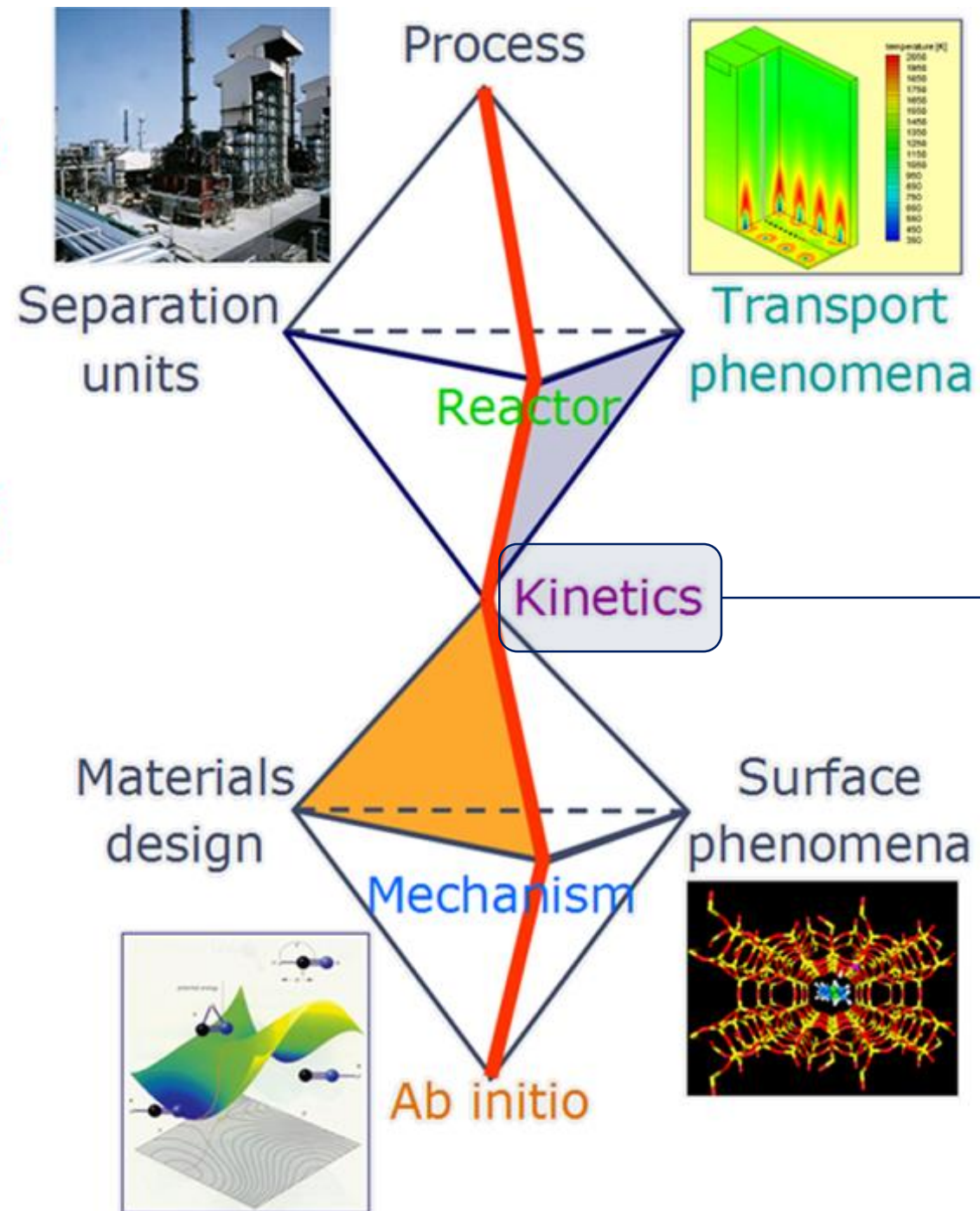


MPI based simulation framework for parallelization of **n independent realizations** on HPC



# MULTISCALE MODELING IN CHEMICAL ENGINEERING

Reaction engineering

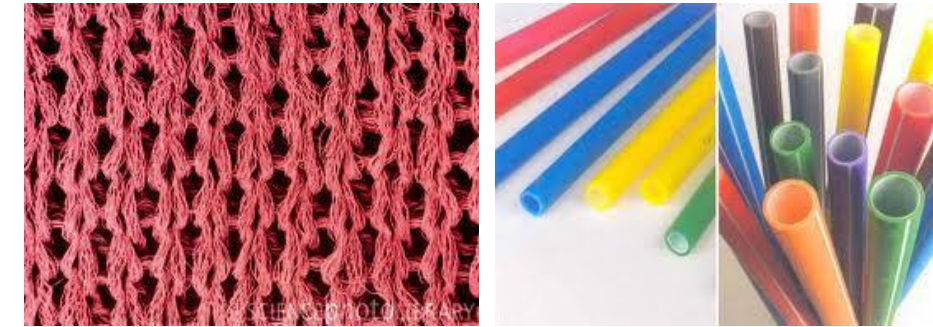
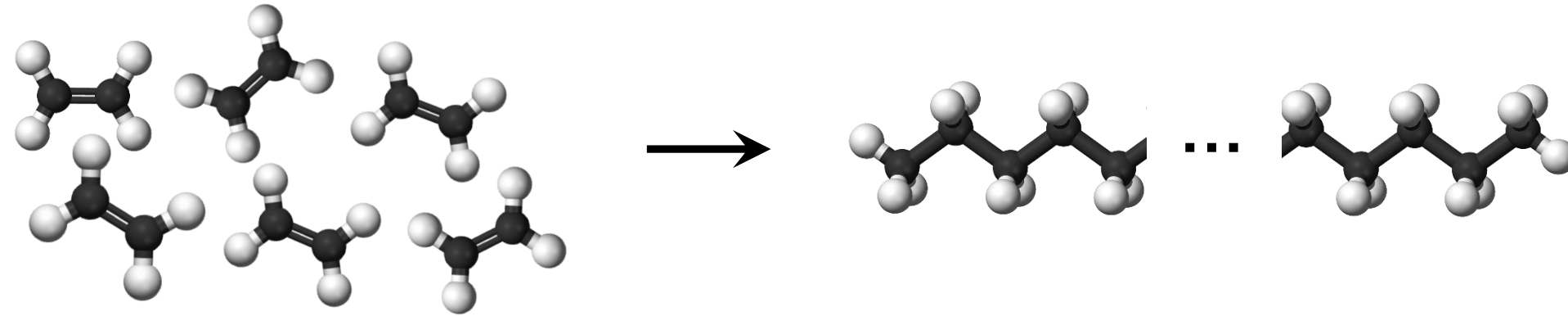
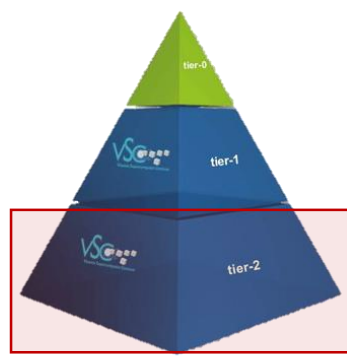




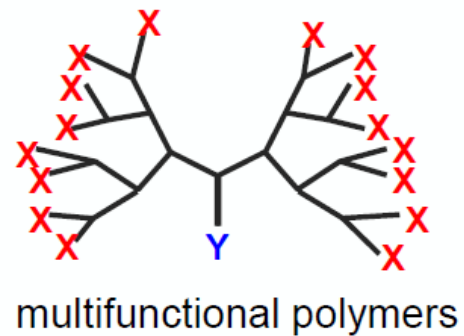
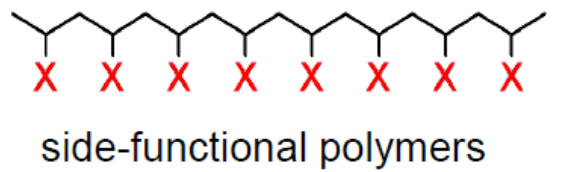
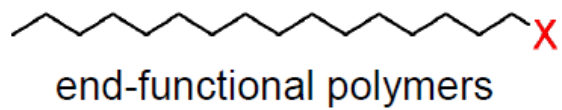
# POLYMER DESIGN – CONTEXT

Model guided design of polymers

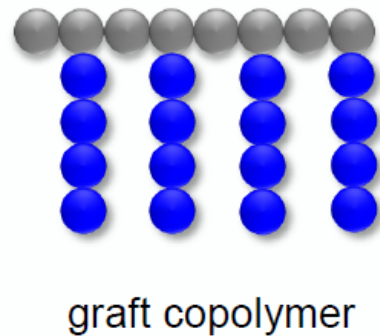
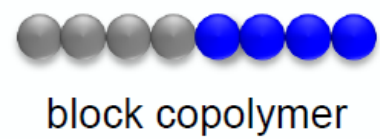
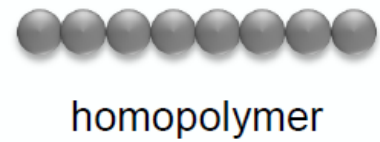
Control over polymer properties starts during polymerization



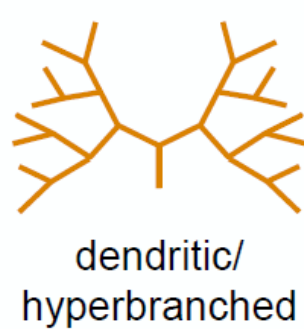
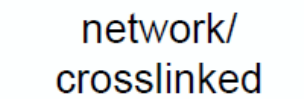
**Functionality:**



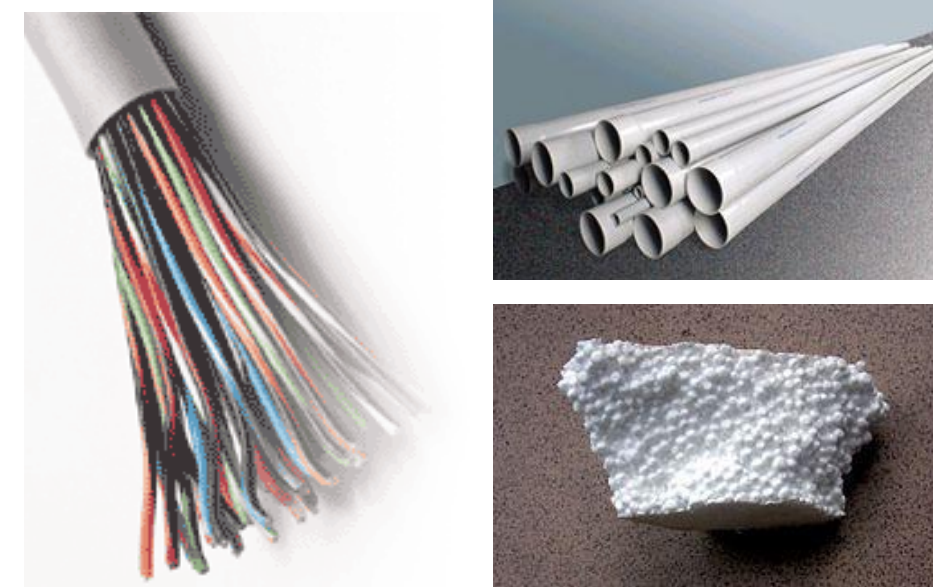
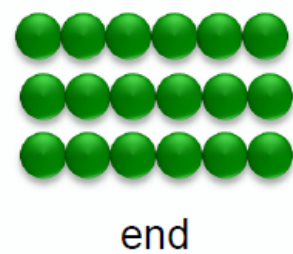
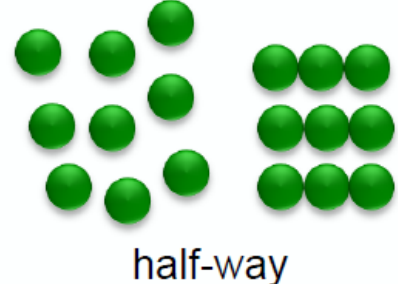
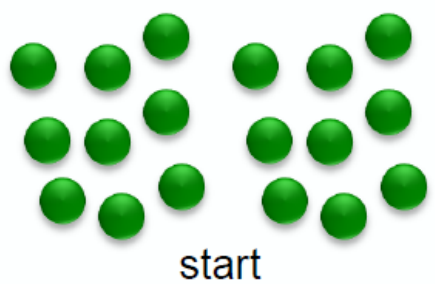
**Composition:**



**Topology:**



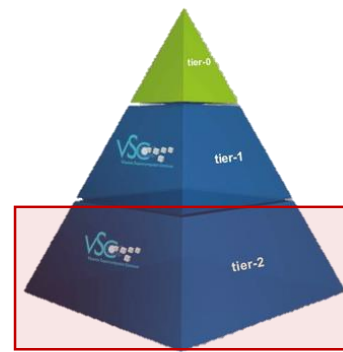
**Chain length:**



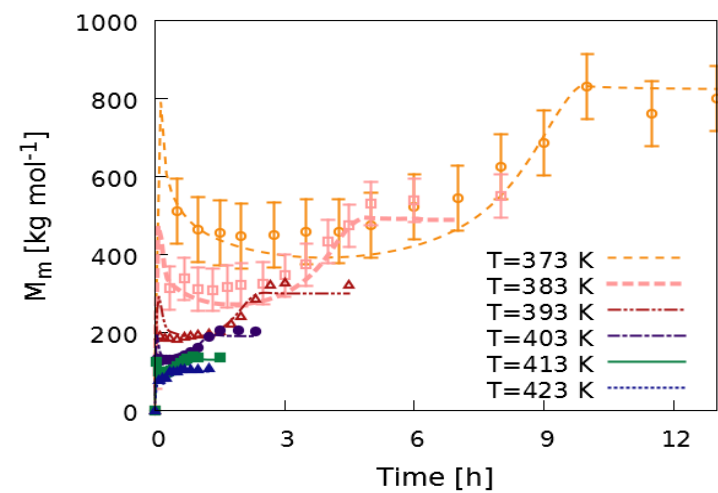
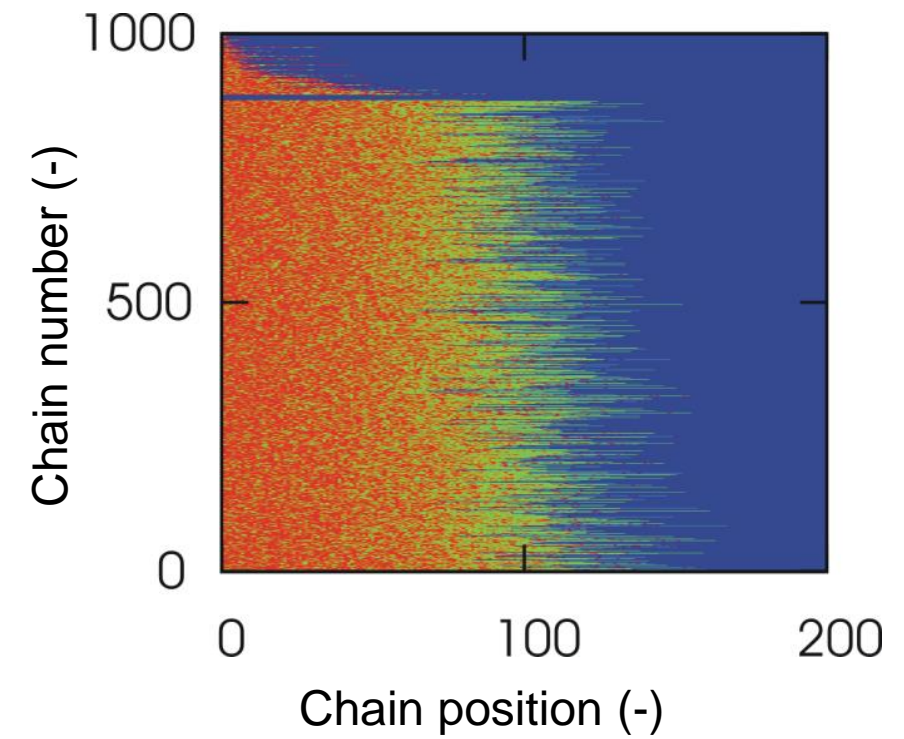
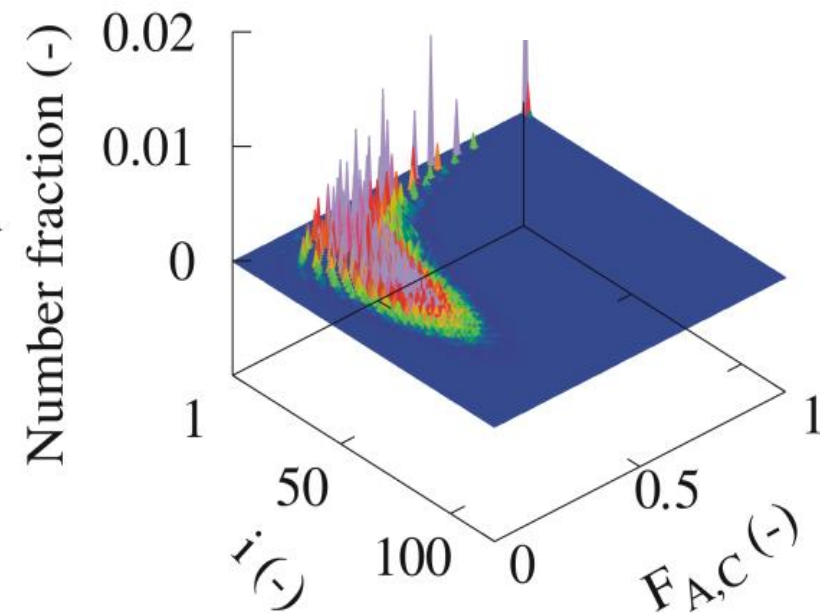
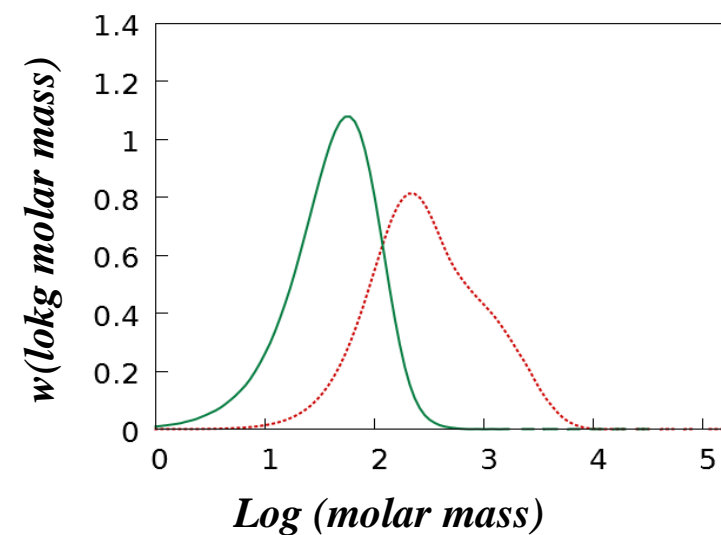
# POLYMER DESIGN – CONTEXT

Model guided design of polymers

Control over polymer properties starts during polymerization



Model complexity



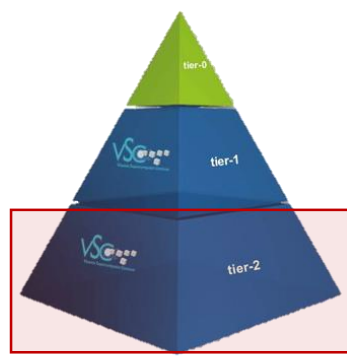
Van Steenberge P.H.M. *et al. Macromolecules* **2011**, 44, 8716  
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**Van Steenberge P.H.M. et al. Chem. Eng. Sci.** **2014**, 11, 185  
 D'hooge D.R. *et al. Polym. Chem.* **2015**, 6, 7081.  
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 D'hooge D.R. *et al. Prog. Polym. Sci.* **2016**, 58, 59



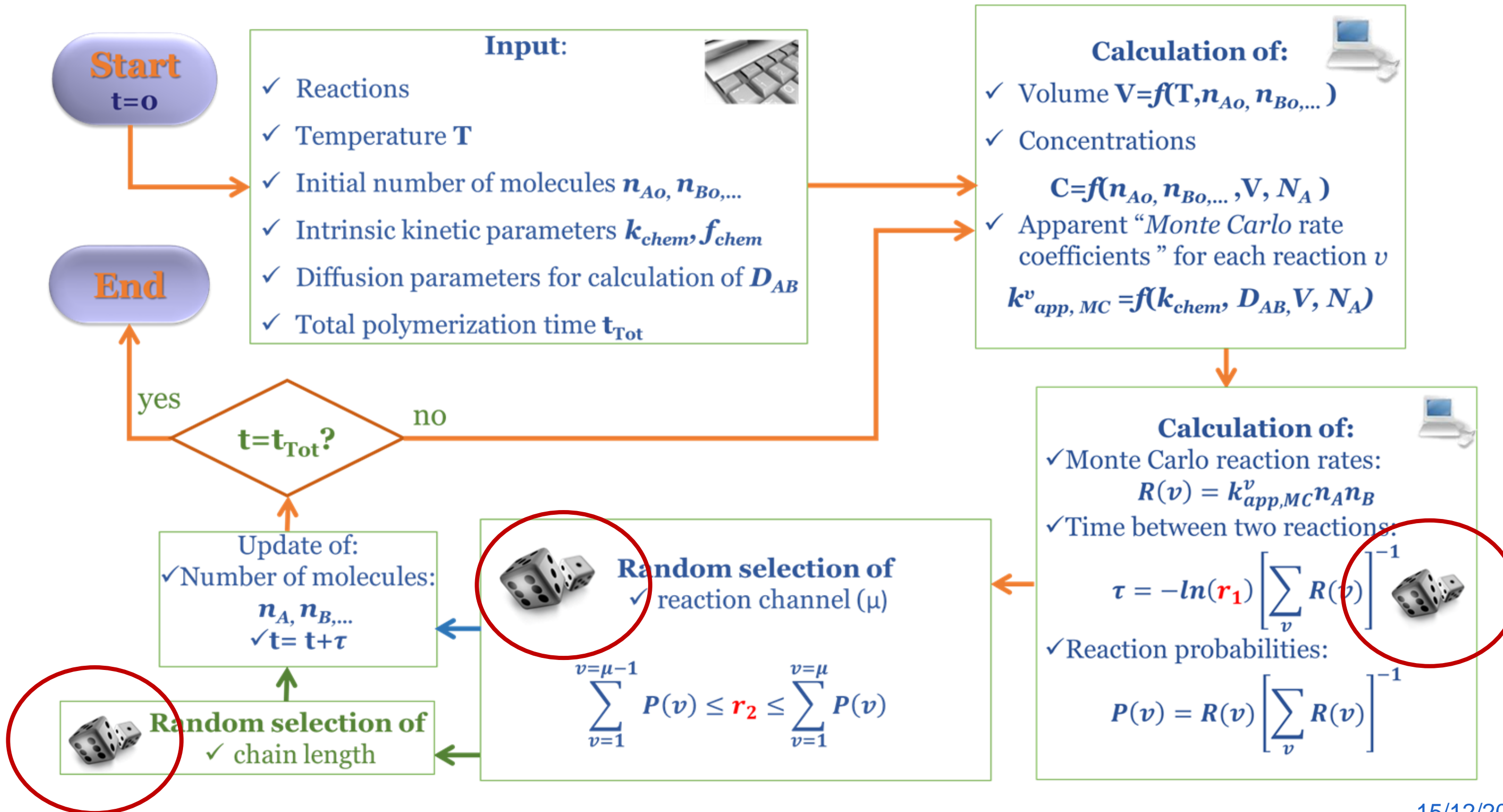
Macromolecular structural detail



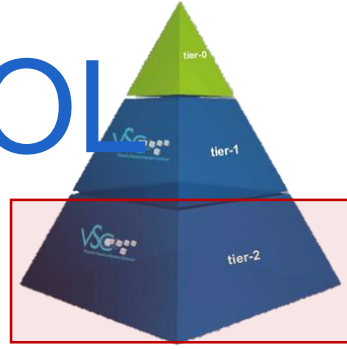
# POLYMER DESIGN – KINETIC MONTE CARLO



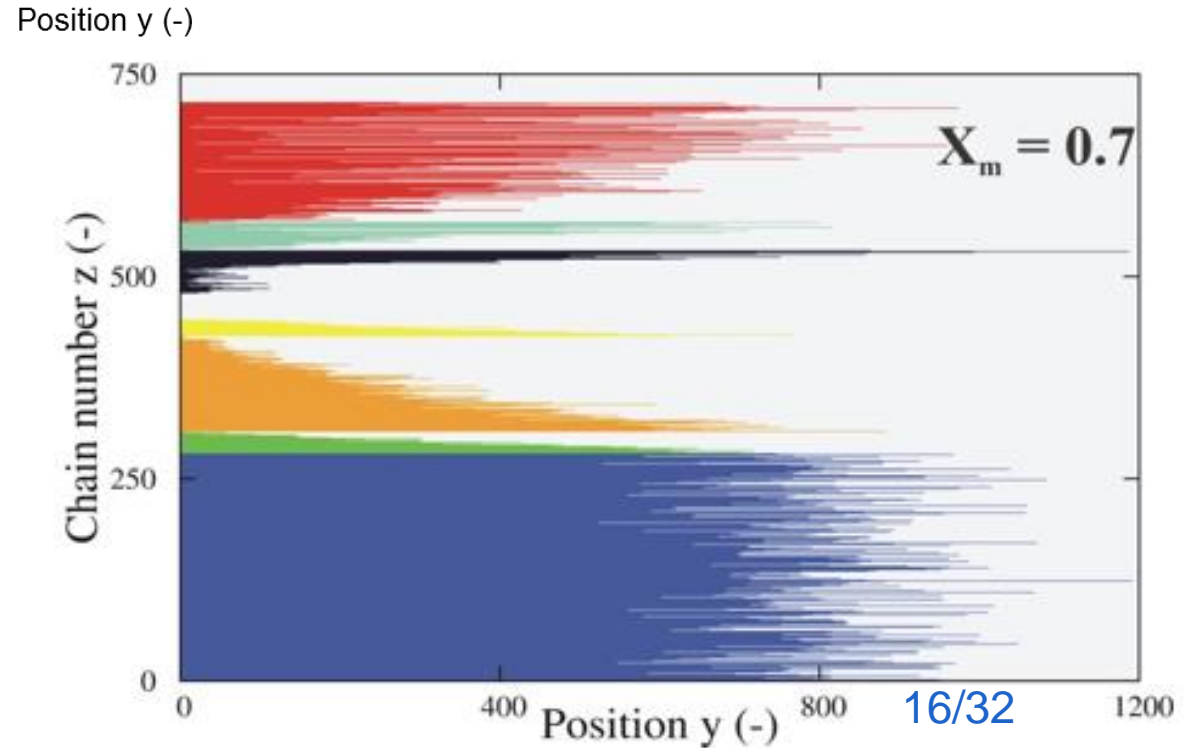
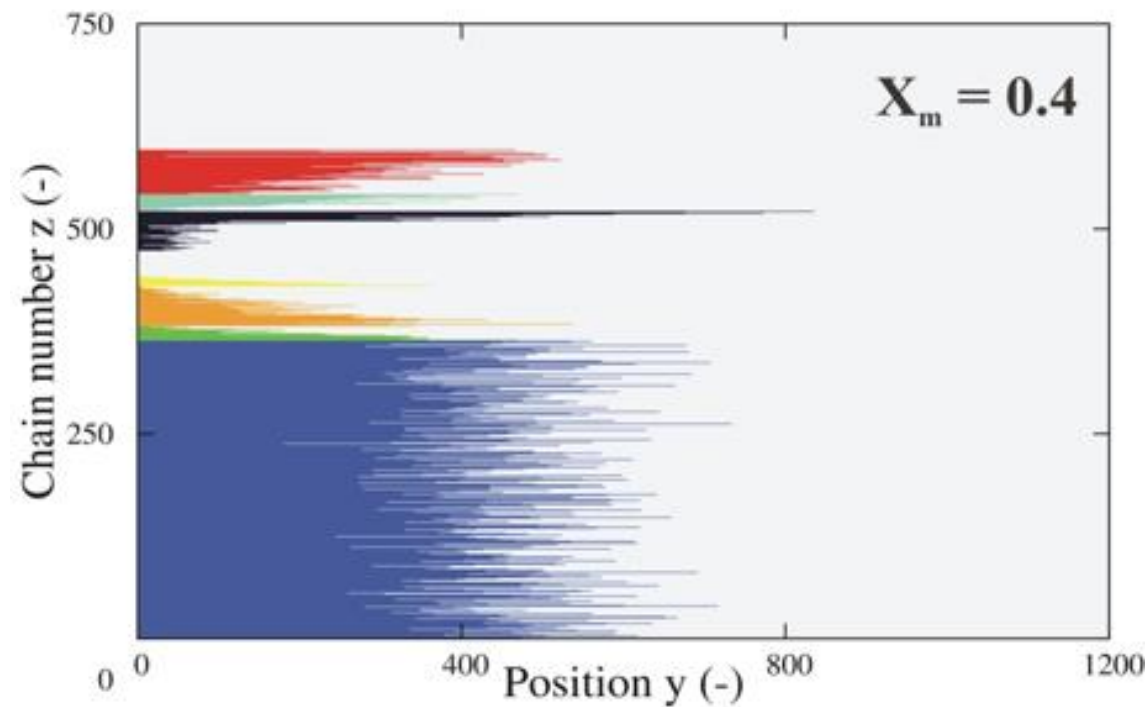
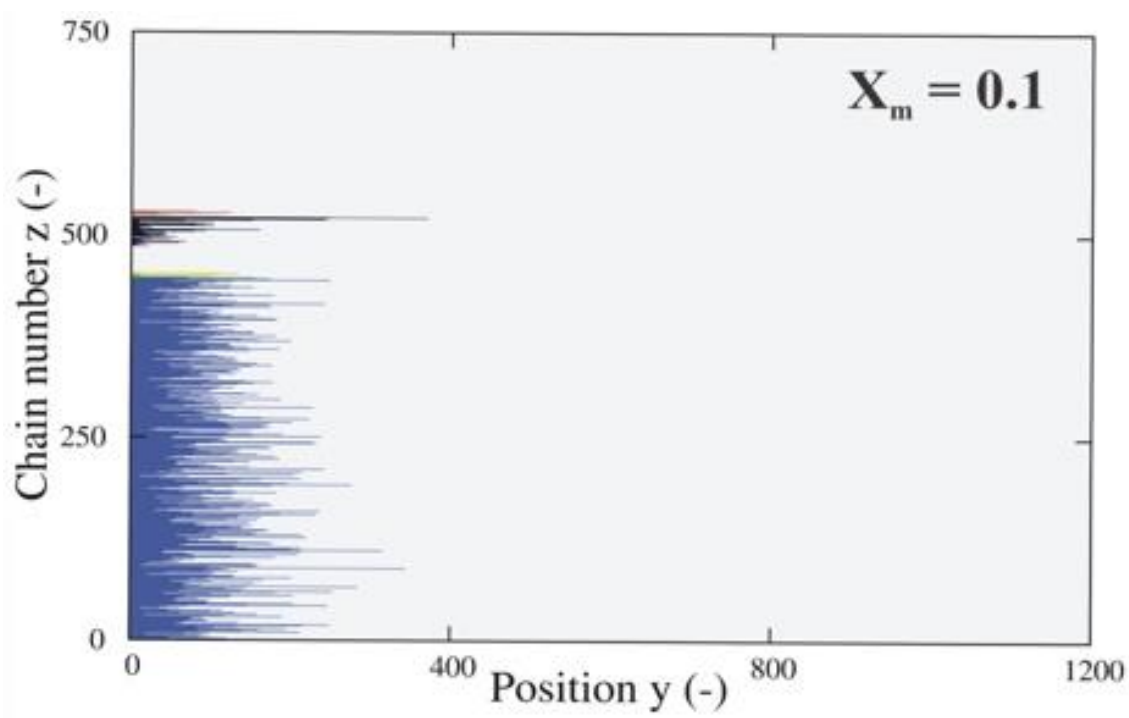
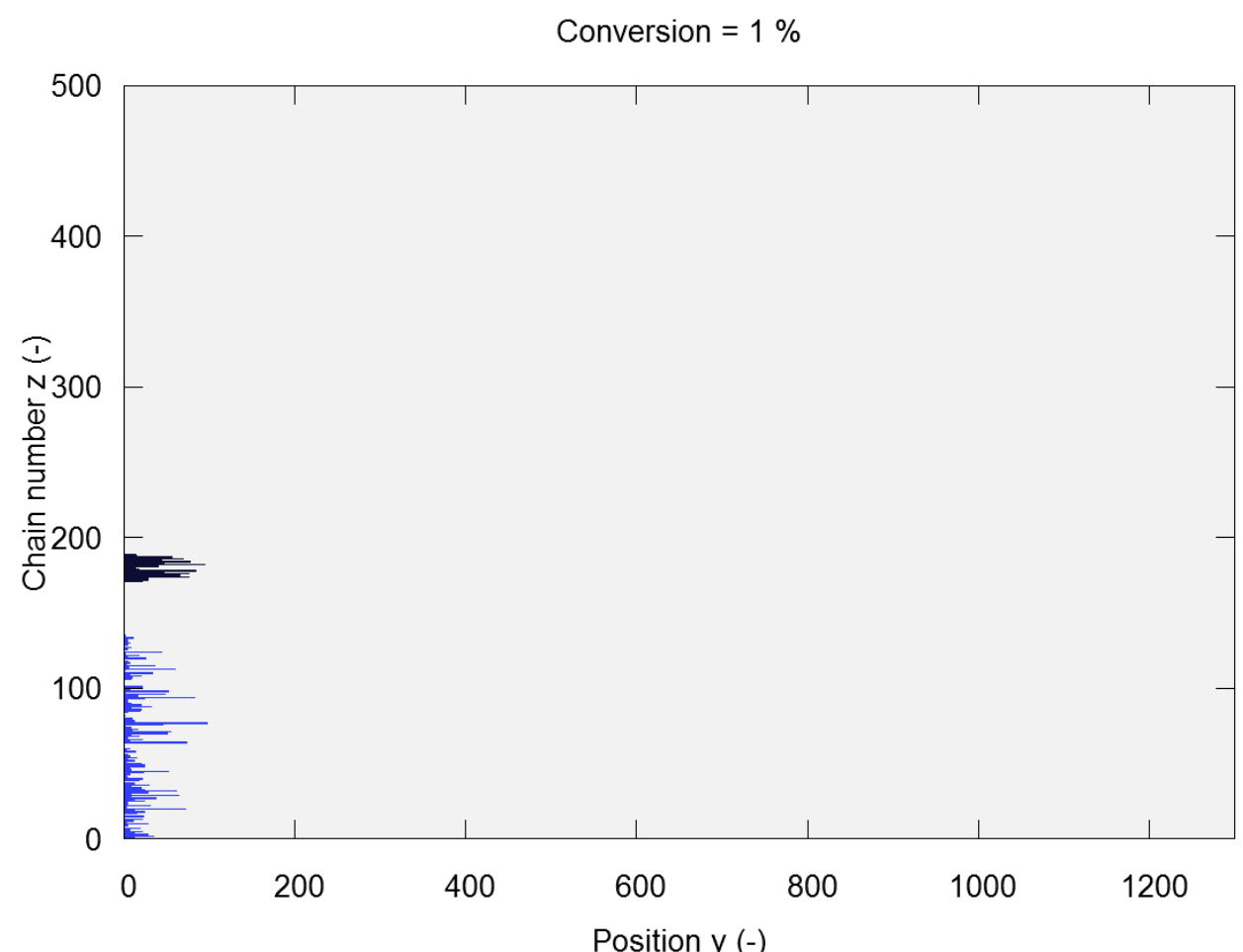
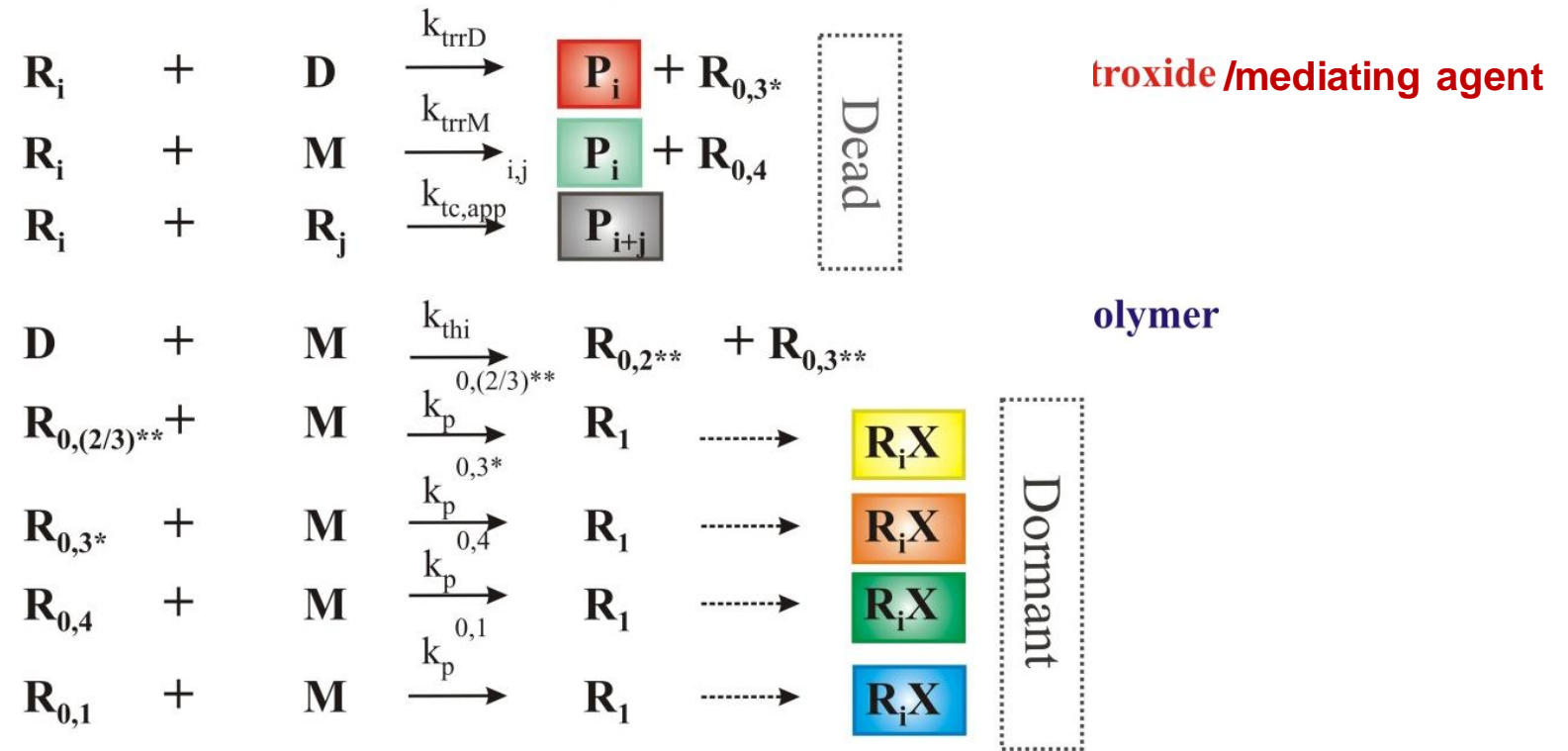
Event-per-event simulation of polymerization: embarrassingly parallel



# POLYMER DESIGN – MICROSTRUCTURAL CONTROL



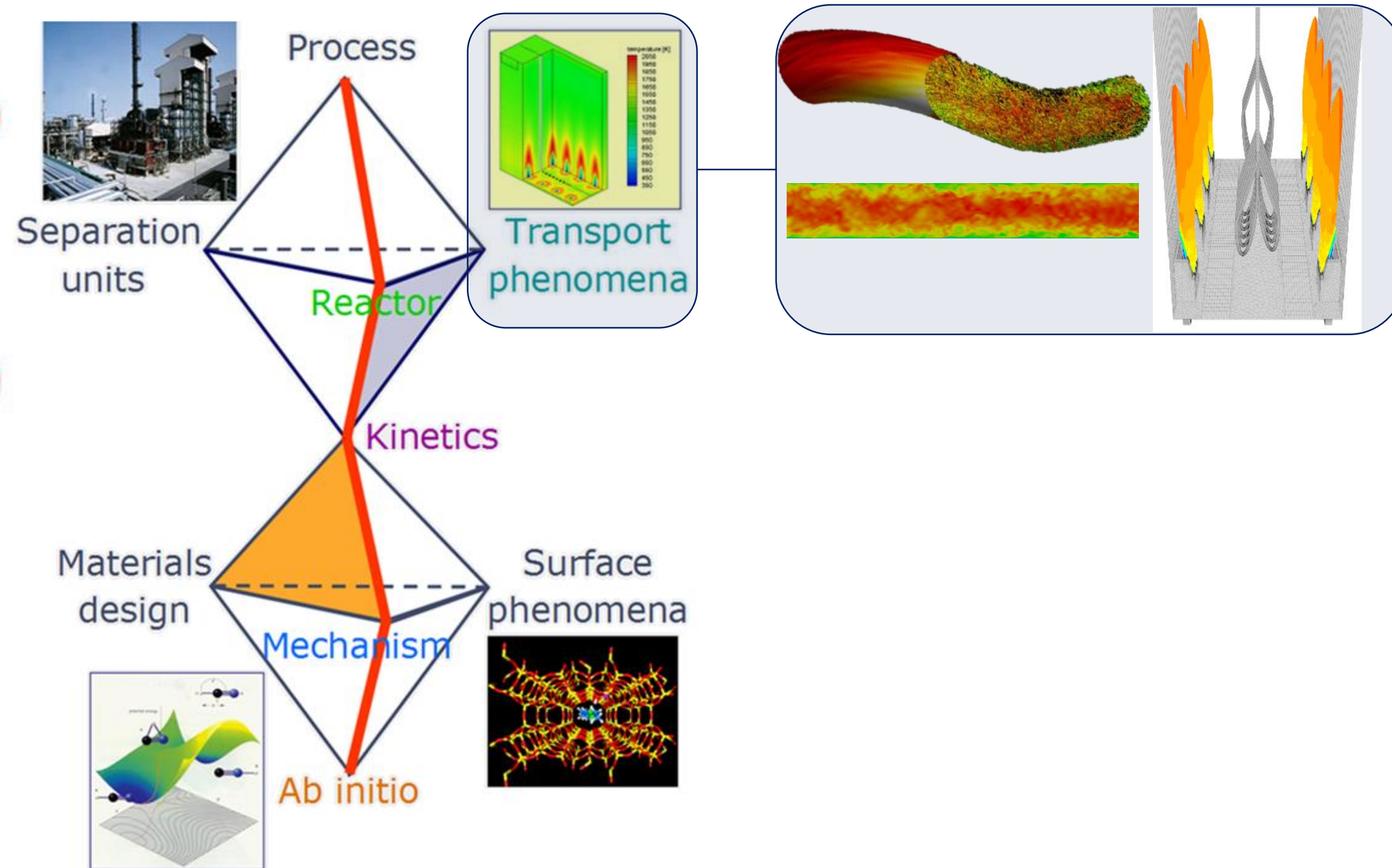
## Event-per-event simulation of polymerization





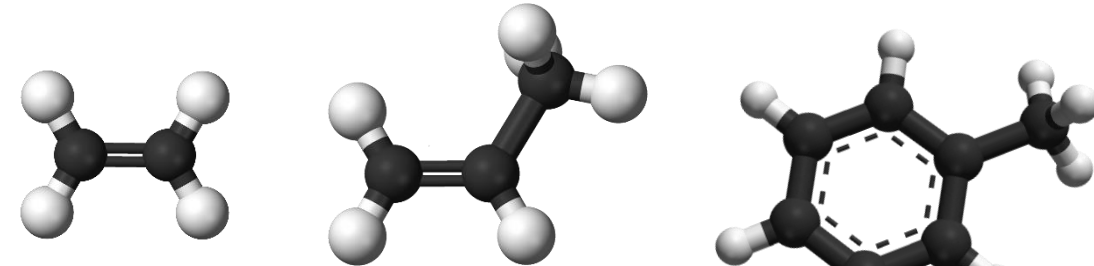
# MULTISCALE MODELING IN CHEMICAL ENGINEERING

Reaction engineering





# STEAM CRACKING



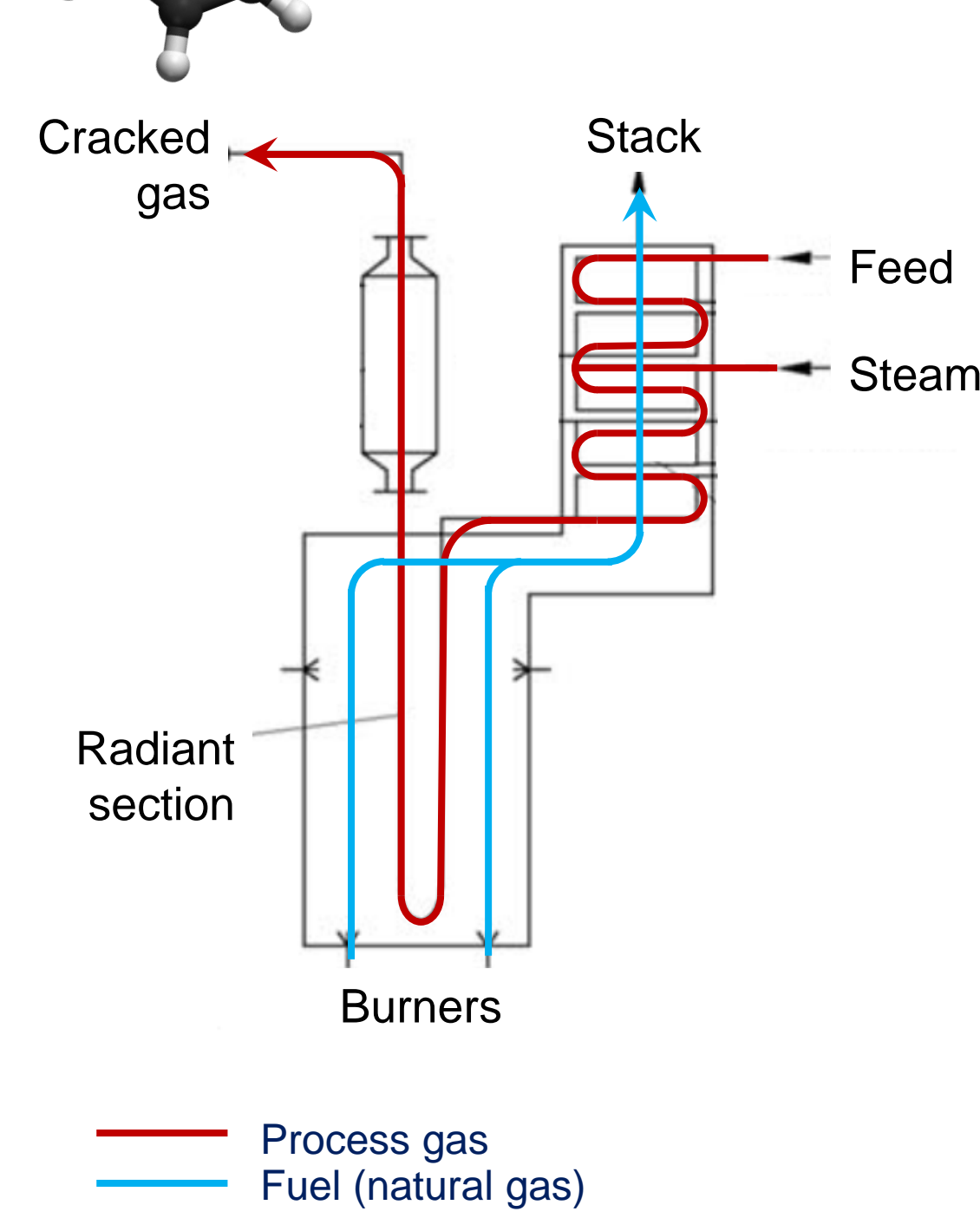
Crude oil

Natural gas

Bio-based feeds

Steam cracking

Consumer goods from chemical industry





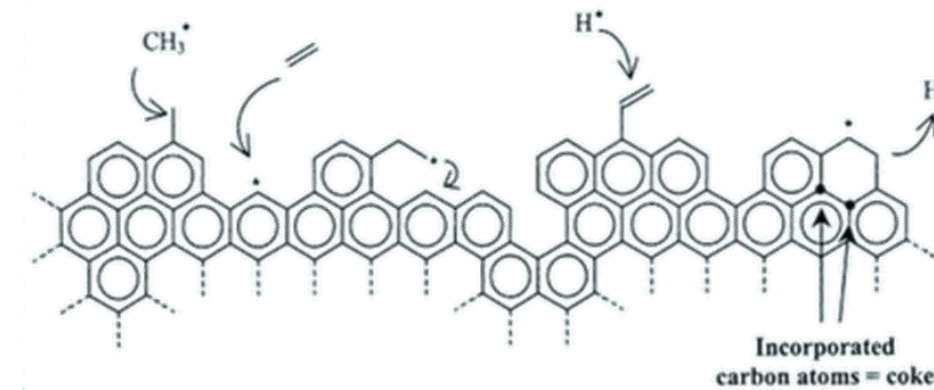
# COKE FORMATION IN STEAM CRACKING

Endothermic process at temperatures of 800–900 °C

Deposition of a carbon layer on the reactor surface

- ➔ Reduced thermal efficiency
- ➔ High pressure causes loss of product selectivity
- ➔ Coil carburization and thermal stress

Coke reduction method: 3D reactor technology



$$r_C = \sum_i c_i \cdot A_i \cdot \exp\left(\frac{-E_{a,i}}{RT_{int}}\right)$$



Coil cracking due to differences in thermal expansion rate

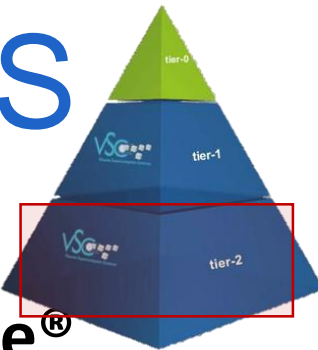


Hot spots due to inhomogeneous coke formation





# ENHANCED STEAM CRACKING REACTOR DESIGNS

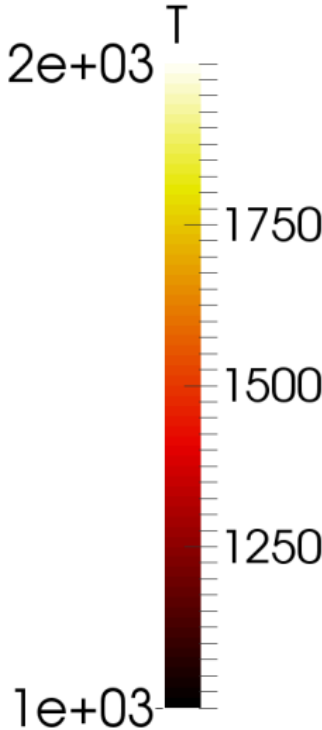
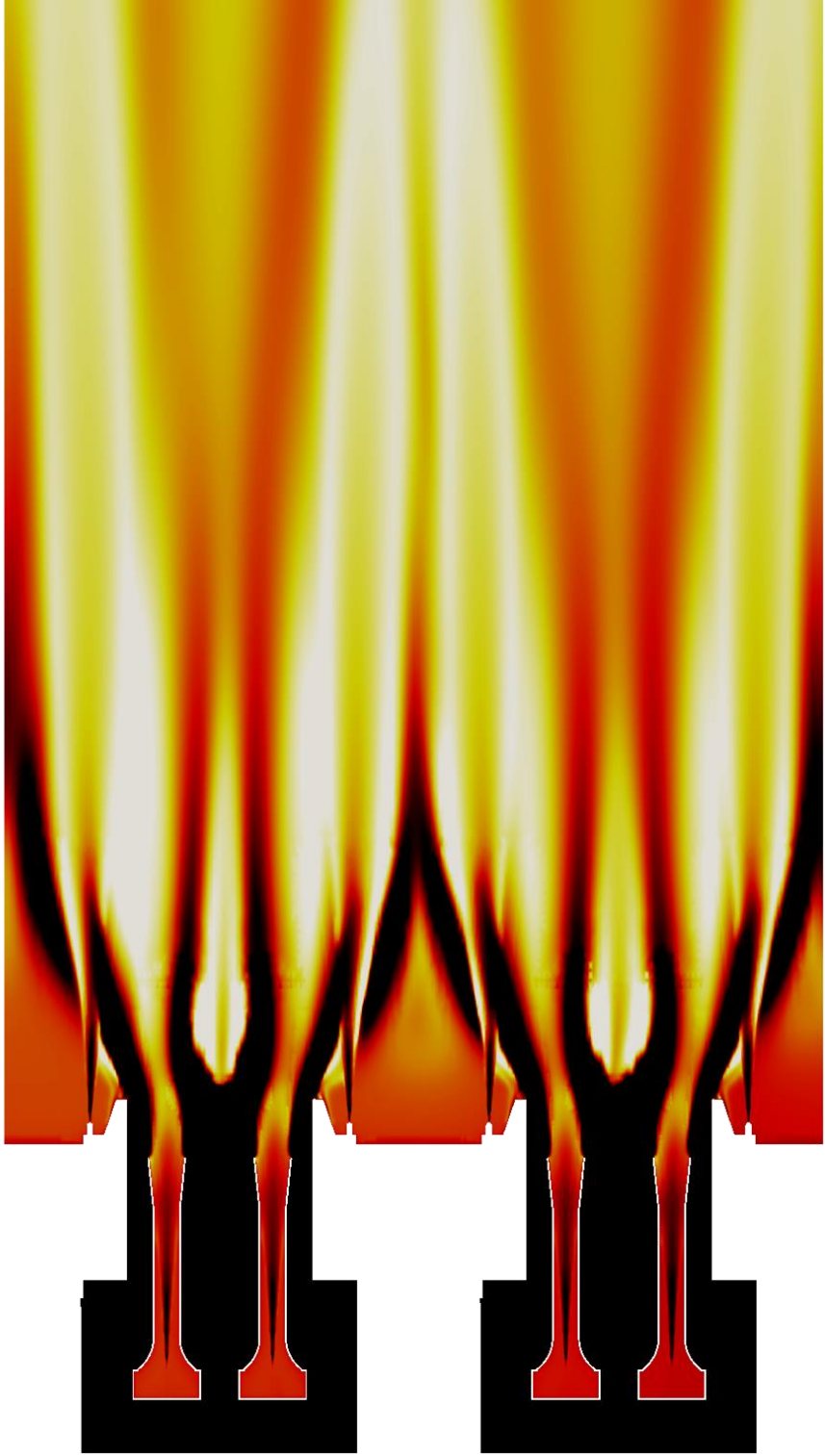
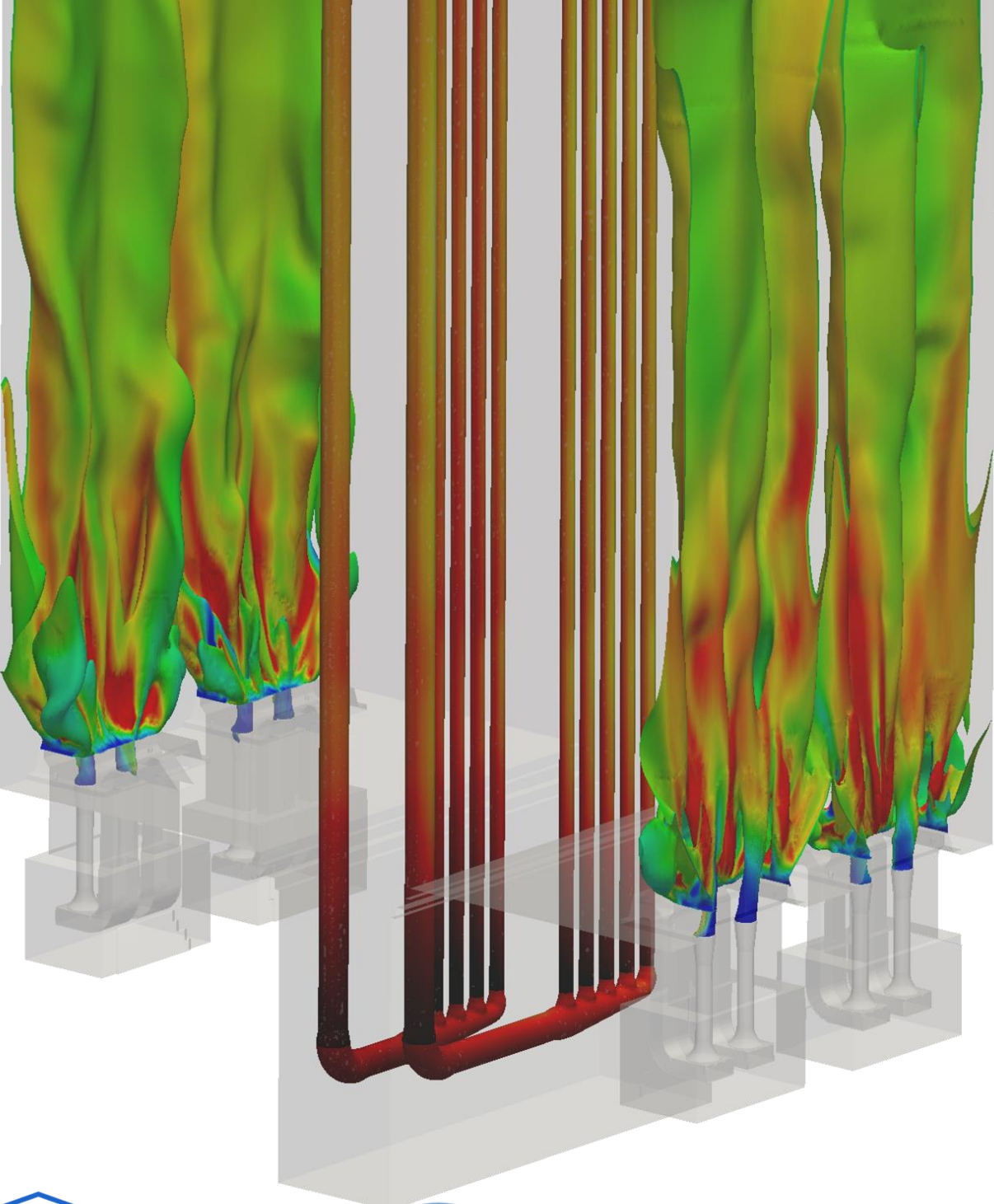
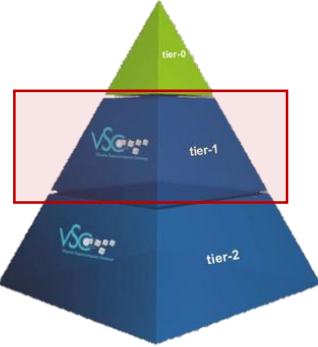


KBR / S+C <b>Straight / rifled fins</b>	Kubota <b>(Slit- / X-) MERT®</b>	Lummus/Sinopec <b>IHT®</b>	Technip <b>Swirl Flow Tube®</b>
1988, 2002, 2011	1996, 2003, 2009	2009	2011





# FURNACE MODELLING USING CFD



# PREDICTION OF TURBULENT REACTIVE FLOWS

PRETREF: <http://www.pretref.ugent.be/>

Contact: dr. Georgios Maragkos ([Georgios.Maragkos@UGent.be](mailto:Georgios.Maragkos@UGent.be))



A project by **Ghent University** which aims to develop a flexible, open source **Large-Eddy Simulations (LES) Computational Fluid Dynamics (CFD)** code-base for multiscale modelling of several multidisciplinary applications.






Objectives defined in the following fields

1. Reduced chemistry
2. Sprays
3. Turbulent steady spray flames
4. Unsteady sprays, in internal combustion engines
5. Fire dynamics

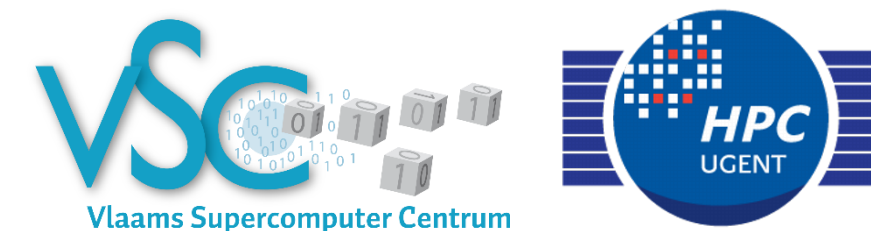


# HPC-UGENT HARDWARE



	#nodes	CPU	Mem/node	Diskspace/node	Network	Online since...	
	<b>Raichu</b>	64	2 x 8-core Intel E5-2670 (Sandy Bridge @ 2.6 GHz)	32 GB	400 GB	GbE	January 2013
	<b>Delcatty</b>	160	2 x 8-core Intel E5-2670 (Sandy Bridge @ 2.6 GHz)	64 GB	400 GB	FDR IB (6.5 GB/s)	November 2013
	<b>Phanpy</b>	16	2 x 12-core Intel E5-2680v3 (Haswell-EP @ 2.5 GHz)	512 GB	3 x 400 GB (SSD)	FDR IB (6.5 GB/s)	July 2015
	<b>Golett</b>	200	2 x 12-core Intel E5-2680v3 (Haswell-EP @ 2.5 GHz)	64 GB	500 GB	FDR-10 IB (5.0 GB/s)	July 2015
	<b>Swalot</b>	128	2 x 10-core Intel E5-2660v3 (Haswell-EP @ 2.5 GHz)	128 GB	1000 GB	FDR IB (6.5 GB/s)	August 2016

# HPC-UGENT +TIER-1 HARDWARE



	#nodes	CPU	Mem/node	Diskspace/node	Network	Online since...
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**Raichu**

64	2 x 8-core Intel E5-2670 (Sandy Bridge @ 2.6 GHz)	32 GB	400 GB	GbE	January 2013
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**Delcatty**

160	2 x 8-core Intel E5-2670 (Sandy Bridge @ 2.6 GHz)	64 GB	400 GB	FDR IB (6.5 GB/s)	November 2013
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**Phanpy**

16	2 x 12-core Intel E5-2680v3 (Haswell-EP @ 2.5 GHz)	512 GB	3 x 400 GB (SSD)	FDR IB (6.5 GB/s)	July 2015
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**Golett**

200	2 x 12-core Intel E5-2680v3 (Haswell-EP @ 2.5 GHz)	64 GB	500 GB	FDR-10 IB (5.0 GB/s)	July 2015
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**Swalot**

128	2 x 10-core Intel E5-2660v3 (Haswell-EP @ 2.5 GHz)	128 GB	1000 GB	FDR IB (6.5 GB/s)	August 2016
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**Tier-1a Muk**

528	2 x 8-core Intel E5-2670 (Sandy Bridge @ 2.6 GHz)	64 GB	500 GB	FDR IB (6.5 GB/s)	July 2013
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**Tier-1b BrENIAC**

580	2 x 14-core Intel E5-2680v4 (Broadwell @ 2.4 GHz)	128 GB/256 GB	128 GB (SSD)	EDR IB (11.75 GB/s)	January 2017
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# THE NEXT STAGE: USING TIER-1

Information on project access

<https://www.vscentrum.be/en/access-and-infrastructure/project-access-tier1>.

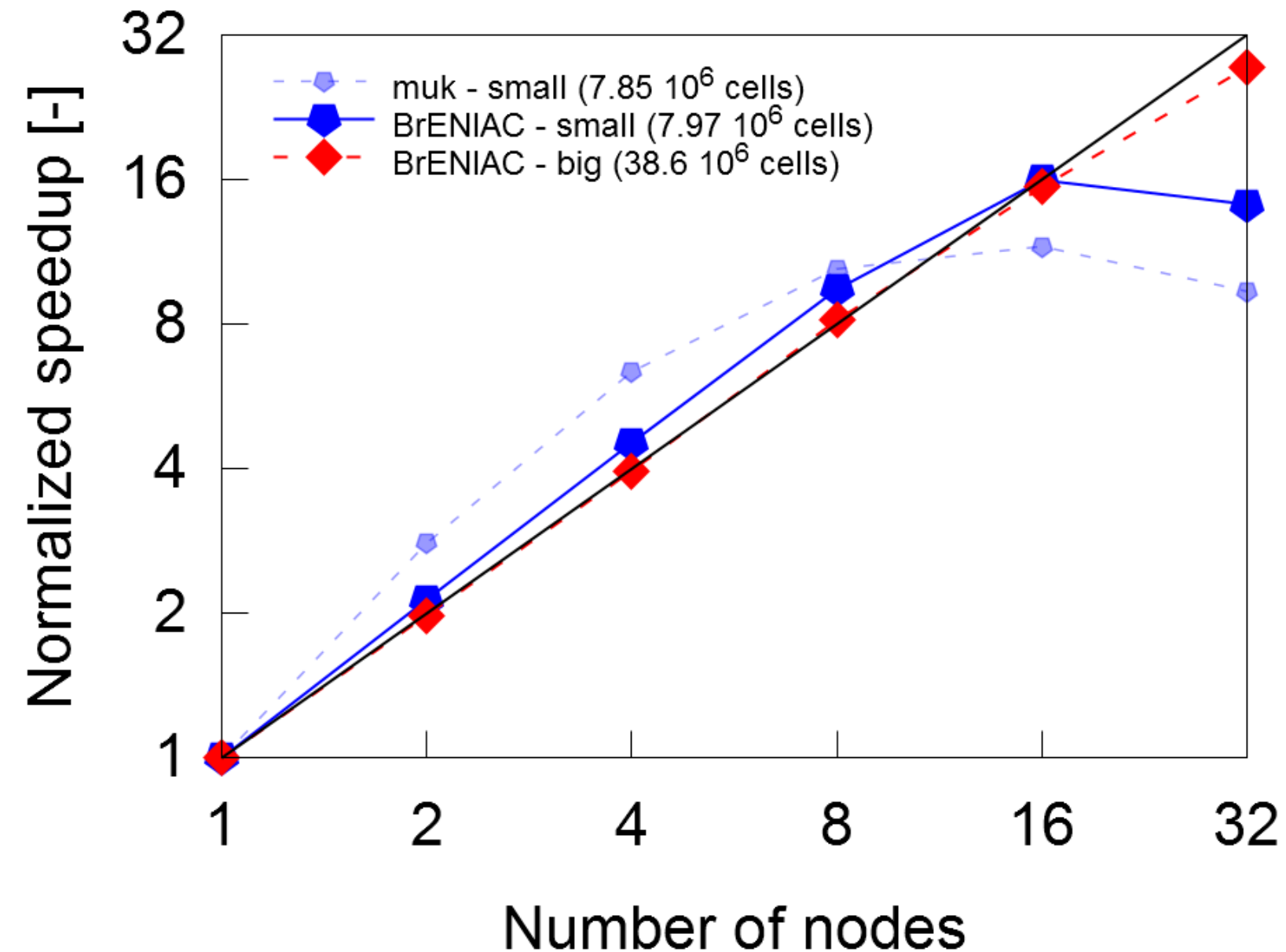


- Project proposal in a [single document](#) (maximum 17 pages)
- Scientific relevance is demonstrated by framing the calculation time in an approved project
- Next cut-off date for proposals: **February 5th, 2018**.
- Possibility of requesting a [starting grant](#) (continuous call)
- FWO bears all the cost but the number of nodedays is limited
  
- Nearly identical in use compared to UGent Tier-2 machines (modules, scheduler, job-scripts)
- Major difference: accounting system to keep track of consumed nodedays
- Connection between BrENIAC (@KULeuven) and UGent via BelNet (1 Gbps).
- Request software (well in advance) via [hpcinfo@icts.kuleuven.be](mailto:hpcinfo@icts.kuleuven.be)

# SCALING OF OPENFOAM ON TIER-1

## Scaling on Tier-1b

muk – small: OpenFOAM/2.2.0-ictce-4.1.13 / Intel MPI v4.1.0  
BrENIAC – small: OpenFOAM/2.2.0-intel-2016a / Intel MPI v5.1.3  
BrENIAC – big: OpenFOAM/2.2.0-intel-2016a / Intel MPI v5.1.3



**Better scaling** compared to Tier-1a – Muk

**Fast interconnect** (EDR IB) reduces wall-clock time and maintains efficiency while scaling on more cores

**Bottleneck:** pre- and postprocessing

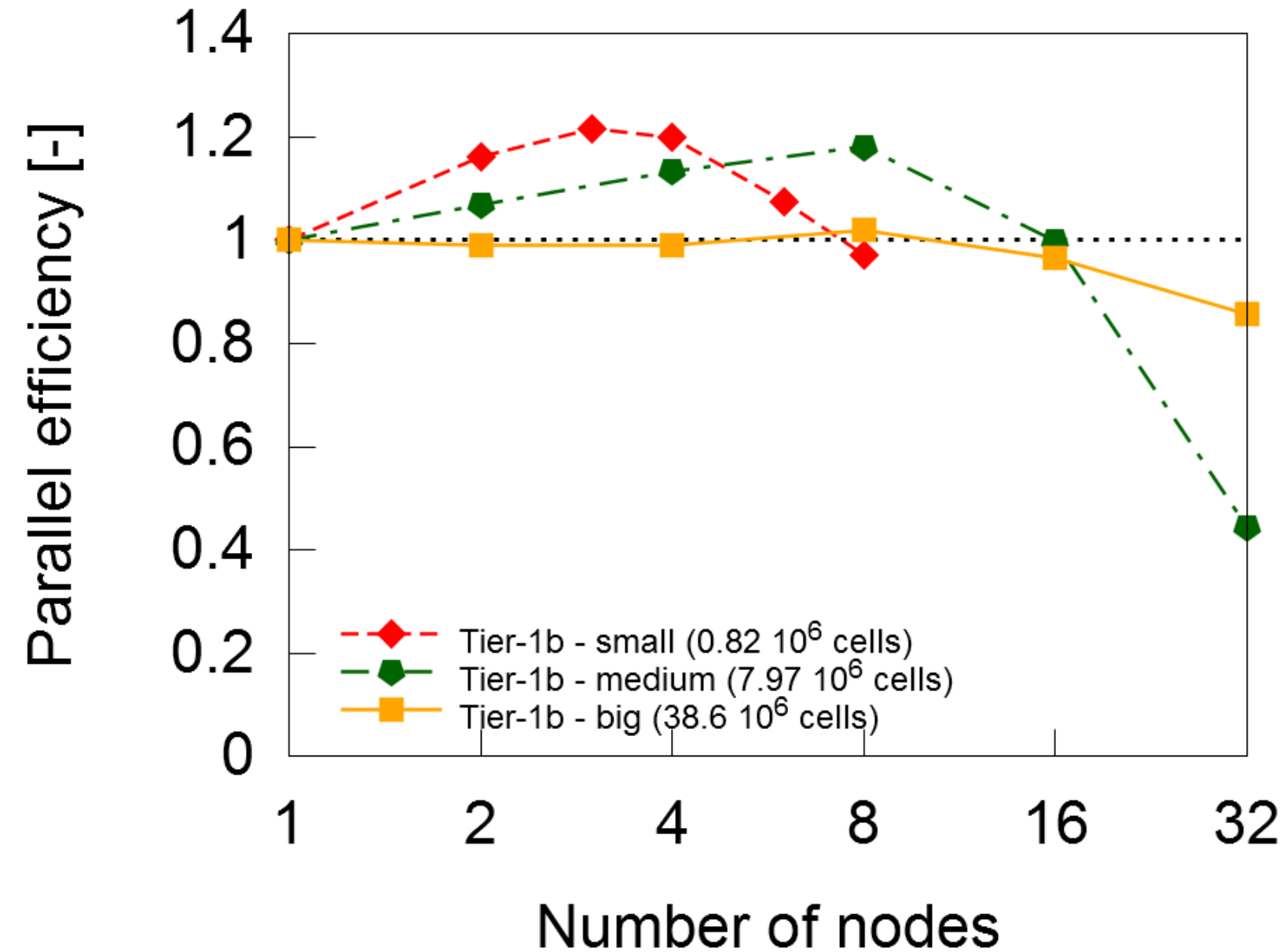
**Remote desktop on Tier-1b login node** with GPU via NoMachine client.



# OPENFOAM ON TIER-1

## Scaling on Tier-1b

BrENIAC – small: OpenFOAM/2.2.0-intel-2016a / Intel MPI v5.1.3  
BrENIAC – medium: OpenFOAM/2.2.0-intel-2016a / Intel MPI v5.1.3  
BrENIAC – big: OpenFOAM/2.2.0-intel-2016a / Intel MPI v5.1.3



Super-linear scalability due to cache effect and better accommodation of memory patterns across multiple nodes

The choice of decomposition method (scotch, simple, metis, etc.) is important for the number of processor faces

# GENERAL BEST PRACTICES

Use modules compiled with **recent compiler toolchains** (UGent Tier-2)

```
----- /apps/gent/C07/sandybridge/modules/all -----
OpenFOAM/2.2.2-intel-2017a      OpenFOAM/2.4.0-intel-2017a      OpenFOAM/4.0-intel-2016b
OpenFOAM/2.3.1-intel-2017a      OpenFOAM/3.0.1-intel-2016b      OpenFOAM/4.1-intel-2017a (D)

----- /apps/gent/SL6/sandybridge/modules/all -----
OpenFOAM-Extend/3.2-intel-2016a  OpenFOAM/2.3.1-intel-2015a
OpenFOAM/2.1.1-ictce-4.0.10      OpenFOAM/2.4.0-intel-2015b
OpenFOAM/2.2.0-ictce-4.1.13      OpenFOAM/3.0.0-intel-2015b-eb-deps-Python-2.7.10
OpenFOAM/2.2.2-intel-2015a      OpenFOAM/3.0.1-intel-2016b
OpenFOAM/2.2.2-intel-2016a      OpenFOAM/4.0-intel-2016b
OpenFOAM/2.3.0-intel-2014b
```

Run your job from the **appropriate location**, excessive I/O on low-bandwidth locations will seriously slow down your job

**\$VSC\_DATA**: not meant for calculations, only long-term storage

**\$VSC\_SCRATCH**: default scratch on 15k disks

**\$VSC\_SCRATCH\_NODE**: /tmp location on local node, only accessible as long as the jobs is running, suited for single-node jobs



# DOCUMENTATION AND TRAINING

Documentation is available at

- <https://www.ugent.be/hpc/en/support/documentation.htm>
- <https://www.vscentrum.be/en/user-portal>
- (<http://hpc.ugent.be/userwiki>, being phased out)

Regular training sessions (HPC-UGent intro, Introduction to Linux,...)

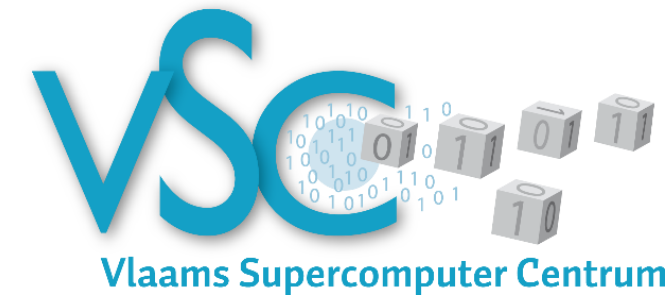
- <https://www.ugent.be/hpc/en/training>
- <https://www.vscentrum.be/en/education-and-trainings>

Support and new software installations

- [hpc@ugent.be](mailto:hpc@ugent.be)

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HPC-UGent staff!



**Stijn De Weirdt**  
*technical lead*



**Ewald Pauwels**  
*team lead*



**Kenneth Hoste**  
*user support & training*



**Wouter Depypere**  
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**Jens Timmerman**  
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**Kenneth Waegeman**  
*sysadmin, storage*



**Andy Georges**  
*sysadmin, tools & testing*

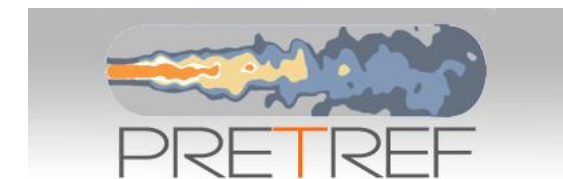
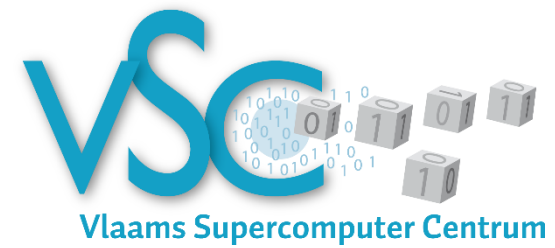


**Alvaro Simon Garcia**  
*cloud, user support*



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