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On the Descriptional Complexity of Iterative Arrays

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Abstract

The descriptional complexity of iterative arrays (IAs) is studied. Iterative arrays are a parallel computational model with a sequential processing of the input. It is shown that IAs when compared to deterministic finite automata or pushdown automata may provide savings in size which are not bounded by any recursive function, so-called non-recursive trade-offs. Additional non-recursive trade-offs are proven to exist between IAs working in linear time and IAs working in real time. Furthermore, the descriptional complexity of IAs is compared with cellular automata (CAs) and non-recursive trade-offs are proven between two restricted classes. Finally, it is shown that many decidability questions for IAs are undecidable and not semidecidable.

1 Introduction

The descriptional complexity of abstract machines is a field of theoretical computer science where the conciseness of the representation of a formal language in one model is compared with the size of representation in other models. One early result is the exponential trade-off between nondeterministic finite automata (NFAs) and deterministic finite automata (DFAs). On the one hand, it is known that each n-state NFA can be converted to a DFA with at most 2^n states. On the other hand, Meyer and Fischer [9] proved that there is an infinite sequence of languages such that each language can be accepted by an n-state NFA, but every DFA accepting the same language needs at least 2^n states. Apart from this exponential trade-off between two descriptional systems, Meyer and Fischer proved that the trade-off between context-free grammars and DFAs is not bounded by any recursive function. Such a trade-off is said to be non-recursive. Recursive and non-recursive trade-offs have been proven between many language classes. For a summary of results we refer to [3].

In this paper, we continue the investigation of cellular models which started in [7] with the study of cellular automata (CAs). The main results obtained there may be summarized as follows. There are non-recursive trade-offs between CAs and the sequential models DFAs and pushdown automata (PDAs), between lineartime-CAs and realtime-CAs, and between realtime-CAs with one-way communication and realtime-CAs with two-way communication. Here, these results are complemented by studying iterative

arrays (IAs) which are identical to CAs except that the input mode is sequential. In analogy to the approach in [7], which is based on a technique of Hartmanis [4], we are going to show non-recursive trade-offs between IAs and sequential models as well as between lineartime-IAs and realtime-IAs. It is known that the two language classes generated by realtime-IAs and realtime-CAs are incomparable. Here, it is shown that both language classes are incomparable from a descriptional complexity point of view, since there exist non-recursive trade-offs between realtime-IAs and realtime-CAs and vice-versa. In [7] it is shown that many decidability questions for CAs are undecidable and that CA language classes possess no pumping lemma. Identical results can be achieved for IAs.

2 Preliminaries and Definitions

Let Σ^* denote the set of all words over the finite alphabet Σ , $\Sigma^+ = \Sigma^* \setminus \{\epsilon\}$. By |w| we denote the length of a string w, and the reversal of a word w is denoted by w^R . Let REG, DCF, CF, RE denote the families of regular, deterministic context-free, context-free and recursively enumerable languages. DCF_{\varepsilon} denotes the family of languages which can be accepted by a deterministic pushdown automaton (DPDA) with no \varepsilon-moves. In this paper we do not distinguish whether a language L contains the empty word ϵ or not. I.e., L is identified with $L \setminus \{\epsilon\}$. We assume that the reader is familiar with the common notions of formal language theory as presented in [5]. Let S be a set of recursively enumerable languages. Then S is said to be a property of the recursively enumerable languages. A set L has the property S, if $L \in S$. Let L_S be the set $\{\langle M \rangle \mid T(M) \in S\}$ where $\langle M \rangle$ is an encoding of a Turing machine M. If L_S is recursive, we say the property S is decidable; if L_S is recursively enumerable, we say the property S is semidecidable. Concerning cellular automata and iterative arrays, we largely follow the notations and definitions given in [6].

Definition: A two-way cellular automaton (CA) A is a quintuple $A=(Q,\#,\Sigma,\delta,F)$, where

- 1. $Q \neq \emptyset$ is the finite set of cell states,
- 2. $\# \notin Q$ is the boundary state,
- 3. $\Sigma \subseteq Q$ is the input alphabet,
- 4. $F \subseteq Q$ is the set of accepting cell states and
- 5. $\delta: (Q \cup \{\#\}) \times (Q \cup \{\#\}) \times (Q \cup \{\#\}) \rightarrow Q$ is the local transition function.

Restricting the flow of information only from the right to the left, we get a one-way cellular automaton (OCA) and the local transition function maps from $(Q \cup \{\#\}) \times (Q \cup \{\#\})$ to Q. To simplify matters we identify the cells by positive integers.

A configuration of a cellular automaton at some time step $t \geq 0$ is a description of its global state, formally a mapping $c_t: \{1, \ldots, n\} \to Q$ for $n \in \mathbb{N}$. The initial configuration at time 0 is defined by the input word $w = x_1 \ldots x_n$: $c_{0,w}(i) = x_i$, $1 \leq i \leq n$. During a computation the O(CA) steps through a sequence of configurations whereby successor configurations are computed according to the global transition function Δ .

Let c_t , $t \ge 0$, be a configuration, then its successor configuration is defined as follows.

$$c_{t+1} = \Delta(c_t) \iff c_{t+1}(1) = \delta(\#, c_t(1), c_t(2))$$

$$c_{t+1}(i) = \delta(c_t(i-1), c_t(i), c_t(i+1)), 2 \le i \le n-1$$

$$c_{t+1}(n) = \delta(c_t(n-1), c_t(n), \#)$$

for CAs and

$$c_{t+1} = \Delta(c_t) \iff c_{t+1}(i) = \delta(c_t(i), c_t(i+1)), 1 \le i \le n-1 c_{t+1}(n) = \delta(c_t(n), \#)$$

for OCAs. Thus, Δ is induced by δ .

Definition: A two-way iterative array (IA) A is a tuple $A = (Q, q_0, \nabla, \Sigma, \delta, \delta_0, F)$, where

- 1. $Q \neq \emptyset$ is the finite set of cell states,
- 2. $q_0 \in Q$ is the initial (quiescent) state,
- 3. $\nabla \notin \Sigma$ is the end-of-input symbol,
- 4. Σ is the input alphabet,
- 5. $\delta: Q^3 \to Q$ is the local transition function for non-communication cells where $\delta(q_0, q_0, q_0) = q_0$,
- 6. $\delta_0: Q^3 \times (\Sigma \cup \{\nabla\}) \to Q$ is the local transition function for the communication cell and
- 7. $F \subseteq Q$ is the set of accepting cell states.

A configuration of an IA at some time $t \geq 0$ is a pair (w_t, c_t) where $w_t \in \Sigma^*$ is the remaining input sequence and $c_t : \mathbb{Z} \to Q$ is a function that maps the single cells to their current states. The configuration (c_0, w_0) at time 0 is defined by the input word w_0 and the mapping $c_0(i) = q_0, i \in \mathbb{Z}$. The global transition function Δ is induced by δ and δ_0 as follows. Let $(w_t, c_t), t \geq 0$, be a configuration.

$$(w_{t+1}, c_{t+1}) = \Delta((w_t, c_t)) \iff c_{t+1}(i) = \delta(c_t(i-1), c_t(i), c_t(i+1)), i \in \mathbb{Z} \setminus \{0\}$$

$$c_{t+1}(0) = \delta_0(c_t(-1), c_t(0), c_t(1), x)$$

where $x = \nabla, w_{t+1} = \epsilon$ if $w_t = \epsilon$, and $x = w_1, w_{t+1} = w_2 \dots w_n$ if $w_t = w_1 \dots w_n$.

An input string w is accepted by an (O)CA (IA) if at some time step i during its computation the leftmost cell (communication cell) enters an accepting state from the set of accepting states $F \subseteq Q$.

Definition: Let $A = (Q, \#, \Sigma, \delta, F)$ be an (O)CA $(A = (Q, q_0, \nabla, \Sigma, \delta, \delta_0, F)$ be an IA).

1. A word $w \in \Sigma^+$ is accepted by A if there exists a time step $i \in \mathbb{N}$ such that $c_i(1) \in F$ holds for the configuration $c_i = \Delta^i(c_{0,w})$ $((w_i, c_i) = \Delta^i(w, c_0))$.

- 2. $T(A) = \{w \in \Sigma^+ \mid w \text{ is accepted by } A\}$ is the language accepted by A.
- 3. Let $t: \mathbb{N} \to \mathbb{N}$, $t(n) \ge n$, be a mapping and i_w be the minimal time step at which A accepts $w \in T(A)$. If all $w \in T(A)$ are accepted within $i_w \le t(|w|)$ time steps, then T(A) is said to be of time complexity t.
- 4. $\mathcal{L}_t((O)CA) = \{L \mid L \text{ is accepted by an } (O)CA \text{ with time complexity } t\}$. $\mathcal{L}_t(IA) = \{L \mid L \text{ is accepted by an } IA \text{ with time complexity } t\}$.
- 5. If t(n) = n (t(n) = n + 1), we say these languages are accepted in real time. The corresponding language classes are denoted by $\mathcal{L}_{rt}((O)CA)$ ($\mathcal{L}_{rt}(IA)$). The languages accepted in linear time $\mathcal{L}_{lt}((O)CA)$ are defined as $\mathcal{L}_{lt}((O)CA) = \bigcup_{k \in \mathbb{Q}, k \geq 1} \mathcal{L}_{k \cdot t}((O)CA)$ with t(n) = n. $\mathcal{L}_{lt}(IA)$ is defined analogously. The corresponding cellular devices are denoted by realtime-(O)CA, lineartime-(O)CA, realtime-IA, and lineartime-IA.

It is known that $DCF_{\epsilon} \subset \mathcal{L}_{rt}(IA)$ and that CF and $\mathcal{L}_{rt}(IA)$ are incomparable [6]. $\mathcal{L}_{rt}(IA)$ is closed under union, intersection, complementation, right concatenation with regular sets, marked iteration, and marked concatenation [10].

In the sequel we will use the set of valid computations of a Turing machine. Details are presented in [4] and [5]. The definition of a Turing machine and of an instantaneous description (ID) of a Turing machine may be found in [5].

Definition: Let $M = (Q, \Sigma, \Gamma, \delta, q_0, B, F)$ be a deterministic Turing machine.

$$\begin{aligned} \text{VALC}[M] &= \{ID_0(x) \# ID_1(x)^R \# ID_2(x) \# ID_3(x)^R \# \dots \mid \\ & x \in \Sigma^*, ID_0(x) \in q_0 \Sigma^* \text{ is an initial ID, } ID_n(x) \in \Gamma^* F \Gamma^* \text{ is an accepting ID,} \\ & ID_{i+1}(x) \in \Gamma^* Q \Gamma^* \text{results from } ID_i(x), \text{i.e., } ID_i(x) |_{M} ID_{i+1}(x) \} \end{aligned}$$

The invalid computations INVALC[M] are defined as the complement of VALC[M] with respect to a suitable coding alphabet.

Remark: Let M be an arbitrary Turing machine and $Q_1 \subset Q$ denote the set of states being assumed in M's first computation, i.e., $Q_1 = \{q \in Q \mid \exists \gamma_1, \gamma_2 \in \Gamma, S \in \{L, R\} : \delta(q_0, \gamma_1) = (q, \gamma_2, S)\}$. Then, M can be modified such that all $q \in Q_1$ are entered only in the first computation. This can be achieved by copying the state set Q to Q' and by replacing each transition $\delta(q, \gamma_1) = (p, \gamma_2, S)$ with $\delta(q, \gamma_1) = (p', \gamma_2, S)$ for $q \neq q_0$ and by adding transitions $\delta(q', \gamma_1) = (p', \gamma_2, S)$ for each transition $\delta(q, \gamma_1) = (p, \gamma_2, S)$.

Concerning the notations and definitions of descriptional complexity we follow the presentation in [3]. A descriptional system K is a set of finite descriptors (e.g. automata or grammars) relating each $M \in K$ to a language T(M). The language class being described by K is $T(K) = \{T(M) | M \in K\}$. For every language L we define $K(L) = \{M \in K | T(M) = L\}$. A complexity measure for K is a total function $|\cdot|: K \to \mathbb{N}$. Comparing two descriptional systems K_1 and K_2 , we assume that $T(K_1) \cap T(K_2)$ is not finite. We say that a function $f: \mathbb{N} \to \mathbb{N}$, $f(n) \geq n$ is an upper bound for the trade-off when changing from a minimal description in K_1 for an arbitrary language to an equivalent minimal description in K_2 , if for all $L \in T(K_1) \cap T(K_2)$

the following holds.

$$\min\{|M| \mid M \in K_2(L)\} \le f(\min\{|M| \mid M \in K_1(L)\})$$

If no recursive function is an upper bound for the trade-off between two descriptional systems K_1 and K_2 , we say the trade-off is non-recursive and write $K_1 \xrightarrow{nonrec} K_2$.

3 Non-Recursive Trade-Offs

Theorem 1 Let M be a Turing machine. Then two realtime-IAs A_1 and A_2 can be constructed such that $T(A_1) = VALC[M]$ and $T(A_2) = INVALC[M]$.

Proof: It is known [6] that $DCF_{\epsilon} \subset \mathcal{L}_{rt}(IA)$. We show that VALC[M] is the intersection of two languages $L_1, L_2 \in DCF_{\epsilon}$. Since $\mathcal{L}_{rt}(IA)$ is effectively closed under intersection and complementation, we then can construct two realtime-IAs accepting VALC[M] and INVALC[M], respectively. We first observe that DCF_{ϵ} is closed under marked concatenation and marked iteration, since both operations do not introduce ϵ -moves. It is shown in [5] that $VALC[M] = L_1 \cap L_2$ where

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L_{1} = (L_{3}\{\#\})^{*}(\{\epsilon\} \cup \Gamma^{*}F\Gamma^{*}\{\#\}),
L_{2} = \{q_{0}\}\Sigma^{*}\{\#\}(L_{4}\{\#\})^{*}(\{\epsilon\} \cup \Gamma^{*}F\Gamma^{*}\{\#\}),
L_{3} = \{y\#z^{R} \mid y \mid \underline{M} z\},
L_{4} = \{y^{R}\#z \mid y \mid \underline{M} z\}.
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In [5] it is described how two pushdown automata can be constructed to accept L_3 and L_4 , respectively. It is not difficult to modify this construction such that L_3 and L_4 are accepted by DPDAs with accepting states and no ϵ -moves. Thus, $L_3, L_4 \in \text{DCF}_{\epsilon}$. Then, $(L_3\{\#\})^*, (L_4\{\#\})^* \in \text{DCF}_{\epsilon}$, since DCF_{\epsilon} is closed under marked iteration and every second # acts as a marking symbol. Since DCF_{\epsilon} is closed under marked concatenation, we obtain that $\{q_0\}\Sigma^*\{\#\}(L_4\{\#\})^*$ is in DCF_{\epsilon}. It remains to be shown that the right concatenation with the regular set $(\{\epsilon\} \cup \Gamma^*F\Gamma^*\{\#\})$ does not introduce \epsilon-moves. This can be realized with a second component in the state sets of the DPDAs accepting $(L_3\{\#\})^*$ and $(L_4\{\#\})^*$, respectively. This component checks after every second # whether the remaining input is of the form $(\{\epsilon\} \cup \Gamma^*F\Gamma^*\{\#\})$. If so, the input is accepted. Obviously, no \epsilon-moves are necessary. Hence, two DPDAs without \epsilon-moves can be constructed accepting L_1 and L_2 , respectively.

The next theorem provides a criterion for the existence of non-recursive trade-offs. The proof uses a technique given by Hartmanis [4] and may be found in [7].

Theorem 2 Let K_1 and K_2 be two descriptional systems. If for every Turing machine M a language $L_M \in T(K_1)$ can be effectively constructed such that $L_M \in T(K_2) \Leftrightarrow T(M)$ is finite, then the trade-off between K_1 and K_2 is non-recursive.

Remark: Let A be a realtime-IA. In [6] it is explained that the result of the computation of a remaining input of length m in A depends on the states of the cells

 $-m-1,\ldots,0,\ldots,m+1$. These cells will be denoted by the term m-window. It is observed in [6] that there are at most $n^{2(m+1)+1}$ different m-windows, where n denotes the number of states in A.

Remark: Every $x \in VALC[M]^R$ is ending with a string $\#yq_0$ where # is a separating symbol, $y \in \Sigma^*$ the reversal of the input and q_0 the initial state of M. The mapping $\pi: VALC[M]^R \to \Sigma^*$ is defined as $\pi(x) = y$ and extracts the input.

Lemma 1 Let A be a realtime-IA accepting VALC[M]^R where M is a Turing machine accepting an infinite language $L \subseteq \Sigma^*$. Let $U = \{u_1, u_2, \ldots\}$ be an infinite subset of VALC[M]^R which has the property that $|\pi(u_j)| > |\pi(u_i)| + 3$ and $|u_i| < |u_j|$ for $i \ge 1$ and j > i. Then there exists a natural number m such that there exist $u_i, u_j \in U$ with j > i, $|\pi(u_i)| > m$ and the m-window of the configuration at time $|u_i| - m$ when processing u_i occurs in at least one m-window of the configurations up to time $|u_j| - m - 3$ when processing u_j .

Proof: By way of contradiction we assume that the above statement does not hold. Then, for all natural numbers m there exist no such words u_i and u_j . Or, in other words, for arbitrary m holds: each $u_i \in U$ with $|\pi(u_i)| > m$ has an m-window at time $|u_i| - m$ which occurs in no other computation of words $u_j \in U$ with j > i up to time $|u_j| - m - 3$. We now consider the words $w_1 = u_{i+1}, w_2 = u_{i+2}, \ldots, w_{|Q|^{2(m+1)+1}+1} = u_{i+|Q|^{2(m+1)+1}+1}$. Due to our assumption, we know that, for $1 \le j \le |Q|^{2(m+1)+1} + 1$, w_j has an m-window c_j at time $|w_j| - m$ that does not occur in any m-window of computations of $w_{j+1}, \ldots, w_{|Q|^{2(m+1)+1}+1}$ up to time $|w_l| - m - 3$ for $j + 1 \le l \le |Q|^{2(m+1)+1} + 1$. Hence the set $\{c_1, c_2, \ldots, c_{|Q|^{2(m+1)+1}+1}\}$ is pairwise distinct which is a contradiction to the fact that there are at most $|Q|^{2(m+1)+1}$ different m-windows. \square

Lemma 2 Let M be a Turing machine and $L[M] = \{w^{|w|!} | w \in \{\#_0\} \text{VALC}[M] \{\#_1\} \}$.

- (1) INVALC[M] $\in REG \Leftrightarrow T(M)$ is finite
- (2) $VALC[M] \in CF \Leftrightarrow T(M)$ is finite
- (3) VALC[M]^R $\in \mathcal{L}_{rt}(IA) \Leftrightarrow T(M)$ is finite
- (4) INVALC[M]^R $\in \mathcal{L}_{rt}(IA) \Leftrightarrow T(M)$ is finite
- (5) $L[M] \in \mathcal{L}_{rt}(OCA) \Leftrightarrow T(M)$ is finite
- (6) $L[M] \in \mathcal{L}_{rt}(IA)$

Proof: (2) is proven in [5] and (1) is then easy to show. The "if" portion of (3) is obvious, since REG is a subset of $\mathcal{L}_{rt}(IA)$. The "only if" portion is proven by using Lemma 1. We show that $VALC[M]^R \not\in \mathcal{L}_{rt}(IA)$ if T(M) is infinite: we assume that $VALC[M]^R \in \mathcal{L}_{rt}(IA)$. Let $m \in \mathbb{N}$ be the integer from Lemma 1 which can be applied, since T(M) is infinite. Then there exist two words $u, u' \in VALC[M]^R$ with |u'| > |u| and $|\pi(u)| > m$. Let $u = u_1u_2$ with $|u_2| = m$ and $u' = u'_1u'_2$ where u'_1 is the shortest prefix of u' such that the m-window c after processing u'_1 is identical to that after processing u_1 . It is easily observed that u has a suffix $ID_1(\pi(u))^R \#\pi(u)q_0$ and that

u' has a suffix $ID_1(\pi(u'))^R \# \pi(u')q_0$. We now consider the string $w = u'_1u_2$ and have to differentiate three cases. At first, w may have the wrong format of an ID. Then $w \notin VALC[M]^R$. If w has the correct format, then w has a suffix $\#ID_l(\pi(u'))\#xu_2$, if l is even, or $\#ID_l(\pi(u'))^R \#xu_2$ otherwise. Since u_2 is a suffix of $\pi(u)q_0 \in \Sigma^+\{q_0\}$, we can assume that $x \in \Sigma^*$. If l > 1, then $w \notin VALC[M]^R$, since M can be modified such that, due to an above remark, certain states are assumed only in the first computation. If l=1, then w has a suffix $ID_1(\pi(u'))^R \# xu_2$ and $|xu_2| \leq |\pi(u')q_0| - 3 < |\pi(u')q_0| - 2$, since the identical m-window c is assumed after at most |u'|-m-3 time steps. We can observe that an ID changes its length when compared to its preceding ID by at most 1. I.e., $|ID_{k+1}(y)| = |ID_k(y)| + p$ with $p \in \{-1,0,1\}$ and $k \geq 0, y \in \Sigma^*$. Hence, $|ID_1(\pi(u'))| = |xu_2| + p \le |xu_2| + 1 < |\pi(u')q_0| - 1 = |ID_0(\pi(u'))| - 1$. This implies that $w \notin VALC[M]^R$. So we obtain in all three cases that $w \notin VALC[M]^R$. On the other hand, since the m-window c leads to acceptance when processing u_2 , we obtain $w = u_1'u_2 \in VALC[M]^R$ which is a contradiction. This proves (3). Since INVALC[M]^R = $\overline{VALC[M]^R}$ and $\mathcal{L}_{rt}(IA)$ is closed under complementation, (4) follows from (3). A proof of (5) may be found in [7]. To prove (6) we show how to construct a realtime-IA recognizing L[M]. L[M] is the intersection of the following three languages L_1, L_2, L_3 . Let VALC[M] $\subset \Sigma^*$ and $\#_0, \#_1$ be new symbols with $\{\#_0, \#_1\} \cap \Sigma = \emptyset$.

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\begin{split} L_1 &= \{ w \, | \, w \in (\{\#_0\} \text{VALC}[M] \{\#_1\})^* \} \\ L_2 &= \{ w^n \, | \, w \in \{\#_0\} \Sigma^* \{\#_1\}, n \geq 2, n \text{ is even} \} \\ L_3 &= \{ wx \, | \, w \in \{\#_0\} \Sigma^* \{\#_1\}, x \in (\{\#_0\} \Sigma^* \{\#_1\})^*, |wx|_{\#_0} = |w|! \} \end{split}
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Since $\mathcal{L}_{rt}(IA)$ is closed under intersection, it remains to be shown that $L_i \in \mathcal{L}_{rt}(IA)$ for $1 \leq i \leq 3$. $L_1 \in \mathcal{L}_{rt}(IA)$, since $\{\#_0\} \text{VALC}[M] \in \mathcal{L}_{rt}(IA)$ and $\mathcal{L}_{rt}(IA)$ is closed under marked concatenation where $\#_1$ acts as a marking symbol.

In [1] it is shown that $\{ww \mid w \in \Sigma^*\} \in \mathcal{L}_{rt}(IA)$. Thus, $L = \{ww \mid w \in \{\#_0\}\Sigma^*\{\#_1\}\} \in \mathcal{L}_{rt}(IA)$. We can observe that $L^* \in \mathcal{L}_{rt}(IA)$, because the iteration of languages L is in a way a marked iteration: after the second $\#_1$, the next L starts. By a similar argument and the fact that $\mathcal{L}_{rt}(IA)$ is closed under right concatenation with regular sets, we can see that $\{\#_0\}\Sigma^*\{\#_1\}L^*\{\#_0\}\Sigma^*\{\#_1\} \in \mathcal{L}_{rt}(IA)$. Hence, $L_2 = L^* \cap \{\#_0\}\Sigma^*\{\#_1\}L^*\{\#_0\}\Sigma^*\{\#_1\} \in \mathcal{L}_{rt}(IA)$.

Now it remains for us to show that $L_3 \in \mathcal{L}_{rt}(IA)$. We sketch the construction. We use an iterative array where each cell is split into four subcells, so we can speak of four tracks. On the first track we are checking whether the input string has the correct format $(\{\#_0\}\Sigma^*\{\#_1\})^*$. The second track computes the factorials according to the construction presented in [8]. I.e., the communication cell assumes a designated state whenever a factorial has been computed. On the third and fourth track we install binary counters (Counter1, Counter2) starting in the communication cell. The number of cells used for storing is varying with the size of the number that has to be stored. The rightmost cell used is marked with a special symbol. A construction of such counters is possible in realtime-IAs due to their two-way communication. While reading the input up to the first $\#_1$, Counter1 is incremented in every step. Whenever a factorial has been computed on the second track, Counter1 is decremented. When Counter1 is decremented to zero, we know that |w|! has been computed. Up to this moment, Counter2 is incremented in every time step. Whenever $\#_0$ is read, Counter2 is decremented. When Counter2 is decremented to zero, we know that |w|! $\#_0$'s have

been read. If the remaining input is a string in $\Sigma^*\#_1$, the input is accepted, otherwise the input is rejected. Hence, a realtime-IA accepting L_3 can be constructed. Thus, $L[M] \in \mathcal{L}_{rt}(IA)$.

Now, we can show the following non-recursive trade-offs by combining Theorem 2 and Lemma 2.

Theorem 3 (1) realtime-IA $\stackrel{nonrec}{\longrightarrow}$ DFA

- (2) realtime-IA \xrightarrow{nonrec} PDA
- (3) PDA \xrightarrow{nonrec} realtime-IA
- (4) lineartime-IA \xrightarrow{nonrec} realtime-IA
- (5) realtime-OCA nonrec realtime-IA
- (6) realtime-IA \xrightarrow{nonrec} realtime-OCA

Proof: (1) and (2) can be shown with $L_M = \text{INVALC}[M]$ and $L_M = \text{VALC}[M]$, respectively. To prove (3) we set $L_M = \text{INVALC}[M]^R$. $L_M \in \text{CF}$, since $\text{INVALC}[M] \in \text{CF}$ [5] and CF is closed under reversal. Now, we consider $L_M = \text{VALC}[M]^R$. It is shown in [7] that $\text{VALC}[M] \in \mathcal{L}_{rt}(\text{OCA})$. Since $\mathcal{L}_{rt}(\text{OCA})$ is closed under reversal, we obtain that $L_M \in \mathcal{L}_{rt}(\text{OCA}) \subset \mathcal{L}_{lt}(\text{CA}) = \mathcal{L}_{lt}(\text{IA})$ [2]. This proves (4) and (5). Finally, (6) can be shown with $L_M = \text{L}[M]$.

4 Decidability Questions

Due to the theorem of Rice [5], all non-trivial decidability questions for Turing machines are undecidable. Furthermore, it is known that certain decidability questions such as emptiness, inclusion, equivalence, finiteness, infiniteness, regularity, and context-freedom are not semidecidable for Turing machines [5]. The fact that VALC[M] is in $\mathcal{L}_{rt}(IA)$ and the results of Lemma 2 imply that many decidability questions for IAs can be reduced to decidability questions for Turing machines. Hence, the above-mentioned decidability questions are undecidable and not semidecidable for IAs. A detailed discussion may be found in [7] where similar results are shown for cellular automata. It should be noted that some of the undecidability results for realtime-IAs were first proven by Seidel in [10] using reductions of the Post Correspondence Problem. The approach discussed here provides simpler proofs and shows the non-semidecidability of the questions.

Theorem 4 It is not semidecidable for arbitrary realtime-IAs A, A' whether

- $T(A) = \emptyset$, $T(A) = \Sigma^*$
- T(A) is finite, T(A) is infinite
- $\bullet^{\tau}T(A)=T(A'),\,T(A)\subseteq T(A')$

• $T(A) \in REG, T(A) \in CF$

In [7] it is shown that there exists no pumping lemma and no minimization algorithm for cellular automata. The proofs rely on the fact that for cellular automata infiniteness is not semidecidable and emptiness is undecidable. Since both statements are valid for IAs, we obtain that there exists no pumping lemma and no minimization algorithm for IAs.

Theorem 5 $\mathcal{L}_{rt}(IA)$ and each language class containing $\mathcal{L}_{rt}(IA)$ does not possess a pumping lemma.

Theorem 6 For realtime-IAs there is no minimization algorithm converting an arbitrary realtime-IA A to a realtime-IA A' which accepts T(A) and has a minimal number of states.

5 Conclusion

We studied descriptional complexity aspects of iterative arrays. The results known for CAs were complemented by similar results for IAs. Moreover, non-recursive trade-offs were shown to exist between CAs and IAs operating in real time.

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