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Richter, Jacob; Arnoldus, Morten Berg; Hansen, Ole; Thomsen, Erik Vilain

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Four point bending setup for characterization of semiconductor piezoresistance

J. Richter,^{a)} M. B. Arnoldus, O. Hansen,^{b)} and E. V. Thomsen Department of Micro- and Nanotechnology, Technical University of Denmark, DTU Nanotech Building 345 East, DK-2800 Kongens Lyngby, Denmark

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We present a four point bending setup suitable for high precision characterization of piezoresistance in semiconductors. The compact setup has a total size of 635 cm³. Thermal stability is ensured by an aluminum housing wherein the actual four point bending fixture is located. The four point bending fixture is manufactured in polyetheretherketon and a dedicated silicon chip with embedded piezoresistors fits in the fixture. The fixture is actuated by a microstepper actuator and a high sensitivity force sensor measures the applied force on the fixture and chip. The setup includes heaters embedded in the housing and controlled by a thermocouple feedback loop to ensure characterization at different temperature settings. We present three-dimensional finite element modeling simulations of the fixture and discuss the possible contributions to the uncertainty of the piezoresistance characterization. As a proof of concept, we show measurements of the piezocoefficient π_{44} in *p*-type silicon at three different doping concentrations in the temperature range from T=30 °C to T=80 °C. The extracted piezocoefficients are determined with an uncertainty of 1.8%. © 2008 American Institute of Physics. [DOI: 10.1063/1.2908428]

I. INTRODUCTION

Since the pioneering work of Smith in 1954,¹ piezoresistivity of silicon has attracted attention from both academia²⁻⁴ and industry.^{5,6} Smith experimentally determined the three piezoresistance coefficients of lightly doped silicon. The piezoresistance coefficients for more heavily doped silicon were later experimentally determined by other research groups,^{2,3} and still, today, the piezoresistance coefficients of silicon and other materials are topics of interest in both academia^{4,7,8} and industry. The continued academic interest is partly due to the scarcity of reliable measurements and partly due to a discrepancy between theoretical models and available measurements especially for *p*-type silicon.⁹⁻¹²

Essentially, the piezoresistance effect is a change in the resistivity tensor (second order) caused by an applied stress.¹³ The effect is characterized by a fourth order piezoresistivity tensor, which, in the case of silicon due to symmetry, has three independent coefficients.¹⁴ The piezoresistance coefficients are dependent on sample temperature, doping level, and doping type.² In order to measure these coefficients and characterize the effect, it is necessary to apply a well controlled stress to the silicon sample with well defined resistors and measure the relative change in resistance of these.

In the original experiments by Smith,¹ silicon rods were pulled to apply a uniform uniaxial stress. Machined pull samples with through holes were used in Ref. 2 and a pull force was applied by pins inserted in the through holes.

For microfabricated thin film devices, it is more conve-

^{b)}Also at CINF-Center for Individual Nanoparticle Functionality, Department of Micro- and Nanotechnology, Technical University of Denmark, DTU Nanotech Building 345 East, DK-2800 Kongens Lyngby, Denmark. nient to use a four point bending (4PB) fixture.^{8,15–19} In Refs. 16 and 18 an optical method is used to measure the deflection and curvature of the chip. The stress is applied to the chip using a piezoelectric actuator and a translation stage, respectively. In Ref. 17, the displacement of the chip is known at the contact points between chip and 4PB fixture and this enables a calculation of the applied stress in the chip. Reference 8 and 15 use simple loads to apply the force and has no external measurement of the applied force. This is a cumbersome and time consuming method, especially for characterization at different temperatures.

We present a four point bending method where a motorized stepper actuator is used to apply a displacement while the force on the chip sample is measured with a dedicated force sensor. With this method, the measured force can be directly applied to calculate the stress. Thus, Young's modulus is not included in the stress calculation as is the case when a deflection is measured. The compact setup has a total volume of 635 cm².

The four point bending setup is designed and fabricated to analyze the piezoresistance coefficients of embedded piezoresistors located on a dedicated silicon chip. The main focus is to characterize the piezoresistivity of *p*-type silicon and other related semiconductor materials, e.g., Si under tensile strain¹⁹ and compressively strained SiGe.⁸ In this paper, we present measurements of the piezocoefficient π_{44} in *p*-type silicon with several different doping concentrations in the temperature range T=30-80 °C as an example of use of the setup. Boron doped silicon is the preferred piezoresistive material in commercial micro electromechanical systems (MEMS) due to the large piezocoefficient π_{44} and the very low values of the two other piezocoefficients π_{11} and π_{12} . When the piezoresistors are directed along certain crystal directions, $\langle 110 \rangle$, and placed in a Wheatstone bridge configu-

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^{a)}Electronic mail: jar@mic.dtu.dk.



FIG. 1. Schematic of the complete piezoresistance characterization setup. The setup comprises cartridge heaters (1) that are embedded in the bottom plate of the Al housing surrounding the 4PB fixture (2) consisting of base and slider, the chip (3), and the force sensor (4). The actuator motor (5) is placed outside the Al housing to prevent heating of the motor.

ration, these piezocoefficients result in a high sensitivity of the MEMS device, since the effective longitudinal and transverse piezocoefficients are large and almost matched in magnitude but of opposite sign.

We characterize the 4PB setup using analytical expressions, finite element modeling (FEM), calibration measurements, and an application specific stress sensor chip. The stress distribution in a chip in the 4PB setup is thoroughly investigated and this analysis is used to estimate the uncertainties of the measured piezocoefficients.

II. APPARATUS

The piezoresistance characterization setup consists of a 4PB fixture with integrated thermocouples and temperature control. An actuator applies a displacement to the fixture and the force is measured by a force sensor. A schematic of the setup is shown in Fig. 1.

The fixture is placed in an aluminum housing including a metal lid (not shown in the figure) to stabilize temperature and shield off light. The thickness of the aluminum bottom plate and sidewalls are 10 and 20 mm, respectively. Aluminum has a very high thermal conductivity of 239 W/m K⁻¹ (Ref. 20) compared to air (≈ 0.02 W/m K⁻¹), ensuring a uniform heat distribution in the aluminum casing.

The 4PB fixture is guided along the axis of the actuator by small rails in the bottom plate of the setup. The contact area between the rails and the 4PB fixture is very small (40 mm²) which minimizes friction between fixture and bottom plate. The frictional force may be even lower in a vertical design, however, the horizontal design was chosen in order to use gravity to assist alignment of the different parts of the setup and of the sample.

A. Four point bending fixture

An exploded view of the 4PB setup is shown in Fig. 2. It consists of a base part containing two blades separated by a distance of 28 mm and a sliding part where two more blades are formed separated by a distance of 12 mm. The base part



FIG. 2. Exploded view of the setup illustrating the uniaxial force interaction between actuator, 4PB fixture, chip, and force sensor. This ensures uniformity of the stress in the center region of the chip.

slides on two rails formed in the bottom plate of the setup casing. The sliding part is in contact with the base only at two steel pins that ensure good directional control of the force and low frictional force. A dedicated silicon chip is placed in between the sliding part and the base part; the force is applied to the chip by the four blades.

The 4PB fixture applies a uniaxial and uniform stress distribution to the center region of the chip. The bending force *F* is measured using a force sensor. From simple bending beam theory, we obtain the stress σ_{xx} (Ref. 8)

$$\sigma_{xx} = \frac{6Faz}{wh^3},\tag{1}$$

where *h* is the thickness of the chip, *z* is the position of the resistor with respect to the neutral plane of the chip, i.e., z=h/2 at the surface, *a* is the distance between the inner and outer blades of the 4PB fixture, and *w* is the width of the chip.

The 4PB fixture is made from the thermoplastic material polyetheretherketon (PEEK) which is a semicrystalline material. Young's modulus of PEEK is Y=3.5 GPa and the mechanical properties are quite stable in the temperature range from -64 to 250 °C.²¹

The vertical alignment of the chip to the 4PB fixture is ensured by resting the chip on the two steel pins in the setup (see Fig. 2). Horizontal alignment of the chip is done by visual inspection. The stress is considered constant in the area between the two inner blades on the slider. The dimensions of the resistor are much smaller than the 12 mm distance between the two inner blades. Thus, the resistors which are located in the middle region between the two inner blades experience a uniform stress.

B. Force sensor

The force sensor is a Strain Measurement Devices s415 button cell.²² It consists of a plate with four sputter defined resistors in a Wheatstone bridge configuration. The force sensor is fastened to the setup casing. An input voltage, $V_{in} = 10$ V, is applied to the bridge. The output voltage of the Wheatstone bridge, V_o , depends linearly on the force F, as described by



FIG. 3. Force sensor calibration curve at T=30 °C. A force F is applied by adding weights to the force sensor with an input voltage $V_{in}=10$ V while the output voltage, V_o of the Wheatstone bridge is measured. The slope and offset of linear fit (solid line) at different temperatures are listed in Table I.

$$F = \frac{1}{\alpha} (V_o - V_{\text{off}}), \qquad (2)$$

where $1/\alpha$ is the constant of proportionality and V_{off} is the offset voltage.

The constant α is measured in a calibration setup where the force sensor is horizontally placed and well known forces are applied using weights of different masses. The calibration curve at T=30 °C is shown in Fig. 3, where the calibration constant, α , is determined as the slope of the linear fit. The force sensor showed a small hysteresis in the output when increasing and decreasing the applied force. The hysteresis is described by a 0.3% change in the calibration constant and this will not significantly contribute to the uncertainty. The force sensor was calibrated at different temperatures (see Table I). The temperature dependence of α is significant and the values in Table I are included in the analysis.

The force sensor offset is a result of imbalance in the thin film Wheatstone bridge and the actual force sensor temperature. This offset has no influence on the measurements since the force sensor is offset compensated before the actual measurement.

C. Actuator

The motorized Newport NSA12 microstep actuator²³ used in the setup has a resolution of 0.3 μ m and a maximum loading capacity of 25 N. The actual force on the actuator is

TABLE I. Force sensor calibration. The table summarizes the slope α and offset $V_{\rm off}$ of the linear fits to experimentally obtained force-voltage curves at four different temperatures with an input voltage of $V_{\rm in}$ =10 V.

<i>T</i> (°C)	$V_{\rm off}~({\rm m~V})$	$\alpha \text{ (m V/N)}$
30	0.25	2.280
47	0.10	2.290
64	-0.05	2.294
81	-0.20	2.312



FIG. 4. Actuator position hysteresis. The force sensor output voltage $\Delta V_o = V_o - V_{off}$ as a function of the nominal actuator displacement in three subsequent measurement series. The output voltage depends on the direction of the actuator motion due to a hysteresis in the actual actuator position.

of no interest since the force on the chip is measured by an independent force sensor. Characterization of the actuator has shown severe hysteresis during increasing and subsequent decreasing loads. The displacement of the actuator is affected by the force thus the actual position of the actuator is not reliable (see Fig. 4). The actuator is, in turn, increasing the load on the chip and decreasing the load while the output voltage on the force sensor is measured. Since previous characterization in Fig. 3 showed that the force sensor signal linearly depends on the applied force, we conclude that the hysteresis is caused by the actuator. This actuator hysteresis has no influence on the piezoresistance measurements since the force sensor signal (and not the actuator displacement) is used in the characterization.

D. Temperature monitor and control

The setup contains two integrated thermocouples. One thermocouple is placed in air close to the chip and reads the temperature near the chip. The other thermocouple is attached to the aluminum casing. This thermocouple supplies a feedback signal to the temperature controller. The thermocouples have small thermal masses resulting in a fast response time. The temperature is read using a Pico Technologies data logger.²⁴

A Watlow series 96 temperature controller is used to control the temperature and a Watlow solid state DIN-A-MITE power relay supplies bias current to three Watlow cartridge heaters embedded in the aluminum plate placed below the setup, as seen in Fig. 1. The actuator has a temperature operating range between 5 and 40 °C, thus cooling of the actuator is necessary. This is done using an ARX Cera Dyne fan and a heat sink on the actuator. A metal shield between the fan and the metal casing prevents significant cooling of the setup casing.

The time to reach a given temperature is measured to be less than $t_{eq}=20$ min. This is done by measuring the temperature inside the Al housing as a function of time at a



FIG. 5. The temperature readout from the thermocouple placed inside the aluminum housing near the chip as a function of time. The temperature is sequentially set to the values T=30, 47, 64, and 81 °C. The maximum time for equilibrium is $t_{eq}=20$ min seen when the temperature is increased from 64 to reach 81 °C.

given set temperature. Figure 5 shows the temperature development at set temperatures 30, 47, 64, and 81 °C. The maximum equilibration time is $t_{eq}=20$ min when increasing the temperature by 17 °C from 64 °C. All measurement series are performed by increasing the temperature in steps of 10 °C, thus thermal equilibrium is reached within less than 20 min.

E. Chip design

The silicon chip to be inserted in the setup is a 4 cm long and 5.3 mm wide beam. The resistors are fabricated on 350 μ m (001) silicon on insulator wafers with a device layer of 2 μ m. The device layer is thinned down to 500 nm by oxidation thinning. The piezoresistors are formed in the 500 nm thick device layer by boron doping using ion implantation. The implanted doses were $D=1.5\times10^{13}$, 1.5 $\times 10^{14}$, and 1.5×10^{15} cm⁻², respectively, all at an energy of 50 keV. The resulting final doping concentrations in the samples are then $N_A = 1.5 \times 10^{17}$, 2.0×10^{18} , and 2.2 $\times 10^{19}$ cm⁻³, respectively. The fabrication of the piezoresistors was performed by using a long postoxidation annealing which activates the acceptors and results in an extremely uniform doping profile in the piezoresistors, as verified in a simulation using the SILVACO ATHENA process simulator.²⁵ The piezoresistors are patterned using UV lithography and reactive ion etching (RIE). Contact windows in the oxide are formed using buffered HF on a photoresist mask. This mask is also used to pattern an additional high dose $(5 \times 10^{15} \text{ cm}^{-2})$ boron ion implant to improve the contact resistance. A Ti/Al metal layer is deposited in a lift-off process to form interconnects and electrical contact to the piezoresistors. Finally, the chips are diced in a deep RIE using the Bosch process²⁶ with an etch angle of $90^{\circ} \pm 1^{\circ}$ to accurately define the chip direction with respect to the crystal orientation. A cross sectional schematic of the chip is shown in Fig. 6.

The chip layout is sketched in Fig. 7. This test chip is



FIG. 6. Schematic cross section of the chip. The silicon piezoresistor (white) is surrounded by silicon dioxide (dark gray) and electrically connected by Al/Ti metal tracks (black) on a silicon substrate (light gray). The electrical resistance measurement is performed by a four terminal high impedance voltage measurement where a current I_{\parallel} is forced through the resistor from the outer contacts while a voltage drop V_{\parallel} is measured on the inner contacts.

designed to measure the piezocoefficient π_{44} and the sum of the two other coefficients $\pi_{11} + \pi_{12}$ in *p*-type silicon. The relative resistance change, $\Delta R/R$, in a resistor with an applied uniaxial stress, σ_{xx} , is given by⁸

$$\frac{\Delta R}{R} = \sigma_{xx} \left[\frac{\pi_{11} + \pi_{12} + \pi_{44} \cos(2\theta)}{2} \right],$$
(3)

where $R = V_{\parallel}/I_{\parallel}$ is the resistance according to Figs. 6 and 7(b) and θ is the angle of the resistor direction with respect to the σ_{xx} stress direction, i.e., [110] according to Fig. 7(a). By plotting the relative resistance change as a function of the applied stress, we obtain a value of the bracketed piezocoefficient linear combination in Eq. (3) for each resistor. This value is plotted as a function of $\cos(2\theta)$ to determine the piezocoefficient π_{44} as the slope of a linear fit and the sum $(\pi_{11} + \pi_{12})$ as the offset of a linear fit.

F. Electrical measurements

The full electrical setup is sketched in Fig. 8. A thermocouple placed in the Al housing supplies the signal to a feedback loop for the heaters in the bottom of the Al housing through the temperature controller. A Keithley 2400 sourcemeter and a Keithley 2700 multimeter are used for the electrical measurements on the chip with a simple four ter-



FIG. 7. Chip design for piezoresistance measurements, not to scale. (a) The chip has six resistors all placed in the center region of the chip. The resistors are oriented along the different angles with respect to the [110] stress direction. (b) A close-up of the resistor showing the four terminal electrical resistance measurement used on each resistor.



FIG. 8. A schematic of the complete setup. (1) Temperature logger. (2) proportional-integral-derivative temperature controller. (3) Thermocouples. (4) Cartridge heaters. (5) Motion controller. (6) Actuator. (7) 4PB fixture. (8) Force sensor. (9) Chip. (10) Keithley 2700 Multimeter with multiplexer. (11) Keithley 2400 sourcemeter. (12) TTi EL302T triple Power Supply. (13) Keithley 2000 multimeter.

minal measurement, as illustrated in Fig. 6. The force sensor is connected to a power supply and a multimeter. All instruments are controlled via a NATIONAL INSTRUMENTS LABVIEW software interface.

The chip is contacted using zero insertion force flat flexible cable (FFC) connectors [Molex Electronics, part No. 52746-1090 (Ref. 27)], as shown in Fig. 9. The connectors do not influence the stress distribution in the chip and allow for mechanical movement of the chip.

III. ERROR ESTIMATES

The accuracy of the piezoresistance measurements depends on several factors associated with the 4PB fixture. First, the inaccuracy of the intended uniaxial stress caused by force and geometry errors is discussed. Second, deviations from the assumed uniaxial stress distribution caused by model insufficiencies and alignment errors are analyzed using FEM and analytical approaches. Third, we discuss the contribution from errors due to the electronic equipment. Finally, the frictional forces in the setup are outlined and their contribution to the piezoresistance measurement is discussed.

A. Stress uncertainty

The stress at the surface (z=h/2) is determined by Eq. (1), thus the relative uncertainty in the applied stress is given by

$$\frac{\Delta\sigma_{xx}}{\sigma_{xx}} = \sqrt{\left(\frac{\Delta F}{F}\right)^2 + \left(\frac{\Delta a}{a}\right)^2 + 2\left(\frac{\Delta h}{h}\right)^2 + \left(\frac{\Delta w}{w}\right)^2}.$$
 (4)

The uncertainty of the applied force *F* we obtain from the standard deviation on the calibration factor α and an estimate of the precision of the actual electrical measurement on the force sensor. This gives an uncertainty on the measured force of 0.25%. The distance between the inner and outer blades in the 4PB fixture is *a*=8.0 mm with an uncertainty of Δa =0.1 mm. The chip thickness is *h*=356 μ m with an uncertainty of Δh =2 μ m. The chip dimensions are individually measured. The width of the chip, *w*=5.3 mm, is accurately defined by photolithography, as described in Sec. II E and the uncertainty can be assumed to be negligible compared to the



FIG. 9. The chip is electrically interfaced through zero insertion force flat flexible cable (FFC) connectors. The assembly is an easy "plug and measure" method, (a) before insertion of chip in the connector, (b) during insertion, and (c) the chip is connected to the electronic instrumentation.

above values. The vertical sidewalls of the chip are obtained from the deep RIE to have an angle of $90^{\circ} \pm 1^{\circ}$. This angle results in a negligible difference of the width of $2 \times 6 \ \mu m^2$ on front side compared to back side of the chip. Thus, the relative uncertainty of the induced stress is

$$\frac{\Delta\sigma_{xx}}{\sigma_{xx}} = \sqrt{(0.0025)^2 + \left(\frac{0.1}{8.0}\right)^2 + 2\left(\frac{2}{356}\right)^2} = 1.5\%.$$
 (5)

B. Stress distribution: FEM analysis

The stress distribution in the chip is simulated in COMSOL MULTIPHYSICS 3.3. In the simulation, the inner blades of PEEK are constrained in the *z* direction on the bottom surface of the PEEK mass. The outer blades each have a distributed force, F/2 on the surface plane. The chip is assigned the elastic parameters Young's modulus Y=170 GPa and Poisson's ratio $\nu=0.07$ from Ref. 28, since the chip is stressed along the [110] direction. Figure 10(a) shows the stress distribution σ_{xx} in the chip at a force of F=2.5 N. The resistor area in the center region of the chip is sketched by the dashed square (3×3 mm²). The in-plane stress distribution



FIG. 10. FEM analysis of the 4PB setup. (a) Gray-scale encoded stress distribution σ_{xx} in the chip placed in the 4PB fixture with an applied force *F*. The area marked by a dashed square $(\Delta x \times \Delta y = 3 \times 3 \text{ mm}^2)$ indicates the location of the piezoresistors in the surface of the chip center. In (b), a zoom-in on this area shows all in-plane stress distributions, σ_{xx} , σ_{yy} , and σ_{xy} . Notice the different scales, the stress σ_{xx} is more than 100 times larger than σ_{yy} and σ_{xy} .

tions (σ_{xx} , σ_{yy} , and σ_{xy}) in the resistor area are shown in Fig. 10(b).

Since the chip is subjected to pure bending, we expect an exact solution to the problem with σ_{xx} given by Eq. (1) and all other stress components to be zero for a narrow chip. However, due to the rather large width to length ratio, w/l=0.13, the chip is also subjected to a transverse stress. As is seen in Fig. 10(b), this transverse stress is at least a factor 100 less than the stress σ_{xx} .

Ideally, the blades are considered sharp wedges contacting the chip in lines. Real blades will have a finite width in the contact area. FEM analysis shows that even 30 μ m wide contact areas causes a change in σ_{xx} less than 0.6% and insignificant changes to σ_{yy} and σ_{xy} . Considering the quality of the milling machine used in the fabrication of the setup, these effects are insignificant.

C. Horizontal blade misalignment: FEM analysis

FEM is used to describe the influence of a possible misalignment of the blades. The simulations are done with a total misalignment of $\phi = \pm 2^{\circ}$ on each blade in order to



FIG. 11. Top-view schematic of chip and blades (A)–(D). In the FEM analysis, each blade is rotated an angle ϕ . The stress distribution for different configurations of blade rotation is listed in Table II.

TABLE II. Results of a FEM analysis of blade rotation. The blades are rotated with a worst case misalignment of $\phi = 2^{\circ}$ according to Fig. 11. Column 2: the relative difference in the extracted FEM σ_{xx} and the analytically calculated stress $\sigma_{xx,an}$ in the center of the chip surface. Column 3: σ_{yy}/σ_{xx} at the chip surface in (x, y)=(1.5 mm, 0). Column 4: σ_{xy}/σ_{xx} at the chip surface in (x, y)=(1.5 mm, 0).

Configuration	$rac{\Delta\sigma_{xx}}{\sigma_{xx,\mathrm{an}}}$	$rac{\sigma_{yy,\max}}{\sigma_{xx}}$	$rac{\sigma_{xy,\max}}{\sigma_{xx}}$
A,B,C,D: $\phi = 0^{\circ}$	0.1%	0.8%	0.3%
B,C,D: $\phi = 0^\circ$; A: $\phi = 2^\circ$	0.2%	0.8%	0.2%
B,C: $\phi = 0^{\circ}$; A,D: $\phi = 2^{\circ}$	0.1%	0.8%	0.2%
B,C: $\phi = 0^{\circ}$; A: $\phi = 2^{\circ}$; D: $\phi = -2^{\circ}$	0.2%	0.8%	0.2%
A,C,D: $\phi = 0^\circ$; B: $\phi = 2^\circ$	0.2%	0.8%	1.9%
A,D: $\phi = 0^\circ$; B,C: $\phi = 2^\circ$	0.4%	0.7%	3.5%
A,D: $\phi=0^\circ$; B: $\phi=2^\circ$; C: $\phi=-2^\circ$	0.2%	0.7%	0.3%

analyze the effect (see illustration in Fig. 11). The investigation is summarized in Table II, where the ratio of the stress components are listed for different blade configurations. The ratios listed are the maximum values obtained in the resistor area. To obtain the maximum value of σ_{yy}/σ_{xx} , the (x,y)coordinate is (1.5 mm, 0). The maximum σ_{xy}/σ_{xx} value is found in the coordinate (1.5, 1.5 mm).

Notice, that the σ_{xx} stress in all cases varies less than 0.5% with respect to the analytical expression in Eq. (1). It is also seen that the transverse stress, σ_{yy} , does not depend on the blade misalignment. Thus, we assume that this stress is constant and less than 0.8% of the σ_{xx} stress. A misalignment of the outer blades, A and D in Fig. 11 does not have a significant impact on the stress distribution. However, a misalignment of the inner blades does change the shear stress distribution. As listed in Table II, the largest shear stress, σ_{xy}/σ_{xx} =3.5%, is obtained with a rotation in the same direction of the two inner blades.

The above values are extracted for a 2° horizontal rotation of the blades. This rotation is very large compared to the realistic value, but it is used in order to illustrate the influence. The milling machine used to fabricate the 4PB fixture has a very high precision (precision of 1 μ m), thus, it is not expected that a horizontal misalignment influences the stress distribution in the chip.

D. Vertical blade misalignment

A vertical rotation of the two inner blades as sketched in Fig. 12 results in a pure torsion of the beam. The resulting shear stress in the surface can be described by 29



FIG. 12. Schematic of chip exposed to torsion due to vertical misalignment α and β of the inner blades. The outer blades each applies a line force of F/2 to the chip of thickness *h*.



FIG. 13. (a) Schematic showing the design of the dedicated shear stress sensor chip. The chip consists of two resistors each placed in a corner of the 3×3 mm² resistor area in the middle of the chip. (b) The current I_{\parallel} is forced through the resistor and a voltage drop V_{\perp} is measured perpendicular to the current direction. The illustrations are not to scale.

$$\sigma_{xy} = Gh \frac{\Delta \psi}{\Delta x},\tag{6}$$

where the elastic shear modulus $G=Y/(2+2\nu)$, $\Delta \psi = \alpha + \beta$ in Fig. 12 measured in radians, and the distance between the two inner blades of the chip $\Delta x=12$ mm. For example, at a misalignment angle of $\Delta \psi = 0.1^{\circ}$, the shear stress component is as large as $\sigma_{xy}=4$ MPa.

E. Shear stress measurement and discussion

In order to investigate the actual shear stress component during measurements, we have fabricated a dedicated shear stress sensor chip with the long axis along [100]. The chip design is shown in Fig. 13 where the primed coordinate system is along the $\langle 100 \rangle$ crystal axes. A constant current I_{\parallel} is forced through the resistor while the potential drop V_{\perp} perpendicular to the current is measured. The relation between shear stress, σ'_{xv} , and resistance $R_H = V_{\perp}/I_{\parallel}$ is

$$\frac{\Delta R_H}{R_{\Box}}[100] = \frac{1}{2}\pi_{44}\sigma'_{xy},\tag{7}$$

where R_{\Box} is the sheet resistance. The result of this measurement is shown in Fig. 14 and proves that there is a nonideal shear stress distribution in the chip. The figure shows a linear relation between the resistance change and the applied stress σ'_{xx} . Thus, when increasing σ'_{xx} , the shear stress also increases. Inserting a measured value of $\pi_{44}=85 \times 10^{-11}$ Pa⁻¹ (measured at a doping concentration of $N_A=2.2 \times 10^{19}$ cm⁻³ and T=30 °C) in Eq. (7), we obtain $\sigma'_{xy}=3$ MPa and $\sigma'_{xy}=2$ MPa for the two resistors, respectively, at $\sigma'_{xx}=85$ MPa. Thus, we find a shear stress component value which is 3.6% and 2.5% of the σ'_{xx} value. The two resistors are placed in two different corners of the resistor area sketched in Fig. 10, thus, we assume that all resistors experience a shear stress which is in between these two values.

The shear stress is most likely caused by vertical misalignment of the inner blades, since a very small vertical misalignment results in a rather large shear stress component as argued in Eq. (6). In order to accommodate a shear stress of $\sigma'_{xy}=3$ MPa for $\sigma'_{xx}=85$ MPa, the vertical misalignment of



FIG. 14. Plot of the relative resistance change $\Delta R_H/R_{\Box}$ for the two resistors on the shear stress sensor chip at a temperature of 30 °C as a function of applied stress σ'_{xx} along [100]. The resistance change indicates the presence of a shear stress component σ'_{xy} proportional to σ'_{xx} .

the inner blades is $\Delta \psi = 0.07^{\circ}$. A FEM of the shear stress in a chip placed in a 4PB fixture as a function of vertical misalignment of the outer blades is performed in Ref. 30. They report a linear relation between the applied force and shear stress until full contact between the vertically misaligned blades and chip is reached. By further increasing the force, the shear stress contribution approaches a constant value. In Fig. 14, it is seen that the shear stress linearly depends on the applied force in the measured stress interval and taking the results from Ref. 30 into account, we find that the inner blades may be misaligned at a larger angle than $\Delta \psi = 0.07^{\circ}$. The actual misalignment angle can be measured by increasing the stress even further than what is seen in Fig. 14. Thus, if the shear stress is caused by a vertical misalignment of the inner blades, we conclude that the misalignment is at least $\Delta \psi = 0.07^{\circ}$. Another contribution to the shear stress may rise from an in-plane misalignment of the inner blades, as described in Sec. III B. A misalignment of $\phi \approx 2^{\circ}$ of both inner blades need to be present in order to accommodate the measured shear stress and considering the precision of the mechanical equipment used to machine the 4PB (precision = 1 μ m), this is not possible. A third contribution is a rotation of the whole PEEK 4PB fixture. The distance between the guiding rails in the bottom of the aluminum housing is 0.15 mm larger than the width of the 4PB base to accommodate thermal expansion of the two materials. Thus, a rotation of the 4PB fixture is possible. However, due to the very small air gap, the rotation angle is at most 0.3° and this does not significantly contribute to the shear stress. A fourth contribution to the shear stress is a misalignment of the slider and the base. However, since the two steel pins and guiding holes are specifically fabricated to fit each other with a very small air gap, this is not expected to induce a significant shear stress. Thus, we conclude that the present shear stress in the chip is an effect from a vertical rotation of the inner blades in the 4PB fixture.



FIG. 15. The relative change in resistance $\Delta R/R$ when increasing and decreasing the load. The measurement data from two resistors, $\theta=0^{\circ}$ and $\theta=90^{\circ}$ in Fig. 7(a), are plotted as a function of the applied stress σ_{xx} at T=30 °C. The solid lines are linear fits to the data. The lateral distance between the fit lines is a measure of the frictional force described by $\Delta \sigma=2\sigma_{f}$.

F. Electronic setup

The electronic instruments used to measure the voltage drop and to inject the current are all high precision instruments. The Keithley 2700 multimeter with multiplexer has a resolution of 1 μ V at a voltage of 1 V and the Keithley 2400 sourcemeter has a current source accuracy of 0.03% at 100 μ A. These uncertainties are significantly smaller than the uncertainties described in Sec. III A.

G. Friction

Frictional forces are expected to be present in the four point bending fixture since it consists of two parts where one is moving (slider) with respect to the other (base). Figure 15 shows a characterization of two resistors during sequential increasing and decreasing loads where the resistance change is measured at each applied load. For both resistors, a hysteresis loop is seen where the measured data are shifted to the right during increasing loads and to the left during decreasing loads. This behavior is explained by the frictional force, F_f , between the slider and the base, while we conclude from the force diagram sketched in Fig. 16 that the frictional force, F_{ch} , between the 4PB base and the aluminum housing does not affect the measurements. The only significant frictional force contribution is from the movement of the slider on the steel pins. The frictional force may be a multivalued



function of the velocity **v** in a stick-slip fashion. At rest, the magnitude of the applied force must exceed the static stiction-friction force, F_{f00} , to initiate motion, while in motion, the frictional force may be assumed to have a static and a dynamic component, $\mathbf{F}_{f} = \mathbf{F}_{f}(\mathbf{v}) = -F_{f0}\mathbf{v}/|\mathbf{v}| - \beta\mathbf{v}$, where $F_{f00} \ge F_{f0}$ and β is a viscous friction coefficient. In a steady state sequence of measurement steps in a given direction, the stiction-friction force is unimportant, as is the viscous friction, since the important frictional force is found when motion stops. Thus, we expect the relation, $F_{\text{sensor}} = F_s \pm F_{f0}$, between the force, F_{sensor} , measured on the force sensor and the actual force, F_s , on the sample in the 4PB fixture, where the sign depends on the direction of motion.

The force F_{f0} can be estimated from the width of the hysteresis loop in the stress direction, $\Delta \sigma \approx 2\sigma_{f0} \approx 8.2$ MPa, as seen in Fig. 15. This corresponds to a frictional force of $F_{f0}=0.11$ N. If this frictional force is load independent, it does not affect the piezoresistance characterization since only the slopes of the linear fits to the measured resistance change during increasing or decreasing loads are used. We do, however, slightly find different slopes for increasing and decreasing loads, such that they equal the mean slope $\pm 0.6\%$. This uncertainty must be included in the total uncertainty derived in Sec. III A.

H. Discussion

In Eq. (3), only the effect of the stress σ_{xx} was considered. However, if all in-plane stress components are included, we obtain a relative resistance change $\Delta R/R$ of

$$\frac{\Delta R}{R} = \sigma_{xx} \left[\frac{\pi_{11} + \pi_{12} + \pi_{44} \cos(2\theta)}{2} \right] + \sigma_{yy} \left[\frac{\pi_{11} + \pi_{12} - \pi_{44} \cos(2\theta)}{2} \right] + \sigma_{yy} (\pi_{11} - \pi_{12}) \sin(2\theta).$$
(8)

In *p*-type silicon, the piezocoefficient π_{44} is much larger than π_{11} and π_{12} , i.e., $|\pi_{11}/\pi_{44}| \approx 0.8\%$ and $|\pi_{12}/\pi_{44}| \approx 4.3\%$.¹ Moreover, the maximum value of the shear stress in the chip was measured to be 3.6% of σ_{xx} . Thus, it follows from Eq. (8) that the shear stress causes an insignificant error.

However, the contribution from the stress σ_{yy} cannot be neglected since its effect is proportional to the large piezocoefficient π_{44} in Eq. (8). From the FEM analysis, we conclude that the contribution from σ_{yy} is approximately 0.8% of the contribution from σ_{xx} . Including this contribution (and the contribution from the uncertainty of the linear fits from Sec. III G) in the uncertainty calculation of the magnitude of σ_{xx} in Sec. III A, we conclude that the piezocoefficient π_{44} is determined with an uncertainty of 1.8%.

IV. SAMPLE CHARACTERIZATION

FIG. 16. Force diagram of the 4PB fixture. The actuator applies a force $F_{\rm ac}$ to the 4PB fixture and the force on the force sensor is $F_{\rm sensor}$. The frictional force, $F_{\rm ch}$, between base and aluminum housing does not contribute to the frictional force seen in Fig. 15. The only contribution to this is the frictional force between base and slider F_{f} .

The chips presented in Sec. II E have been characterized in the 4PB setup. An example of a measurement series at T=30 °C on a chip with a doping concentration of $N_A=1.5$ $\times 10^{17}$ cm⁻³ is shown in Fig. 17 for increasing loads. The



FIG. 17. Relative change in resistance $\Delta R/R$ for the six resistors sketched in Fig. 7(a) as a function of the applied stress. The solid lines are the linear fits to the data series. Notice, the nonzero intercept with the *x* axis which is due to the frictional force explained in Sec. III G.

figure shows measurements on a chip with six resistors oriented at six different angles, θ , with respect to the stress direction, [110]. The slopes of the linear fits found in Fig. 17 are plotted for each resistor in Fig. 18 as a function of $\cos(2\theta)$ according to Eq. (3). The slope of the linear fit to these data is proportional to the piezocoefficient π_{44} , as seen in Eq. (3).

This analysis has been done for samples with the three doping concentrations given in Sec. II E at temperatures from T=30 °C to T=80 °C. The results are shown in Fig. 19 where the measured piezocoefficients are plotted as a function of doping density with the measurement temperature as parameter. The data in the figure are in good agreement with the doping concentration and temperature dependence in previous published results.² The analysis in Sec. III concluded that errors due to shear stress are negligible, the error contribution from σ_{yy} is at most 0.8%, and the uncer-



FIG. 18. The slopes of the linear fits in Fig. 17, the effective piezocoefficients π_{eff} , are plotted as a function of $\cos(2\theta)$, where θ is the angle between the resistor direction and the [110] σ_{xx} stress direction. The slope of the linear fit (solid line) is equal to $\pi_{44}/2$.



FIG. 19. The measured piezocoefficient π_{44} in *p*-type silicon as a function of temperature and doping concentration N_A .

tainty in the σ_{xx} value is 1.5%. The slope of the linear fit to the data is found with an uncertainty of 0.6%, thus, the presented 4PB setup allows measurements of the piezocoefficient π_{44} in *p*-type silicon at different doping concentrations and temperatures with an uncertainty of 1.8%.

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