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A Control Strategy for Induction Motors Feed from Single Phase Supply

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Abstract

It is often required that a three-phased asynchronous motor can run at variable speed, which makes it necessary to use a three-phase inverter driven from a DC-source. Today, most inverters are driven from the network using a simple diode bridge and an electrolytic capacitor. The problem with the simple diode bridge and the electrolytic capacitor is that current is only drawn for short periods, which gives rise to harmonic currents in the network. For small drive systems (motor+inverter), i.e. less than 1.5 kW, a single phase network outlet is often used. The paper describes a method whereby the problem with harmonic currents, when using a single phase network connection, can be solved.

Booster as AC-DC converter

There are many types of AC-DC converters that use a sinusoidal network current, e.g. forward, boost, and a number of resonant converters. On some points, the booster has an advantage with respect to the other types:

- 1. There is no capacitor that the energy is required to pass through as would be the case for the cuk-converter.
- 2. Furthermore, the harmonic ripple current caused by the booster is triangular rather than square-wave as in the case of the flyback and the forward converters.

One of the drawbacks of the booster is that the output voltage Vc across the capacitor is always greater than the amplitude of the network voltage.

Two examples of how it is possible to realise the boost converter are shown in the following.



Fig. 1. A four-quadrant, single phase AC-DC boost converter.

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The H-bridge looks like a simple diode bridge, but the diodes are exchanged with power switches. The network current is controlled by changing the voltage across the coil with the power switches. With an H-bridge it is possible to control the current, and therefore also the energy, in both directions, which is required in some applications with servo motors.

In many other applications it is not necessary to be able to send energy back to the network, and by eliminating this degree of freedom it is possible to reduce the number of switches from four to one.



Fig. 2. A single quadrant, single phase AC-DC boost converter.

The most expensive components in the boost converter are the coil, the capacitor, and the switches, but with thick film technology it becomes less expensive to realise the switches.

With modern-day thick film technology it would be convenient to keep the capacitor voltage so low that MOS-FET transistors can be used in both the booster and the inverter.

The size of the capacitor depends on the ripple in the capacitor current and on the energy that must be stored in the capacitor. When the switching frequency is high, and the line frequency is 50 Hz, the 100 Hz component will dominate the capacitor current. This is due to the fact that a three phased asynchronous motor in stationary use absorbs constant energy. It is therefore the capacitor that supplies energy to the inverter, when the line voltage is less than a certain value, which happens twice in every network period.



Fig. 3. A three phased VSI-inverter with a asynchronous motor as load.

If the line voltage Vmain is sinusoidal you find:

where V0, I0, and P0, all are maksimum values.

The mean value of the power supplied from the network to the booster is:

$$< P_{main} > = \frac{P_0}{2} = \frac{V_0 I_0}{2}$$

Due to symmetry the power is only considered in the period:

$$0 < w_1 t_0 < \pi$$

To examine the power flow in the capacitor the following is computed:

If it is asumed that the capacitor is infinitely big it will absorb energy from the network when:

$$\frac{\pi}{4} < w_1 t_0 < \frac{3\pi}{4}$$

and deliver energy to the inverter in the rest of the period.

This means that the capacitor must deliver energy to the

inverter in 50% of the network period, which corresponds to 5ms, when the line frequency is 50 Hz.

If it is possible to reduce the power consumption in the inverter when the line voltage is low, the size of the capacitor can be reduced significantly.

Simulation of the booster

The booster has been simulated on a PC using Matlab. In Figure 4 Vc/V0 = 1.2 and it is assumed that the inverter can be controlled so that *Pmain = Pload*. It is typical for a boost converter that the output voltage, which is equal to the capacitor voltage, is 20% higher than the maximum line voltage.

When *Pmain* = *Pload* the capacitor current is zero, and the output voltage is constant.



Fig. 4. The current and power patterns in a single phase boost converter.

The current in the switch ik (See fig. 2) is only zero, when the line voltage is zero.

In the simulations the capacitor is set to 100uF. The amplitude of the line voltage and the line current are set to 311V and 10A respectively, which gives a power output of 1.6kW.

Field vector control of the asynchronous motor to reduce L and C in the booster

The equations for the asynchronous motor in field vector coordinates are given by:

where
$$Rs = \text{stator resistance}$$

Rr = rotor resistance

L0 = the mutual inductance in the motor

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Fig. 5. The electrical diagram for the asynchronous motor.



Fig. 6. Current vector diagram.

(1)
$$di_{mv}/dt = (i_{sd} - i_{mv})/T_r$$

(2)
$$d\rho/dt = i_{sq}/(T_r i_{mv}) + pw_r$$

(3)
$$i_{sd} = i_{sa}\cos(\rho) + i_{sb}\sin(\rho)$$

(4)
$$i_{sq} = -i_{sa}\sin(\rho) + i_{sb}\cos(\rho)$$

(5)
$$m_r = 2/3pL_0^2/(L_r + L_r)i_r$$

(6)
$$dw/dt = (m_1 - m_{loc})/J$$

L1 = the stray inductance in the stator = L2L2 = the stray inductance in the rotor = L1p = number of pole-pairs in the machine

All these values are per phase, when the motor is coupled in a star-configuration.

Furthermore:

Tr =	rotor time constant = $(L0+L2)/Rr$
rho =	angle between stator current phasor in stator
	and rotor flux coordinates
imr =	magnetising current = magnitude of rotorflux
	divided by LO
isd =	direct part of stator current phasor in rotor
	flux coordinates
isq =	quadrature part of stator current in rotor flux
-	coordinates
isa =	direct part of stator current phasor in stator
	coordinates
isb =	quadrature part of stator current phasor in
	•

	stator coordinates		
md =	the electrical moment		
wr =	rotational speed		

It is thus possible to control the rotor flux size/length and position with the stator current phasor. Normally, when field vector control is used in practice, you have maximum field on the machine, which is the same as keeping the length of the rotor flux phasor constant.

It may be seen that the size of the rotor flux is a lowpass filtering of the current isd, where the time constant equals the rotor time constant, which is the mutual inductance divided by the rotor resistance.

As the efficiency of an asynchronous motor depends on the air gap, motors are normally made with a small air gap, making the mutual inductance and the rotor time constant large. If *isd* is changed quickly there will be no influence on *imr*.

In formula 5 the moment is expressed as the motor constant times *imr* and *isq*. This means that when *isq* is constant, the moment will be constant even if *isd* is changed. By utilising the magnetising inductance and the rotor resistance it has become possible to manipulate the stator current without changing the moment.

The above description corresponds to keeping the stator flux phasor in the Q-direction constant and changing it in the Ddirection. The stator flux phasor thus performs a suitably fast movement in parallel with the rotor flux phasor.

A practical relisation of the algorithm

As described in section before the mean value of *isd* is equal to *imr*, as the rotor circuit acts as a lowpass filter.

The control strategy entails splitting it into a moment control and a flux control. Using a sampling, you must first:

1. Measure wr, isl and is2

and next

2. compute isa, isd, isq, md, and imr.

Moment control:

3. The moment regulator computes *ma^{ref}* using *wr^{ref}* and *wr*, whereafter

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4. $isq^{ref} = 1/(2/3 \ p \ L0^2/(L0+L2) \ imr] \ md^{ref}$ is computed.

Flux control:

5. The flux regulator computes isdref.

The difficult part in this control strategy is finding isd^{ref} , which is done by finding the power consumed by the motor. This can be done using the stator voltages, which either can be measured or computed.

In field vector coordinates the phasor for the stator voltages *usd* and *usq* are given by:

$$\frac{u_s}{t} = u_{sd} + j * u_{sq}$$

$$u_{sd} = 2L_1(L_0 + L_1/2)/(L_0 + L_1)di_{sd}/dt$$

$$+R_s i_{sd} + L_0^2/(L_0 + L_1)di_{md}/dt$$

$$-2L_1(L_0 + L_1/2)/(L_0 + L_1)i_{sq}d\rho/dt$$

$$u_{sq} = 2L_1(L_0 + L_1/2)/(L_0 + L_1)di_{sd}/dt$$

$$+R_s i_{sq} + L_0^2/(L_0 + L_1)i_{md}\rho/dt$$

$$+2L_1(L_0 + L_1/2)/(L_0 + L_1)i_{sd}d\rho/dt$$

and the power is computed as:

$$P_{load}(t) = 2/3 \underbrace{u_s(t) \cdot i_s(t)}_{2/3}(u_{sd}(t) i_{sd}(t) + \underbrace{u_{sn}(t) i_{sn}(t)}_{2/3}(u_{sn}(t) i_{sn}(t))$$

The inverter is considered without losses, so that *Pload* is equal to the power that the AC-DC converter must deliver to the inverter and the motor. To reduce the size of C, isd must be chosen so that *Pload* equals *Pmain*.

An estimate of disq is the difference between isq^{ref} and isq, and in the quasi-stationary mode, which corresponds to the intended application of this control method, disq/dt becomes zero.

The mean value of *isd* must be equal to *imr* (according to formula (1)), and *isd*^{*wt*} is chosen as:

$$i_{sd}^{ref} = i_{mr}^{ref} + \sum_{k=1}^{N} I_{sd,k} \cos(kw_1 t + \phi_k)$$

None of the cosine parts contribute to the mean value of *isd*, and the reason for choosing isd^{ef} as succession of cosine functions is that disd/dt and dimr/dt can be computed exactly.

The exact expressions for disd/dt and dimr/dt are:

As the control is digital, $t=n^*T$, where T is the sampling

$$di_{sd}/dt = -w_1 \sum_{k=1}^{N} kI_{sd,k} \sin(kw_1 t + \phi_k)$$

$$\downarrow$$

$$di_{m}/dt = -w_1 \sum_{k=1}^{N} kI_{sd,k} \sin(kw_1 t + \phi_k)$$

$$+T_r w_1^2 \sum_{k=1}^{N} I_{sd,k} \frac{k^2}{1 + (kT_r w_1)^2}$$

$$\cdot \cos(kw_1 t + \phi_k)$$

period. By using an LMS (least Mean Square) algorithm, the values are found for *Isd*, k and ϕk that give the smallest error between *Pmain* and *Pload*, as well as having *Pmain* equal to *Pload*.

As isd^{ref} , isq^{ref} , and *rho* are known, you may compute isa^{ref} and isb^{ref} , and these values are then carried to the current control that controls the phase currents:

$$i_{sl} = i_{sl}^{ref} = 2/3i_{sa}^{ref}$$

$$i_{s2} = i_{s2}^{ref} = 1/\sqrt{3}i_{sb}^{ref} - 1/3i_{sa}^{ref}$$

$$i_{s3} = -i_{s1} - i_{s2} = -i_{s1}^{ref} - i_{s2}^{ref}$$

$$i_{s1} + i_{s2} + i_{s3} = 0$$

Simulation of control algorithm for the motor

The simulations and measurements are performed on an ASEA motor and inverter with the following data:

$$Rs = 0.88$$
 Ohm
 $Rr = 0.84$ Ohm
 $L0 = 0.122$ H
 $L1 = 5.27$ mH
 $L2 = 5.27$ mH
and
 $Vc = sqrt(2)$ 220V 1.2 = 373 V
 $is1,max = is2,max = is3,max = 20$ A

which gives a rotor time constant Tr of 152 ms.

On Figures 7 through 13 are shown the patterns of power, currents, voltages and moments, when the motor is controlled in the standard manner with *isd* kept constant.



Fig. 7. The main power Pmain and the load power Pload.



Fig. 8. The spectrum for main power *Pmain* and the load power *Pload*.

 $\mathcal{L}_{\mathbf{k}}$











Fig. 11. The stator voltages usd and usq in field vector coordinates.





Fig. 13. The stator currents *is1* and *is2* in stator coordinates.

On Figures 14 through 20 are shown the patterns of power, currents, voltages and moments, when the motor is controlled in a new manner and *isd* is varied.



Fig. 14. The main power Pmain and the load power Pload.

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Fig. 15. The spectrum for the main power *Pmain* and the load power *Pload*.



Fig. 16. The normalize value for the capacitor voltage Vc and the load current *iload* and the main power *Pmain*.



Fig. 17. The magnetization current *imr* and stator currents *isd* and *isq* in field vector coordinates.



Fig. 18. The stator voltages *usd* and *usq* in field vector coordinates.

From Figures 14 to 16 it may be seen that *Pload* is nearly equal to *Pmain*, when the capacitor voltage is large as is the case for the boost converter.

Reduction of the value of the capacitor C

The power Pc and the energy Ec in the capacitor are



Fig. 19. The electrical moment md.



Fig. 20. The stator currents *is1* and *is2* in stator coordinates.

computed at each sampling point and defined by:

$$P_{c}(n) = 2/3(P_{main}(n) - P_{load}(n))$$

$$t$$

$$E_{c}(n) = P_{c}(n)T = 1/2C(dV_{c}(n))^{2}$$

$$= 1/2C(V_{c}(n) - V_{c}(n-1))^{2}$$

where T is the sampling period and t=n*T.

The capacitor voltage and ripple in the capacitor voltage at the sampling point $t=n^*T$ becomes:

$$V_{c}(n) = V_{c,ripple}(n) + V_{c}(n-1) = \sqrt{(2P_{c}(n)T)/C + V_{c}^{2}(n-1)}$$

$$V_{c,ripple}(n) = V_{c}(n) - V_{c}(n-1) = \sqrt{(2P_{c}(n)T)/C + V_{c}^{2}(n-1)} - V_{c}(n-1)$$

In the Table I below may be seen how the ripple in the capacitor voltage changes as a function of control method and of capacitor value. The results in the table are taken from the simulations shown in Figures 7 through 20.

The table shows that C or Vc, ripple can be reduced by

 Table I

 The size of the capacitor and the ripple in the capacitor voltage.

Normal method		New method		Reduction
C (10 ⁴ F)	V _{cripple} (V)	C (10 ⁴ F)	V _{erippie} (V)	(%)
1	1123	1	534	52
3	555	3	230	59
10	230	10	87	62
100	30	100	10	67
97	31	30	31	69

several percent.

Measurements on the inverter and the motor



Fig. 21. The measurement value of the stator current *is1* in stator coordinates.

All signal is passed through a 5 order FIR-lowpass filter with 256 taps. The filter has a cutoff frequency of 200Hz, and the PWM switching frequency is 20 kHz.

From Figure 21 it may be seen that the stator current *isl* look much like the simulated result in Figure 20.



Fig. 22. The measurement value of electrical torque md.

The moment barely changes, even though the stator current is hardly sinusoidal with this method (See figure 22).



Fig. 23. The measurement value of the load power Pload.

The meausrement value of the load power *Pload* in Figure 23 is look like the simulated result in Figure 14.

Conclusions

It can be concluded that the measurements and the simulations give the same results. The new control principle will in some applications reduce the cost of the AC-DC converter concomitant with the converter absorbing a sinusoidal line current.

It is beneficial to design the motor especially for this control method. For example, it would be a good idea to design the stator for a larger current and a smaller voltage. This is due to the fact that the losses in the stator are increased by this method, whereas the losses in the rotor are unchanged. Furthermore, it is easier to control in such a way that *Pload* equals *Pmain*, when the capacitor voltage is greater than the EMF induced in the machine.