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Influence of inductance induced noise in an YBa₂Cu₃O₇ dc-SQUID at high operation temperatures

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The voltage modulation depth of a high T_c dc-SQUID was measured at temperatures close to T_c and compared to a model by Enpuku *et al.* where the flux noise from the SQUID inductance is taken into account. The device was an YBa₂Cu₃O₇ dc-SQUID made on a bicrystal substrate of SrTiO₃. The design was of the Ketchen square-washer type with an inductance of 67 pH. Measurements were made in a temperature interval from 75 to 87 K, where the voltage modulation depth changed from 4.5 to 1.4 μ V in close agreement with the model.

Superconducting quantum interference devices (SQUIDs) have been made of high T_c materials by many groups now, and low noise has been achieved at liquid nitrogen temperature both in the white¹ and 1/f noise regimes.^{2,3} At this high temperature, it is important to understand how the thermal noise affects the behavior of the SQUID, the $k_B T$ value is 18 times higher at 77 K compared to 4.2 K. This high thermal noise creates excess voltage in the Josephson junctions of the SQUID and causes noise rounding in the current-voltage characteristics. The effect has been studied by Gross *et al.*⁴ for grain boundary junctions in YBa₂Cu₃O₇, and the results were in good agreement with the Ambegaokar-Halperin (AH) model.⁵ Another effect of the thermal noise that has been less studied is the effect of noise flux generated by noise currents in the SQUID loop arising from thermal noise in the junctions. However, in a recent paper by Enpuku et al.⁶ this effect was treated and calculations showed that this noise source lowers the voltage modulation depth of the SQUID when the SQUID inductance is high. This is important for practical SQUID applications because a moderately large area, and hence a high inductance, is necessary for SQUIDs designed to be coupled to the outside world through a flux transformer.

An analytical expression for the voltage modulation was given: 6

$$\Delta V = \frac{7}{\pi^2} \frac{I_c R_n}{1+\beta} \left(1 - 3.57 \frac{\sqrt{k_B T L}}{\Phi_0} \right).$$
(1)

 I_c and R_n are the critical current and normal resistance of the Josephson junctions in the SQUID, $\beta = 2LI_c/\Phi_0$ is the screening parameter,⁷ L is the SQUID inductance, and $\Phi_0 = h/2e$ is the flux quantum. The theoretical expression was compared to experimental work and a rather good agreement was found, but with some scatter.⁶ Similar results have also been seen by the Berkeley group.⁸ To further elucidate the issue, which is important for the correct modeling of a SQUID at these temperatures, we made a dc-SQUID of YBa₂Cu₃O₇ (YBCO) that could be operated close to the critical temperature of the material and with well defined parameters.

eters. This SQUID was then used for measurements of the temperature dependence of the voltage modulation depth close to T_c .

The SQUID was designed to be optimal at 77 K operation. A number of design rules have to be followed. The critical current for a junction should give a Josephson coupling energy that is much larger than the thermal energy. Earlier work⁹ has shown that a factor of 5 in difference is sufficient. This gives a minimum critical current of 16 μ A at 77 K. This current, taken together with the requirement that β should be close to 1, gives a maximum SQUID inductance of 64 pH. We designed a SQUID of the traditional Ketchen type¹⁰ from these requirements (Fig. 1). A hole diameter d of 20 μ m and a slit length of 120 μ m were chosen. This gives an estimated¹¹ SQUID inductance of $L_{hole}+L_{slit}=31$ pH+36 pH=67 pH, where $L_{hole}=1.25 \ \mu_0 d$ and $L_{slit}=0.3$ pH/ μ m.

The SQUID was made on a bicrystal, where the critical currents of the junctions could be controlled by selecting an appropriate bicrystal misorientation and junction size.¹² A bicrystal of (001) oriented SrTiO₃ with an in-plane misorientation angle of 36.8° was chosen for the experiment. A grain boundary with this misorientation angle in a YBCO film has a critical current density of 10^{3} A/cm², which implies a junc-



FIG. 1. The SQUID was made using the square-washer Ketchen design with an YBa₂Cu₃O₇ film deposited on a bicrystal SrTiO₃ substrate. The center hole side is 20 μ m and the slit is 120 μ m long. This gives a SQUID inductance of 67 pH. The Josephson junctions, on top of the bicrystal boundary, are 8 μ m wide.



FIG. 2. The *I*-*V* curve for the SQUID junctions at 83 K corresponded to the RSJ model. The AH fit is shown by the diamonds. The inset shows some V vs Φ curves for the SQUID for this temperature at different bias points.

tion width of 8 μ m to achieve a critical current of 16 μ A in a 200 nm thick film. The chip was made in a standard photolithographic process. Briefly, the processing steps were the following:

- Alignment marks of BaF₂ were deposited on the bicrystal substrate by thermal evaporation through a liftoff stencil to allow for alignment of the junctions relative to the bicrystal boundary.
- (ii) A 200 nm c-axis oriented epitaxial film of YBCO was deposited by a standard laser deposition process.¹³ The film was covered with a 40 nm layer of gold that was *in situ* laser deposited on the YBCO film to ensure low contact resistance and to protect the YBCO from resist contamination. The Au/YBCO layer was then patterned with photolithography and Ar ion milling.
- (iii) A contact layer for bonding (15 nm Cr+250 nm Au) was deposited by thermal evaporation through a liftoff mask.
- (iv) An additional brief ion milling step was used to remove the parts of the *in situ* gold layer that were shunting the junctions. This process gave the possibility of aligning the junctions to the bicrystal boundary with an accuracy of about 1 μ m. It was also easy to connect the SQUID to the measurement setup with gold wire bonding.

The measurements were performed in a magnetically shielded liquid helium cryostat in an electromagnetically screened room. The current-voltage (I-V) curves of the SQUID were in agreement with the resistively shunted junction (RSJ) model (Fig. 2), and a response to magnetic field could be seen up to temperatures close to T_c (90 K). The washer of the SQUID was measured to focus the magnetic field six times, as deduced from the period of the response compared to the hole and slit area of the SQUID. The maximum critical current of the SQUID at 77 K was 76 μ A with a 1 μ V criterion. This implies a junction critical current of 38 μ A, about two times the design value of 16 μ A. The critical current is a strong function of the temperature this close to T_c (Fig. 3), which explains the deviation.



FIG. 3. In the upper graph, the junction critical currents I_c (one half times the measured SQUID values) are shown. Both values from a 1 μ V criterion and values achieved from fits to the AH model are shown. The I_c values are used to calculate the voltage modulation depth ΔV from the theory by Enpuku *et al.* (lower graph). Both calculated values from a 1 μ V criterion $[\Delta V (1 \ \mu V)]$ and from the AH model $[\Delta V (AH)]$ are shown. They are compared to the measured ΔV values.

The voltage modulation depth ΔV was measured at several temperatures from 75 to 87 K. The bias current of the SQUID was adjusted to give the maximum voltage modulation depth for a given temperature. The values are shown in Fig. 3, where they are compared to the theoretical values from Eq. (1). In the calculation of the theoretical values the estimated inductance of 67 pH and measured critical currents with a 1 μ V criterion or critical current values calculated from the AH model were used. As can be seen from Fig. 3, the agreement with the calculations from Eq. (1) is good. Note that there are no free parameters that are fitted to the data. The calculations underestimate the voltage modulation depth when the 1 μ V criterion is used. A better fit is achieved if the critical current is calculated from the I-V curves with the AH model. The 1 μ V criterion underestimates the "real" I_c values of the AH model slightly.

The noise of the SQUID at 77 K was measured with standard flux-locked SQUID electronics. A field modulation signal of 100 kHz was used to modulate the SQUID at $\Phi_0/4$. The voltage response was demodulated and connected to a spectrum analyzer. The white noise level of the SQUID was $1.2 \times 10^{-9} \Phi_0^2$ /Hz, which corresponds to an energy resolution of 3.8×10^{-29} J/Hz. This can be compared to the theoretical white noise limit:¹⁴

$$S_{\Phi}(f) = \frac{S_{v}(f)}{(dV/d\Phi)^{2}} = \frac{\left[1 + \frac{1}{2}(I_{c}/I)^{2}\right](4k_{B}TR_{d}^{2}/R_{n})}{(dV/d\Phi)^{2}}.$$
 (2)

 $S_{\Phi}(f)$ is the flux noise, $S_{v}(f)$ the voltage noise, $dV/d\Phi$ the

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transfer function of the SQUID, and I and R_d the current and dynamic resistance of the Josephson junctions at the bias point. For our parameters for the 79 K measurement, the theoretical white noise limit was $3.2 \times 10^{-10} \Phi_0^2$ /Hz. This is within an order of magnitude of the measured noise value. The crossover frequency to 1/f noise was at 260 Hz. The low noise level ensures us that no external noise, lowering the voltage modulation depth, is coupled to the SQUID. Hence, any discrepancy between the model and the experimental data must be accounted for in the model.

In conclusion, we have checked the theory for the reduction of the voltage modulation depth due to flux noise from the SQUID inductance that was published recently by Enpuku *et al.*⁶ The SQUID used for the test was a bicrystal SQUID of the Ketchen design made in a YBCO film that was optimized for 77 K operation. The behavior of this low noise SQUID was close to the theoretical predictions, which means that our measurements verify the theory and it can be used for further modelling and optimizations of SQUIDs at high temperatures. The results also emphasize the importance of accounting for different inductances when noise data for SQUIDs taken at temperatures in the 77 K range are compared.

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