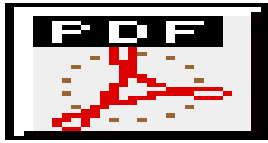
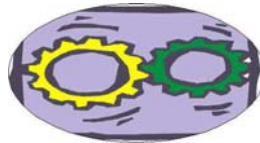


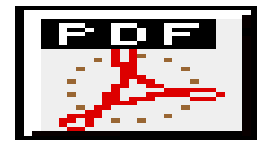
# Desenvolupament d'interfícies HW per a FPGA des de Matlab



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