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Continuous-feed nanocasting process for the synthesis of bismuth nanowire composites[†]

K. Vandaele, (b^a J. P. Heremans, ^{bcd} I. Van Driessche,^a P. Van Der Voort (b^a and K. De Buysser (b *^a)

We present a novel, continuous-feed nanocasting procedure for the synthesis of bismuth nanowire structures embedded in the pores of a mesoporous silica template. The immobilization of a bismuth salt inside the silica template from a diluted metal salt solution yields a sufficiently high loading to obtain electrically conducting bulk nanowire composite samples after reduction and sintering the nanocomposite powders. Electrical resistivity measurements of sintered bismuth nanowires embedded in the silica template reveal size-quantization effects.

Thermoelectrics could play an important role in waste heat recovery and solid-state cooling as they possess the ability to convert heat directly to electricity and *vice versa*.^{1,2} The advantages of thermoelectrics include quiet operation, no mechanical moving parts, and a long lifetime. However, the limiting factor preventing the large-scale production of thermoelectrics for both power production and solid-state cooling is their low efficiency. A material's efficiency is described by the figure of merit, denoted zT, and is defined as: $zT = S^2 T / \rho \kappa$, with *S* the Seebeck coefficient, ρ the electrical resistivity, and κ the thermal conductivity.

Pioneering work by Hicks and Dresselhaus³ theoretically predicted that nanostructured thermoelectrics, could have an enhanced zT value compared to their bulk counterparts.^{3,4} Such increase of zT was predicted in cylindrical Bi nanowires by Lin *et al.*⁵ The synthesis of Bi nanowires by pressure injection⁶ of a melt in porous, anodized alumina was reported Zhang *et al.*, while Heremans *et al.* reported the synthesis of Bi nanowires by the impregnation of Bi vapour in an alumina template.⁷ Attempts at making Bi nanowire thermoelectrics⁷ have resulted in large thermopowers, but comparatively small *zTs*. One issue has been that no Bi_{1-x}Sb_x nanowires could be prepared by conventional methods due the large difference in melting point and vapour pressure between Bi and Sb, whereas theory⁸ predicted superior performance in these alloy wires. In addition, the high thermal conductivity of an alumina matrix causes parasitic thermal losses, deteriorating the zT.

A suitable chemical route to synthesize non-siliceous nanostructured and mesoporous materials is by means of a hard template replication method, known as nanocasting.^{9,10} Typically, the pores of mesoporous silica are used as a nano-mold or template. Since mesoporous silica materials possess pores with dimensions ranging from approximately 2 to 50 nm, they are perfect as a template material to synthesize Bi nanowire composites with diameters well below the threshold of 50 nm to observe a semimetal-to-semiconductor transition.⁵

A crucial step in the nanocasting replication process is the impregnation of the precursor material inside the template's pores and the conversion of that precursor to the target material. Typically, a metal salt dissolved in ethanol or water is loaded inside the mesoporous template through conventional solvent impregnation,¹¹ dry impregnation or incipient wetness,¹² double solvent,^{13,14} or evaporation-induced impregnation method, and subsequently subjected to a thermal treatment.^{9,10,15–17} The replicated mesoporous material is obtained after template removal through chemical etching with NaOH or HF. However, problems associated with the current impregnation methods of mesoporous templates include the low filling degree, inhomogeneous filling of the pores, or the deposition of precursor material on the external surface of the porous template. In many cases, multiple loading steps are required to obtain an acceptable degree of loading.^{11,18,19} Ordered, mesoporous silica templates were used by Xu et al. for the synthesis of Bi nanowires by the decomposition of triphenylbismuth in supercritical toluene.²⁰ However, only small quantities of material were obtained.

In the present work, a scalable, continuous-feed impregnation process based on the two-solvent impregnation method²¹ was used (Fig. 1). This process was developed to fill the pores of a meso-porous silica template with bismuth metal salt by impregnating the template with a diluted acidic precursor solution. A low-temperature reduction treatment (220 to 230 $^{\circ}$ C) allowed the conversion of the impregnated bismuth salt to metallic bismuth nanowires confined in the template's pores. Powder processing

^a Department of Inorganic and Physical Chemistry, Ghent University,

Krijgslaan 281-S3, 9000 Gent, Belgium. E-mail: Klaartje.DeBuysser@ugent.be

^b Department of Mechanical and Aerospace Engineering, Columbus, Ohio, 43210, USA

^c Department of Physics, The Ohio State University, Columbus, Ohio, 43210, USA
^d Department of Materials Science and Engineering, The Ohio State University, Columbus, Ohio. 43210, USA

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Fig. 1 Schematic of the continuous-feed impregnation procedure in refluxing *n*-octane (bp. 125 °C). (1) The aqueous precursor solution is added to the silica/*n*-octane dispersion using an addition funnel or syringe pump. (2) The polar precursor solution and *n*-octane from a two-phase system. Meanwhile, the precursor infiltrates the silica template through capillary impregnation. (3) As the impregnation is performed in refluxing *n*-octane, the water boils off and the diluted precursor solution inside the pores becomes more concentrated until all water is removed and only the precursor salt remains. Partial decomposition of the precursor salt may occur. (4) The aqueous solution is collected by means of a Dean–Stark separator.

techniques allowed the synthesis of bulk-sized samples composed of Bi nanowires, referred to as bulk nanowire composites. Electrical resistivity measurements were performed to identify size quantization effects in the bulk nanowire composite.

The two-solvent impregnation method²¹ (Fig. S3, ESI†) consists of the addition of precursor solution in an amount equal to the pore volume of the template in a dispersion of the template in hexane. In the process described here, the precursor solution is added continuously to a dispersion of the mesoporous silica template in refluxing toluene or *n*-octane. By means of a Dean–Stark separator, the aqueous phase is eliminated from the system, leading to the immobilization of the metal precursor salt inside the template's pores. All chemicals are listed in the ESI† (Section 1).

In a typical synthesis, 1 g SBA-15 or KIT-6 mesoporous silica (Section 3, ESI^{\dagger}) with a pore volume of 1 cm³ g⁻¹ was dispersed in 100 mL n-octane in a 250 mL fluoropolymer (PFA) round bottom flask (Section 4, ESI[†]). The amount of metal salt required to fill the template's pores was derived based on the template's pore volume and the density of the precursor salt. For the impregnation of BiCl₃, the precursor solution was prepared by dissolving 3.5 g Bi_2O_3 in 12 mL 36 w% HCl and 24 mL H₂O. The precursor solution was added to the dispersion of silica in *n*-octane at a rate of 4 mL h^{-1} using a syringe pump, while the non-polar solvent was refluxing (Fig. S4, ESI[†]). All studied samples are listed in Table S1 (ESI[†]). The impregnated silica powder was collected through filtration and subsequently reduced in a sealable flow furnace at 230 °C for 12 h in a N₂-5% H₂ gas flow mixed with hydrazine vapour. The hydrazine vapour was carried through the furnace by bubbling the N_2 -5% H_2 gas through a hydrazine monohydrate solution (Fig. S7, ESI⁺). The Bi-SiO₂ nanowire composite powder was removed from the flow

furnace inside an argon glovebox, and approximately 2 g sample was transferred to a 10 mm \emptyset graphite die, which was sealed with rubber glue to avoid oxidation when transferred to the spark plasma sintering (SPS) device. The nanowire composite powder was sintered at 230 °C for 20 min in a vacuum under a uniaxial pressure of 50 MPa. The sample preparations for four-probe electrical transport measurements and mounting inside the cryostat were performed in an argon glovebox. Typical sample dimensions for electrical transport measurements were 1.5 × 1 × 7 mm. For N₂ sorption analysis, the silica template of the reduced Bi–SiO₂ powder was dissolved in a 1 mol L⁻¹ NaOH solution for 3 h. The mesoporous Bi powder was isolated by centrifugation and washed four times with water.

The X-ray powder diffraction (XRPD) patterns in Fig. S10 (ESI[†]) show that the bismuth precursor salt reduced to bismuth at 230 °C for 12 h in the presence of hydrazine vapour. When measured under argon atmosphere, no secondary phases were observed. However, upon removing the tape, which sealed the sample from air, the XRPD pattern indicates the presence of Bi₂O_{2.5}, which formed within seconds. KIT-6 mesoporous silica, with a 3D interconnected mesopore system, was used as template for the preparation of mesoporous bismuth powder for N₂ physisorption measurements and for electric transport measurements. Mesoporous bismuth, was obtained after chemical etching of the KIT-6 silica template. A BET surface area of 20 m² g⁻¹ and 13 m² g⁻¹ was obtained when Bi(NO₃)₃ and BiCl₃, respectively, were used as precursor salts (Fig. S11, ESI[†]). The pore volumes were 0.14 and 0.06 cm³ g⁻¹, respectively.

SBA-15 mesoporous silica, which possesses hexagonally packed, cylindrical mesopores, was used as template for the TEM analysis shown in Fig. 2. The TEM images depict oxidized bismuth nanowires (due to sample preparation for TEM) with a diameter between 5 and 10 nm confined in the template's pores (a and c) and the replicated nanowires after etching the template (b and d). The appearance of the nanowires depends on the precursor salt used. Namely, for Bi(NO₃)₃, rough nanowires were obtained, whereas smooth wires were obtained for BiCl₃. The latter also forms a melt at approximately 228 °C according to DTA shown in Fig. S6 (ESI†). The theoretical volume fractions of Bi confined in the pores are 12 v% and 30 v% for Bi(NO₃)₃ and BiCl₃, respectively.

Fig. 3 depicts the carrier concentration of a sintered $Bi-SiO_2$ (KIT-6) nanowire composite sample as function of 1/2kT. By fitting the carrier concentration for an intrinsic semiconductor through the data points, a band gap of 45 meV was calculated. Fig. 4 compares the electrical resistivity of bulk Bi with that of Bi nanowire composites normalized by their value at 300 K. The data shows the occurrence of size-quantization effects in the nanowire composite samples, since the electrical resistivity behaviour follows a semiconducting behaviour instead of a metallic one, as in bulk Bi.

The loading of the pores changes significantly depending on the precursor salt used, given the difference in density and molecular weight of $Bi(NO_3)_3$ and $BiCl_3$. If we consider complete filling of the pores with the precursor salts, 12 v% and 30 v%, Bi can be deposited in the pores after reduction of $Bi(NO_3)_3$ and $BiCl_3$, respectively. How the loading could be further enhanced is shown in Table S2 (ESI†). KIT-6, a mesoporous silica template with an interconnected pore system, was used for the synthesis of mesoporous Bi. No high-BET surface area was obtained for either sample,



Fig. 2 (a) TEM image of SBA-15 filled with 12 v% Bi, prepared by the impregnation of Bi(NO₃)₃·5H₂O dissolved in 5 mol L⁻¹ HNO₃, with toluene as non-polar solvent and reduced at 220 °C in a hydrazine loaded Ar–5% H₂ gas flow for 12 h. (b) Bi nanowires after chemical etching of the silica template of the sample in 1 mol L⁻¹ NaOH solution for 3 h. (c) TEM image of SBA-15 filled with 30 v% Bi, prepared by the impregnation of BiCl₃ dissolved in a 4 mol L⁻¹ HCl–80 v% MeOH solution, with *n*-octane as non-polar solvent and reduced at 220 °C in a hydrazine loaded Ar–5% H₂ gas flow for 12 h. (d) Bi nanowires after chemical etching of sample c. Note that Bi nanowires oxidize spontaneous in air, therefore the analysed samples referred to as Bi are oxidized.



Fig. 3 Carrier concentration of bulk bismuth and 29 v% Bi–SiO₂ (KIT-6) nanowire composite sample as function of 1/2kT. The lower carrier concentration of the nanocomposite sample compared to bulk Bi can be attributed to size-quantization effects in the nanowires. The carrier concentration for the intrinsic semiconductor was derived by $n_i = n_0 e^{-E_g/2kT}$. The band gap obtained by the fit through the data points yields: $E_g = 45$ meV, which corresponds to Bi nanowires with diameter 20 nm according to Lin *et al.*⁵ Note that all sample preparations and measurements were performed under inert atmosphere.



Fig. 4 Electrical resistivity of 29 v% $Bi-SiO_2$ nanowire composite sample and bulk bismuth normalized by their value at 300 K. Where the normalized resistivity of bulk Bi drops, and thus exhibits a metallic character when cooled down to 77 K, the relative resistivity of 29 v% $Bi-SiO_2$ increases, providing evidence for size-quantization effects in sintered bismuth nanowire composites. The carrier mobility is depicted on the right axis. Note that all sample preparations and measurements were performed under inert atmosphere.

which primarily is due to the high density of Bi (9.78 g cm^{-3}) and its low melting point (271.5 °C). Bismuth also can leach out of a template's pores during reduction of the bismuth salt. This was mostly observed for BiCl₃, as it also forms a melt at approximately 228 °C (Fig. S6, ESI[†]). We believe that the smooth nanowires in Fig. 2 were obtained specifically because of the formation that melt. The use of BiCl₃ as precursor was preferred since it can lead to a higher loading of Bi inside the template. This is particularly important for the synthesis of nanocomposites, as a sufficiently high loading of Bi in the template is required to form an electrical percolation path when the Bi-SiO₂ nanowire composite powder were sintered. For the same reason, the use of a silica template with 3D interconnected pore system (KIT-6) is preferred over a template with linear pores (SBA-15). Electric transport measurements were performed on a bulk nanowire composite sample using KIT-6 with a pore diameter of approximately 7 nm as silica template. Based on the amount of BiCl₃ precursor salt impregnated in the template, the estimated composition of the composite sample was 33 v% silica, 29 v% Bi nanowires and 38 v% voids.

Evidence of size quantization in the bismuth nanowire composites was obtained from measurements of the Hall effect (Fig. 3) and resistivity (Fig. 4) of the sintered samples. For the nanocomposites, the Hall resistivity is corrected for the volume fractions of Bi by using the effective medium theory²² for the Hall effect; for approx. 30% Bi filling by volume, that correction is a factor of 2.2, does not affect the T-dependence nor the band gap. The inverse of the low-field (<1 Tesla) Hall slope $R_{\rm H}$ was interpreted to give the charge-carrier concentration *via* $n = (eR_{\rm H})^{-1}$ with *e* the electron charge. $R_{\rm H}$ was positive, so the dominant carriers are holes. Fig. 3 shows the temperature dependence of *n* measured on the sample (29 v% Bi–SiO₂), in an Arrhenius plot. A very clean, activated behavior is observed, following a law $n(T) = n_0 e^{-Eg/2k_{\rm B}T}$ valid for intrinsic semiconductors, where $E_{\rm g}$ is the band gap of the semiconductor. A mixture of very narrow nanowires with bulk-like Bi (which displays a metal-like conductivity) will not generate such a simple temperature-dependence. Furthermore, the charge-carrier concentration measured on the nanowire sample is an order of magnitude smaller than that (also shown in Fig. 3) of bulk Bi.²³ Finally, the value of $E_g = 45$ meV obtained from the Arrhenius plot in Fig. 3 corresponds very well to the theoretical value of the band gap opened by size-quantization effects in Bi nanowires of about 20 nm diameter, providing evidence of size quantization effects in the nanowire composites.

At first sight, the 20 nm diameter measured by electrical measurements appears to contradict the observation that the template's pores were 7 nm, which should correspond to a bandgap exceeding 100 meV. However, it has been observed experimentally⁷ that conduction in nanowires of diameter smaller than 9 nm becomes localized. In contrast, no localization is observed in the sample measured here, as is illustrated by the resistivity data in Fig. 4. These data show a temperature dependence consistent with the decrease in carrier concentration, very different from bulk Bi (also shown) and conducive to mobilities (shown using the right-hand ordinate axes) of the order of 500 to 50 $\text{cm}^2 \text{ V}^{-1} \text{ s}^{-1}$. Because there is always a statistical distribution of nanowire diameter, these observations lead to the conclusion that the conduction is dominated by nanowires with diameters near 20 nm with conduction in all the narrower wires simply localized. Indeed, there is a lower limit to the range of validity of the theoretical calculations by Lin *et al.*⁵ set by the condition for Anerson localization:²⁴ the product $k_{\rm F} \cdot l > 1$, where $k_{\rm F}$ is the Fermi wavevector and *l* is the electronic mean-free path, here limited by the wire diameter. For bulk Bi, $k_{\rm F}$ is given in ref. 25 to be very anisotropic with the smallest value 1×10^8 m⁻¹, meaning that nanowires with diameters below 10 nm should not conduct. Because of quantization, the carrier concentration in the present sample is smaller still, leading to a smaller $k_{\rm F}$; therefore, the minimum wire diameter must be larger, e.g. on the order of the observed 20 nm.

In conclusion, a facile nanocasting technique was developed for the synthesis of bismuth nanowire composites. By means of the continuous-feed nanocasting process and a low-temperature reduction treatment of the impregnated bismuth salt, we were able to synthesize bismuth nanowires embedded in the pores of a mesoporous silica powder. The nanocasting method enabled a sufficiently high loading of bismuth inside the pores of a 3D interconnected silica template to form electrically conducting nanowire composite samples. This provides the opportunity to fabricate a bulk material composed of nanowires with potentially enhanced thermoelectric properties. Evidence of size-quantization effects was obtained from electrical resistivity and carrier concentration data, which show the clear signature of semiconducting band conduction with an energy gap of 45 meV and no signs of localization. The results suggest several directions for further research, e.g. aliovalent doping and alloying Bi with Sb, as well as measurements of the thermoelectric properties of the material, in order to enhance the material's zT.

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Conflicts of interest

There are no conflicts to declare.

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