December 16th, 2016

The Heidelberg Laureate Forum

Fabrizio Gagliardi
Claudia Rosas
Paul Carpenter
Barcelona Supercomputing Center

Abstract

The Heidelberg Laureate Forum (HLF) is an annual meeting bringing together the winners of the most prestigious international awards in mathematics (Abel Prize, Fields Medal and Nevanlinna Prize) and computer science (ACM Turing Award) with a select group of highly talented young researchers in computer science and mathematics. PhD students and postdoctoral researchers from all over the world can apply for one of 200 coveted spots to interact with their scientific role models during lectures, discussions and various social events. Over the course of a week, the up and coming scientists will have a unique opportunity to engage in inspiring and motivating conversations with the top researchers in their fields.

Since its first meeting in 2013, five PhD students and researchers from the BSC attended the Heidelberg Laureate Forum. In this seminar, Fabrizio Gagliardi, former ACM Europe Council Chair and Senior Strategy Advisor at BSC will introduce the Heidelberg Laureate Forum, and BSC researchers Paul Carpenter and Claudia Rosas will share their experiences in attending HLF. The seminar aims to encourage BSC students and researchers to apply to HLF 2017 in order to take advantage of this unique opportunity.

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Applications are open for HLF 2017, which will take place from September 24 to 29, 2017. All applications that are submitted by the deadline (14 February 2017) will be meticulously reviewed by an international committee of experts to ensure that only the most qualified candidates are invited. There are 100 spaces available for each discipline of mathematics and computer science. All applicants will be notified by the end of April 2017 whether or not they will be invited.

As described by a young researcher at the fourth HLF, "The best professional experience of my life. I thought that receiving my PhD was the most ultimate event and highlight, but the HLF tops even that."

Short Bios

Fabrizio Gagliardi is the Chair of the ACM Europe Policy Committee (EUACM). He works as Distinguished Research Director at the Polytechnic University of Barcelona and serves as Senior Strategy Advisor at the Barcelona Supercomputing Center (BSC), in Spain. Dr. Gagliardi has been consulting on computing and computing policy matters with the Commission of the European Union, several government and international bodies.

Prior to this and until 2013 he was Europe, Middle East and Africa Director for External Research at Microsoft Research. He joined Microsoft in 2005 after a long career at CERN, the world-leading laboratory for particle physics in Geneva. There he held several technical and senior managerial positions since 1975.

Dr. Gagliardi obtained a doctoral degree in Computer Science from the University of Pisa in Italy in 1974.

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Claudia Rosas got the B.S. degree in computer engineering in 2008 from Universidad Valle del Momboy (Venezuela). She got the M.Sc. in High-Performance Computing in 2009 and the Ph.D. in High-Performance Computing in 2012, both from Universitat Autònoma de Barcelona (Spain). Her research interests include high performance parallel applications, automatic performance analysis and dynamic tuning.

In 2012, she joined as a Post-Doctoral Researcher in the Barcelona Supercomputing Center. Her daily tasks are related to the POP Center of Excellence and the Intel-BSC Exascale Lab, being her main focus the performance analysis of parallel applications for European customers, and the automatic prediction and extrapolation of the expected behavior of the applications at Exascale Computing.

Paul Carpenter graduated in BA Mathematics (1997) and Diploma in Computer Science (1998) from the University of Cambridge. He received his PhD in computer architecture from the Technical University of Catalonia in 2011. He is currently Senior Researcher at BSC, where he is Principal Investigator of the EUROSERVER and ExaNoDe projects. He is co-chair of the ETP4HPC's Working Group onprogramming models, WP3 leader for ExaNoDe, WP2 leader for EuroLab-4-HPC, and co-director of five PhD students.

Prior to starting his PhD, he was Senior Software Engineer at ARM in Cambridge, UK, where he was technical lead for audio/video codec development and part of the small team that designed the ARM Advanced

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SIMD (NEON) vector ISA. He has three worldwide patents. His research interests are server system architecture, energy proportional interconnects, and virtual machine resource sharing.