

IRREGULAR REPETITION CODE HYBRID ARQ IN WIRELESS SYSTEM

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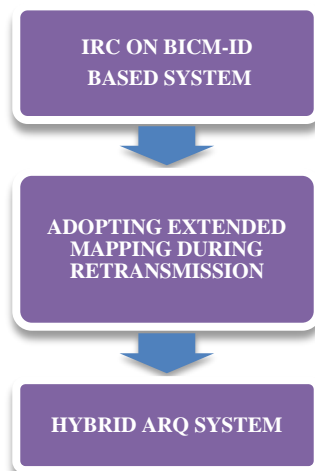
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Graphical abstract



Abstract

Error control consists of error detection and error correction in the communication system. The purpose of this research work is to reduce error in the wireless communication system by using the advantages of both error correction techniques which are forward error correction (FEC) and automatic repeat request (ARQ). Thus, error can be corrected without retransmission and also via retransmission(s) when needed. Combination of FEC and ARQ is known as Hybrid ARQ. In this paper, Hybrid ARQ system is designed using three components which are the irregular repetition code (IRC) as a simple code, bit-interleaved coded modulation with iterative decoding (BICM-ID) as a simple Turbo processing and ARQ. The HARQ system is enhanced by the extended mapping (EM) adopted in the mapping system. The performance of the systems is evaluated in the additive white Gaussian noise (AWGN). The results show the Hybrid ARQ with extended mapping (Hybrid ARQ-EM) outperforms Hybrid ARQ with standard mapping (Hybrid ARQ-SM). Hybrid ARQ-EM achieves low bit error rate BER (10^{-5}) at low signal-to-noise ratio SNR which only 3.03dB close to the theoretical limit. The proposed system Hybrid ARQ-EM achieves 52 percent gain enhancement of SNR gap from the theoretical limit compared to Hybrid ARQ-SM. Hybrid ARQ-EM gives better performance although in worse channel condition.

Keywords: Hybrid ARQ, turbo process, extended mapping, BICM-ID

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1.0 INTRODUCTION

Hybrid ARQ systems have been widely used in various wireless communications systems including 3G, Long Term Evolution (LTE) and/or Worldwide Interoperability for Microwave Access (WIMAX) systems [1]. In Fifth Generation (5G), capability of Hybrid ARQ of improving the transmission system was studied in [2]. Hybrid ARQ compromises of error control coding and ARQ scheme. The combination improves the recovering of the

corrupted information bits during transmission with FEC scheme, while ARQ schemes send the feedback information to the transmitter if error cannot be corrected. It can increase the reliability in the transmission system where the enhancement of retransmission system performance shows the space for improvement.

Several methods have been used to enhance the Hybrid ARQ system performance using various error correcting code [3], then vary the modulation or mapping technique in [4]-[5] and type of equalizer [6].

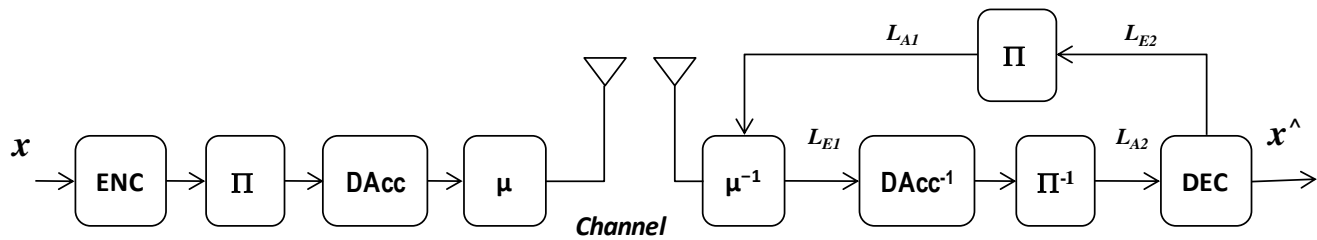


Figure 1 The proposed Hybrid ARQ based IRC with BICM-ID system model

In term of FEC, many researchers mostly used convolutional code as error correcting code in the transmission system [7]-[9]. Convolutional code efficiency in error control mechanism is formerly known for error correction and thus, it has been used in Turbo code development [10]. The efficient of concatenated convolutional code make the transmission system achieves near to the Shannon capacity. It builds a strong codes that have a random long enough to perform as a good error control mechanism [11]-[13].

Instead of the concatenated codes applied in Turbo code to gain the high performance, a simple Turbo process is introduced by Tuchler as stated in [14]. A BICM-ID system reduces the complexity of Turbo processing in Turbo code. BICM-ID based system is widely used by researchers as in [15]-[18]. Various coding can be implemented in BICM-ID based system such as convolutional code, Reed Solomon code and also the simplest code; repetition code. In [19]-[20], they found that the simple code of irregular repetition code (IRC) with simple Turbo processing gives good result to achieve Turbo-like performances.

This paper proposed Hybrid ARQ system considering the combination of BICM-ID with extended mapping. In the BICM-ID, IRC will be applied because it has low complexity coding compared to convolutional code. Then, the system is enhanced with the ARQ scheme if error cannot be corrected by error correcting code. In addition, Doped-Accumulator (DAcc) is implemented to eliminate error floor in the system [21], hence the spectrum efficiency can be improved.

This paper is organised as follows. In Section 2, the general description of the proposed system models which BICM-ID and Hybrid ARQ scheme are presented and some of its characteristics are discussed in order to improve the FEC method. Section 3 presents numerical results and comparisons with simulations using Matlab software where two analyses have been done which are Extrinsic Information Transfer (EXIT) chart and BER analysis. The simulation results investigate the BER performances for several cases in different technique which are BICM-ID with/without retransmission, different modulation and mapping technique and also the effect of doped-accumulator to the system design. Section 4 concludes the paper.

2.0 THE PROPOSED HYBRID ARQ

2.1 IRC with BICM-ID Based System

The hybrid ARQ system proposed in this paper is applied IRC based on BCM-ID system. Iterative decoding process is the heart of BICM-ID system and it plays an important role in turbo process. During the iterative process, some parameters can raise to infinity unless there is a concern in the decoding algorithm. Limitations imposed on these parameters give worse effects on the BER performance especially at high noise environment. Hence, the optimum performance of BICM-ID can be achieved by considering the component codes, the interleaver design and the number of decoding iterations [13].

Figure 1 shows the proposed transceiver system with DAcc optimization for Hybrid ARQ system model. The information bit is encoded by IRC encoder (ENC) for repetition times, d_v which take different values in one block of information. In case of irregular degree allocation, the repetition time, d_{vi} and degree distribution a_i (i.e. a ratio value to the total length of the block) have to be considered. The length of IRC coded block is given as [20],

$$K = \sum_i a_i d_{vi} \times N \quad (1)$$

which is equal to the random interleaver length, where N is the information length and i index represents the number of divided length.

The encoded information is bit interleaved by π and doped accumulated before it mapped the information at the mapper μ . In this paper, extended mapping technique is applied during retransmission process. Then the modulated signal transmitted to the AWGN channel and the discrete time of the received signal $y(q)$ is expressed as

$$y(q) = x(q) + n(q) \quad (2)$$

where q is the symbol timing index, $x(q)$ is the transmitted modulated signal and $n(q)$ is the zero mean complex AWGN component with variance, σ_N^2 . At the receiver part, the received signal goes to demapper μ^{-1} . For initial process, there is no a priori LLR from decoder is assumed. The extrinsic log likelihood ratio (LLR) of demapper L_{E1} is deinterleaved and

forwarded to IRC decoder as *a priori* LLR L_{A2} . For the d_{vi} bits connected to a variable node, the extrinsic LLR is updated by summing up the LLR for the $(d_{vi} - 1)$ bits to produce extrinsic LLR of the j -th IRC coded bit as [20],

$$L_{E2}(j) = \sum_{j=1, j \neq i}^{d_{vi}} L_{A2}(j) \quad (3)$$

The extrinsic LLR of decoder L_{E2} is feed back to the demapper by passing through interleaver as *a priori* LLR L_{A1} . The process is repeated until the iteration is set to discontinue when no more relevant gains in extrinsic LLR can be achieved. After that, the decoder makes the decision based on the *a posteriori* information when no more improvement value of mutual information and the iterative process is then terminated. The decoded signal x^{\wedge} is measured for BER performance.

2.2 Extended Mapping

The implementation of standard mapping technique in huge data transmission system required higher order modulation to utilize the bandwidth more efficiently by increasing the number of bit and labeling pattern. Figure 2(a) depicts the constellation point coordinate structure for 4-Quadrature Amplitude Modulation (QAM) and Figure 2(b) shows that number of labeling pattern is increased for 16-QAM. However, the consequence of increasing the number of constellation point resulting to poor BER performance due to higher noise level.

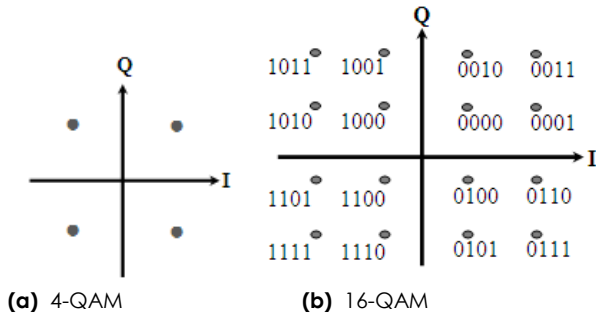


Figure 2 Standard mapping diagram

Extended mapping is introduced to employ higher order modulation with the minimum number of constellation point for the purpose of reducing the error rate in the mapping process [22]-[23]. The technique provides higher possibility to enhance the detection scheme in the system where the possibility of error can be corrected is higher [24]-[25]. In extended mapping, error can be reduced once it maintains the same constellation point with more than a symbol. For extended mapping with 2^M -QAM, there are $l_{map} = 2^M$ bits allocated to one signal point in the constellation.

Figure 3 shows an extended mapping of 4-QAM with $l_{map}=4$ where it is 2-bits extended of 4-QAM with 4-bits

assignment. Therefore, there are 16 patterns; each constellation point has four labeling patterns. The coded bit sequence is bit interleaved, and segmented into l_{map} bit segments, and then each segment is mapped on to one of the 2^M constellation points for modulation. Since $l_{map} > M$ with extended mapping, more than one labels having different bit patterns in the segment are mapped on to each constellation point.

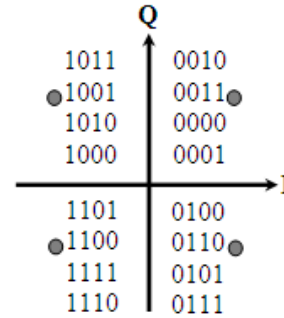


Figure 3 Extended Mapping 4-QAM (2-bits extended)

The spectrum efficiency, R for the IRC code using extended mapping can be expressed as [20];

$$R = \frac{l_{map}}{\sum_i (a_i \cdot d_{vi})} \quad (4)$$

where l_{map} is referred as the index number of mapping technique. It is shown that higher spectrum efficiency can be achieved when extended mapping technique is applied in BICM-ID based system [24]. For the demapping purpose, the demapper calculates the extrinsic LLR of the bit $x(q)$ from the received signal y by

$$L_{E,dem}((y(q)|x_w(q)) = \ln \frac{\sum_{v=1, v \neq w}^m \Pr(y(q)|x_w(q) = +1)}{\sum_{v=1, v \neq w}^m \Pr(y(q)|x_w(q) = -1)} \quad (5)$$

where it can be simplified into

$$L_{E,dem}[y(x_w(q))] = \ln \frac{\sum_{s \in S_0} \exp\left\{-\frac{|y-s|^2}{\sigma_N^2}\right\} \prod_{v=1, v \neq w}^{l_{map}} \exp(-x_v(s) L_{A,dem}(x_v(s)))}{\sum_{s \in S_1} \exp\left\{-\frac{|y-s|^2}{\sigma_N^2}\right\} \prod_{v=1, v \neq w}^{l_{map}} \exp(-x_v(s) L_{A,dem}(x_v(s)))} \quad (6)$$

in which s_0, s_1 indicates the set of labels in mapping symbol having bit $x_w(q)$ being 0 and 1, respectively. $L_{A,dem}(x_v[s])$ is the *a priori* LLR fed back from the decoder corresponding to the v -th bit position in the label allocated to the signal point s .

2.3 Hybrid ARQ

Hybrid ARQ is a system that combines the FEC and ARQ in one transmission system. Hybrid ARQ is categorized in two types which are Hybrid ARQ Type-

It is known as chase combining and Hybrid ARQ Type-II formerly as incremental redundancy method. Many researchers consider integrating BICM-ID to achieve high performance of hybrid ARQ system [26]-[27]. The transceiver with ARQ system design may operate with FEC and then repeats the transmission for error transmitted block received after negative acknowledgment sent in the error control scheme.

In this paper, the proposed Type-I hybrid ARQ system is design concatenated with extended mapping during the retransmission part as shown in Figure 4. Implementation of extended mapping is expected to reduce error in the conventional method of retransmission system. The receiver uses maximum-ratio combining to combine the received bits with the same bits from previous transmissions. Since all transmissions are identical, chase combining can be seen as additional repetition coding.

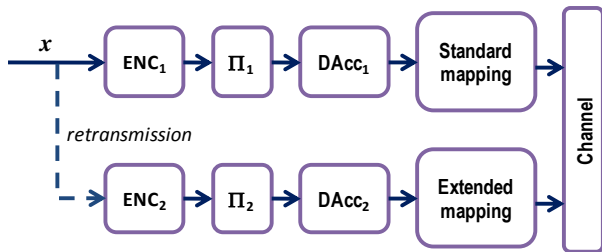


Figure 4 The Proposed HYBRID ARQ extended mapping system

The proposed HYBRID ARQ retransmits the information and uses the mutual information LLR value of both received blocks to update and exchange the mutual information received. The retransmission will be requested and pass to transmitter when negative acknowledgment (NACK) is indicated. In this work, the system stores the transmitted information, so that the new mapping scheme is repeated in the retransmission process. Standard mapping is replaced by the extended mapping scheme while retransmission is requested.

3.0 RESULTS AND DISCUSSION

The analysis of the proposed system is simulated based on Figure 1 with MATLAB software for AWGN channel. We assumed IRC with the node degree distribution $d_{v1} = 8$ ($a_1 = 0.65$) and $d_{v2} = 3$ ($a_1 = 0.35$), and extended mapping with $l_{map}=4$. DAcc with doping ratio, $P=20$ is considered in the performance analysis. The performance is evaluated and compared between standard mapping and extended mapping using EXIT chart and BER analysis. EXIT chart analysis is applied to analyze the matching between decoder and demapper [28]-[30]. The simulation parameters are also summarized in Table 1.

Table 1 Parameter design for Hybrid ARQ system

No.	Parameter / Performance metrics
1	Channel AWGN
2	Mapping Standard: 4-QAM, 16-QAM Extended: EM 4-QAM
3	Frame Length 10,000 bits
4	Doping Ratio $P=20$
5	Coding IRC : Degree distribution: ($a_1=0.65, a_2=0.35$) Repetition time : ($d_{v1}=8, d_{v2}=3$)
6	Interleaver Random Interleaver
7	ARQ Stop-n-Wait

Figure 5 shows the EXIT chart analysis for IRC with demapper curve for standard mapping and extended mapping technique without the DAcc. We can see that, EM 4-QAM intersect at the nearest of (1.0,1.0) point compare to standard mapping which means that better error rate performance is achieved. Although higher iteration is required to achieve the lowest BER, the error floor still cannot be avoided at this point.

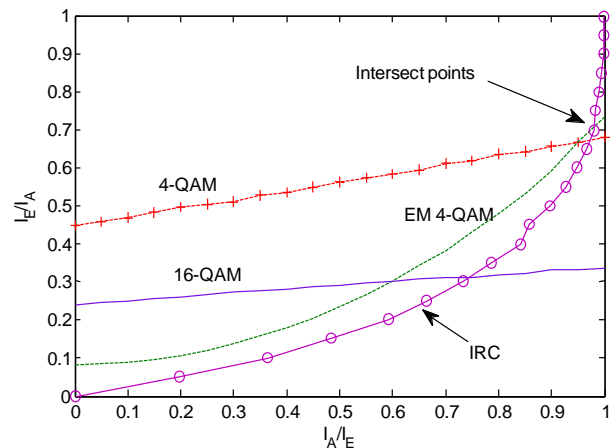


Figure 5 EXIT curves for different mapping technique without DAcc at SNR = 0.5 dB

Figure 6 shows the EXIT chart analysis for IRC with demapper curve for standard mapping and extended mapping technique with the DAcc when $P=20$ to overcome the error floor problem. It is clearly shows that with DAcc, all the ending point is pushed to the mutual information of (1.0,1.0) point. IRC is well matched with EM 4-QAM compared to standard mapping where sharper BER cliff is expected at SNR= 0.5 dB.

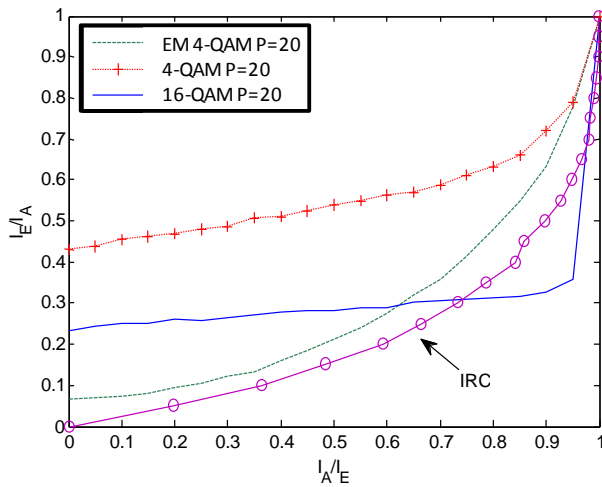


Figure 6 EXIT curves for different mapping technique with DAcc

Figure 7 shows BER performance of Hybrid ARQ-EM and Hybrid ARQ-SM. For the Hybrid ARQ-SM system, 16-QAM mapping is used. Meanwhile, EM4-QAM is used when decoder send NACK to retransmit the information in Hybrid ARQ-EM. Both transmissions using DAcc with doping rate $P=20$. It shows that retransmission using EM 4-QAM improves the BER performance by 3.5dB SNR gain compared to 16-QAM. It is well matched with the EXIT chart in Figure 6 where BER cliff of EM 4-QAM occurred at 0.5dB.

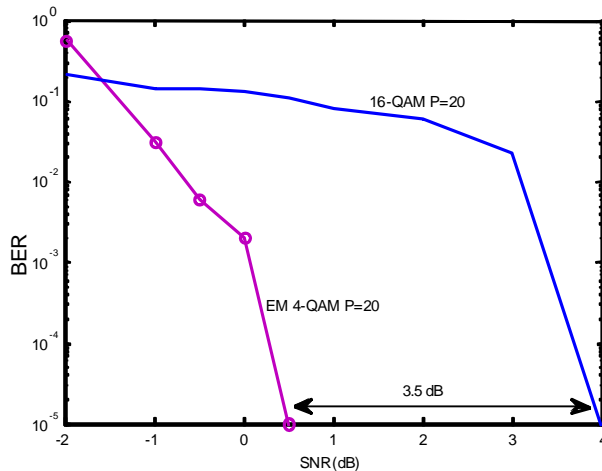


Figure 7 BER of HYBRID ARQs with DAcc

Table 2 tabulates the BER performance for both Hybrid ARQ systems referring to the theoretical limit of Shannon capacity. Theoretical limit in decibel of IRC is at -2.53 dB using equation (4) and followed by equation (7);

$$SNR_{\{lim\}} = 10 \log (2^R - 1) \tag{7}$$

Table 2 BER performances

Hybrid ARQ with DAcc	Turbo Cliff (dB)	Theoretical limit (dB)	SNR Gain (dB)
EM4-QAM	0.5	-2.53	3.03
16-QAM	4.0	-2.53	6.53

For this system, EM4-QAM obtains 3.03 dB away from the limit and the Turbo Cliff occurred at 0.5 dB. System with 16-QAM shows a major SNR gains around 6.53 dB from the limit at BER 10^{-5} . As a result, the use of EM4-QAM during retransmission is significant to achieve system close to the Shannon limit and hence, higher spectrum efficiency can be achieved.

4.0 CONCLUSIONS

In this paper, IRC with BICM-ID based for Hybrid ARQ system has been proposed. It has been shown that the Performance of Hybrid ARQ-EM system outperforms the Hybrid ARQ-SM system. The application of extended mapping technique at the retransmission process improves the Hybrid ARQ system performance. As a result, the proposed technique provides better BER performance where the SNR gain only 3.03 dB from the theoretical limit compared to system that using standard mapping technique. The proposed design describes the benefit of integration using extended mapping in retransmission scheme for Hybrid ARQ. Errors occur in the proposed system can be reduced and removed in higher percentage which achieves 52 % improvement of spectrum efficiency.

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