Sublogarithmic Deterministic Selection on Arrays with a Reconfigurable Optical Bus

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Abstract—The Linear Array with a Reconfigurable Pipelined Bus System (LARPBS) is a newly introduced parallel computational model, where processors are connected by a reconfigurable optical bus. In this paper, we show that the selection problem can be solved on the LARPBS model deterministically in $O((\log \log N)^2 / \log \log \log N)$ time. To our best knowledge, this is the best deterministic selection algorithm on any model with a reconfigurable optical bus.

Index Terms—Analysis of algorithms, massive parallelism, optical bus, parallel algorithms, selection.

1 INTRODUCTION

TIBER optic communication offers the advantage of high Γ bandwidth, low error probability, and gigabit transmission capacity. Therefore, its use in interconnecting multiprocessors of a parallel computer has been proposed by many researchers [4], [15], [16], [31]. Among them, the distributed-memory SIMD (Single Instruction Multiple Data) computer with pipelined optical buses has received much attention [6], [10], [16], [17], [18], [27], [28] due to its simplicity and low cost. On such systems, messages are transmitted concurrently on the optical bus which connects all processors. One cycle of such a parallel system is merely the delay of the light between the furthest processors over a waveguided bus. This design integrates the advantages of both optical transmission and electronic computation. Several slightly different versions of such a system have been proposed in the literature, including the array processors with pipelined buses (APPB) [7], [16], the array processors with pipelined buses using switches (APPBS) [8], the array with synchronous optical switches (ASOS) [28], the reconfigurable array with spanning optical buses (ROSOB) [29].

Based on the research in reconfigurable meshes and pipelined optical buses, the model of arrays with reconfigurable optical buses has been proposed independently by Pavel and Akl [23], Pavel [24], and by Pan and Hamdi [19], and Pan and Li [20]. The model proposed by Pavel and Akl [23] and Pavel [24] is the *arrays with reconfigurable optical buses* (AROB), and the model proposed by Pan and Hamdi [19] and Pan and Li [20] is the *linear arrays with a reconfigurable pipelined bus system* (LARPBS). Many algorithms have been designed on these

 H. Shen is with the Graduate School of Information Science, Japan Advanced Institute of Science and Technology, Tatsunokuchi, Ishikawa 923-1292, Japan. E-mail: shen@jaist.ac.jp. models, including inversion number computation [9], neural network computation [3], various matrix operations [11], [12], [25], selection [18], [30], sorting [19], [20], [26], [30]. The two models differ in some aspects. For example, counting is not allowed during a bus cycle on the LARPBS model, while it is permitted on the AROB model.

In this paper, we study the selection problem on the LARPBS model. The selection problem, when given *s*, is to select the sth smallest data item among the N ordered (but not sorted) data items. A straightforward way of solving the selection problem is to sort the input data items and then pick the sth smallest item. This approach, however, is usually not efficient due the relatively high cost of performing the sorting. Currently, sorting requires $O(\log N)$ expected time or $O(\log^2 N)$ deterministic time for sorting N data items on N processor LARPBS [19], [21]. Solving the selection problem without sorting first has been considered by researchers. Pan has studied the selection problem on the LARPBS model and gave a selection algorithm which runs in $O(N \log N/p)$ expected time using p processors [18]. Li et al. described a deterministic algorithm which selects in $O(\log N)$ deterministic time for N data items on N-processor LARPBS. Selection problem has also been considered on other related model. For example, Rajasekaran and Sahni solved the selection problem on the 2D AROB model. Their randomized algorithm runs in O(1) time with high probability [30].

We note that the selection problem on the PRAM model has been studied by Cole [2] and Chaudhuri et al. [1]. The original idea is from Cole's paper [2]. Chaudhuri et al. improved Cole's algorithm on the CRCW model by using approximate counting. Currently, the PRAM algorithms on the EREW model [2] runs in $O(\log N \log^* N)$ time with optimal processor speedup. On the CRCW model, the algorithm by Chaudhuri et al. [1] runs in $O(\log N / \log \log N)$ time with optimal processor speedup. Selection algorithms have also been studied on hypercubes [32].

In this paper, we exploit the features of the LARPBS model. We also make use of the ideas in Cole's paper [2]. By using fast sorting algorithm to sort a smaller set of data on the LARPBS model, we are able to exhibit a selection

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Fig. 1. A linear optical bus system of *N* processors.

algorithm for the LARPBS model which runs in $O((\log \log N)^2 / \log \log \log N)$ time. Note that our algorithm is not simply a simulation of Cole's PRAM algorithm. Novel features and efficient basic data movement operations on the LARPBS model are exploited to speedup the algorithm, thus achieving a better time complexity than those on the PRAM. Many research groups are working in this area [9], [3], [11], [12], [25], [30]. To our best knowledge, this is the first deterministic selection algorithm with sublogarithmic time complexity on the LARPBS model.

2 THE LARPBS MODEL

The LARPBS model uses an optical bus to connect its processors. A pipelined optical bus system uses optical waveguides instead of electrical buses to transfer messages among electronic processors. The advantages of using waveguides can be seen as follows: Besides the high propagation speed of light, there are two important properties of optical signal (pulse) transmission on an optical bus: unidirectional propagation and predictable propagation delay per unit length. These two properties enable synchronized concurrent access of an optical bus in a pipelined fashion [6], [16], [27], [28]. This, combined with the abilities of a bus structure to do efficient broadcasting or multicasting, makes the architecture suitable for many applications that involve intensive communication operations.

Fig. 1 shows a linear array in which electronic processors are connected with an optical bus. Each processor is connected to the bus with two-directional couplers, one for transmitting on the upper segment and the other for receiving from the lower segment of the bus [6], [16], [27], [28]. Messages are organized as fixed-length *messageframes*. Note that optical signals propagate unidirectionally from left to right on the upper segment and from right to left on the lower segment. This bus system is also referred to as the folded-bus connection in [6].

A linear array with a reconfigurable pipelined bus system (LARPBS) consists of N processors P_1, P_2, \ldots, P_N connected by an optical bus. In addition to the tremendous communication capabilities, a LARPBS can also be partitioned into $k \ge 2$ independent subarrays

 $LARPBS_1, LARPBS_2, \ldots, LARPBS_k,$

such that $LARPBS_j$ contains processors

$$P_{i_{j-1}+1}, P_{i_{j-1}+2}, \dots, P_{i_j}$$

where $0 = i_0 < i_1 < i_2 \cdots < i_k = N$. The subarrays can operate as regular linear arrays with pipelined optical bus systems and all subarrays can be used independently for different computations without interference [19], [21]. Fig. 2 shows the LARPBS model with six processors. The array is split into two subarrays, with the first one having four processors and the second one having two processors. As in many other synchronous parallel computing systems, a LARPBS computation is a sequence of alternate global communication and local computation steps. The time complexity of an algorithm is measured in terms of the total number of bus cycles in all the communication steps, as long as the time of the local computation steps between successive communication steps is bounded by a constant and independent of the problem size. This complexity measure implies that a bus cycle takes constant time and this assumption has been adopted widely in the literature [3], [6], [5], [9], [11], [12], [13], [23], [24], [25], [26], [27], [28], [29], [30], [33]. (Remark: To avoid controversy, let us emphasize that in this paper, by "O(f(p)) time," we mean O(f(p)) bus cycles for global communication plus O(f(p))number of local arithmetic/logic operations.)

3 BASIC OPERATIONS

For ease of algorithm development and specification, a number of basic communication, data movement, and global operations on the LARPBS model implemented



using the coincident pulse processor addressing technique have been developed ([11], [19], [20], [21]). Each of these primitive operations can be performed in a constant number of bus cycles. These powerful primitives that support massive parallel communications, plus the reconfigurability of the LARPBS model, make the LARPBS very attractive in solving problems. Optical buses are not only communication channels among the processors, but also active components and agents of certain computations, e.g., binary prefix sum. The following primitive operations on LARPBS are used in this paper and our selection algorithm is developed using these operations as building blocks.

3.1 One-to-One Communication

In this operation, each processor sends one data item to another processor. This operation can be done easily in one bus cycle (see detailed discussion in [19], [21]).

3.2 Broadcast

In a broadcast operation, we have a source processor who sends a value in its local register to all the *N* processors. The details of this operation are described in [19], [21] and can be accomplished in one bus cycle.

3.3 Multicast

Multicast is a one-to-many communication operation. Each processor may send a message to a group of processors in the system. Each processor receives only one message from a source processor during a bus cycle. This is a special case of the *h*-relation, where h = O(1), defined in [33], can be done in O(1) bus cycles.

3.4 Compression

Assume an array of N data elements with each processor having one data element. Also, assume that the number of active data elements in the array is s. Active elements are labeled based upon certain values of their local variables. A processor with an active element is referred to as an active processor. The compression algorithm moves these active data elements to processors N - s - 1, N - s, ..., N - 1. In other words, the compression algorithm moves all active data items to the right side of the array. This operation can be done in O(1) bus cycle on a LARPBS [20], [21].

3.5 Binary Prefix Sum

Consider a LARPBS with *N* processors and *N* binary values v_i , $0 \le i \le N - 1$. The binary prefix sum requires the computation of

$$psum_i = v_0 + v_1 + \ldots + v_{i-1},$$

for all $0 \le i \le N - 1$. It is shown that the binary prefix sum can be done in O(1) bus cycle on a LARPBS [20], [21].

4 FAST SELECTION ON THE LARPBS MODEL

We apply the ideas in [2] to speed up our algorithm. In order to do so, we need a fast sorting algorithm. We first show that N data items can be sorted in constant time with N^2 processors on the LARPBS model and that M data items can be sorted in $O(\log \log N)$ time with Mk processors on

the LARPBS model, where $M = k\sqrt{\log \log N}$. These sorting algorithms will be used to speed up the selection process.

To sort *N* data items $a_0, a_1, \ldots, a_{N-1}$ with N^2 processors, we first broadcast a_i to processors iN through (i+1)N-1. Then, processor iN+j compare a_i with a_j at processor jN+i. If $a_j < a_i$ or $a_j = a_i$ and j < i processor, iN + j will record a 1 otherwise it will record 0. Then, we compute the sum S_i of binary bits at processors iN through (i+1)N-1. S_i is the rank of a_i . After computing S_i , a_i is moved to the S_i th processor. This sorts the input data items and the time consumed is a constant. See more details of the algorithm in [22].

We use f(M) to denote the time needed to sort M data items with Mk processors. To sort $M = k \sqrt{\log \log N}$ data items with Mk processors, we first divide M data items into \sqrt{k} sets with each set containing M/\sqrt{k} data items. We sort each set in parallel and by recursion. This recursion takes $f(M/\sqrt{k})$ time. After we return from recursion, we have \sqrt{k} sorted sets $S_0, S_1, \ldots, S_{\sqrt{k}-1}$, each containing M/\sqrt{k} data items. Now, we show how to merge them into one sorted set. We use g(a, b) to denote the time needed for merging b sorted sets each containing a data items with k processors allowed for each data item. Thus, our merging problem takes $g(M/\sqrt{k},\sqrt{k})$ time. For each set, we pick every M/kth data item from the sorted data items. Thus, \sqrt{k} data items are picked from each set. The total number of data items picked is k. We sort picked data items in constant time using the algorithm outlined in the previous paragraph. Let the k data items in sorted order be $b_0, b_1, \ldots, b_{k-1}$. These *k* picked data items, after being sorted, defines k + 1 intervals. The *j*th interval is $> b_{j-1}$ but $\le b_j$ (b_{-1} is $-\infty$ and b_k is ∞). Each input data item is in one interval. Because the way these k data items are picked, there are no more than M/k data items from any one of S_i , $0 \le i < \sqrt{k}$, falls within any interval. Each data item uses k processors to compare itself with the k picked data items to determine into which interval it falls. Thus, each set S_{i} , $0 \le i < \sqrt{k}$, is divided into k + 1 segments with each segment containing no more than M/k data items. We first compress the data items from S_i , $0 \le i < \sqrt{k}$, in the 0th interval, in constant time, using the compressing algorithm in the previous section. We then move data item a in processor $i, 0 \le i < M$, to processor M(j-1) + i if a falls into the *j*th interval for j > 0. We compress these data items. We then arrange data items so that data items that fall into the 0th interval are before data items in other intervals. What we have done so far is to have data items in the j + 1th interval follow data items in the *j*th interval. The data items in the *j*th interval are coming from S_i , $0 \le i < M/\sqrt{k}$. Therefore, what we need to do next is to merge the M/\sqrt{k} sets, $T_0, T_1, \ldots, T_{\sqrt{k}-1}$, of sorted data items in each interval into one set. As we have explained before, each T_i , $0 \le i < \sqrt{k}$, has no more than M/k data items. Thus, the merging which needs to be done takes $g(M/k, \sqrt{k})$ time.

From the above paragraph, we arrive at two equations:

$$f(M) = f(M/\sqrt{k}) + g(M/\sqrt{k}, \sqrt{k}),$$

$$g(M/\sqrt{k}, \sqrt{k}) = g(M/k, \sqrt{k}) + c_1,$$

where c_1 is a constant. The first equation says that to sort M data items we need to only sort M/\sqrt{k} data items (in parallel) plus merge \sqrt{k} sorted sets with each set containing no more than M/\sqrt{k} data items. The second equation says that we can expend constant time to reduce the problem of merging \sqrt{k} sorted sets with each set containing no more than M/\sqrt{k} data items to the problem of merging \sqrt{k} sorted sets with each set containing no more than M/\sqrt{k} data items to the problem of merging \sqrt{k} sorted sets with each set containing no more than M/\sqrt{k} data items to the problem of merging \sqrt{k} sorted sets with each set containing no more than M/\sqrt{k} data items. We can also add that $f(k) = c_3$ and $g(\sqrt{k}, \sqrt{k}) = c_2$ because k data items can be sorted in constant time with k^2 processors using the sorting algorithm in previous paragraph.

Solving $g(M/\sqrt{k}, \sqrt{k}) = g(M/k, \sqrt{k}) + c_1$, we obtain that $g(M, \sqrt{k}) = c_3 \log M / \log k$, where c_3 is a constant.

We now have that

$$f(M) = f(M/\sqrt{k}) + c_3 \log M/\log k$$
$$= \frac{c_3}{\log k} \log \left(M \cdot \frac{M}{\sqrt{k}} \cdot \frac{M}{k} \cdot \cdots \right)$$

Because $M = k^{\sqrt{\log \log N}}$, we have that

$$f(M) = \frac{c_3}{\log k} \log \frac{M^{2\sqrt{\log \log N}+1}}{k^{\sqrt{\log \log N} \left(2\sqrt{\log \log N}+1\right)/2}}$$
$$= \frac{c_3}{\log k} \log k^{\sqrt{\log \log N} \left(2\sqrt{\log \log N}+1\right)/2}$$
$$= O(\log \log N).$$

Using the ideas in [2], we now show how to speed up the selection process. Suppose that we have L data items and N = Lk processors and we are going to select the *s*th item. We show how to find a data item p such that the rank of p is between $s - L/(2k\sqrt{\log \log N/4})$ and s. The same method also allows us to find another data item q such that the rank of q is between s and $s + L/(2k\sqrt{\log \log N/4})$. After we find p and q, we can then eliminate $L - L/k\sqrt{\log \log N/4}$ data items. Therefore, we reduce the selection problem of selecting from L data items to the problem of selecting from $L/k\sqrt{\log \log N/4}$ data items. As will be seen, the whole reduction takes $O(\log \log N)$ time.

We use *S* to denote the set of *L* data items. We have *Lk* processors and we are going to select the *s*th data item. We use two stages. In the first stage, we first divide *L* data items in *S* into $L/k\sqrt{\log \log N}$ sets with each set containing $k\sqrt{\log \log N}$ data items. We sort each set. Because *k* processors can be allocated to each data item, the sorting can be done in $O(\log \log N)$ time by our sorting algorithm given in previous paragraphs. We pick every $k\sqrt{\log \log N/2}$ th item from each sorted set and form set *S*₁. The item ranked *s*₁th in set *S*₁ is ranked at least $s_1k\sqrt{\log \log N/2}$ th and at most

$$\left(s_1 k^{\sqrt{\log \log N}/2} + L/k^{\sqrt{\log \log N}/2}\right)$$
th

in S. Let

$$s_1 = \left\lfloor s/k^{\sqrt{\log\log N}/2} - L/k^{\sqrt{\log\log N}} \right\rfloor$$

We now try to find the s_1 th items in S_1 . Note that S_1 has only $L/k^{\sqrt{\log \log N}/2}$ data items.

In the second stage, we execute a loop until we find p. Suppose we have a set S_i of L_i data items and L_ik_i processors at the beginning of the *i*th iteration of the loop and we are looking for the s_i th item. If $k_i \ge L_i$, we use our constant time sorting algorithm to sort the L_i data items and then return the s_i th data item. Otherwise, we divide L_i data items into L_i/k_i sets with each set containing k_i items. We then sort each set in constant time by using our sorting algorithm. After sorting, we pick every $\sqrt{k_i}$ th item from each set and form set S_{i+1} . The s_{i+1} th item in S_{i+1} is ranked at least $s_{i+1}\sqrt{k_i}$ and at most $s_{i+1}\sqrt{k_i} + L_i/\sqrt{k_i}$ in S_i . Let $s_{i+1} = \lfloor s_i/\sqrt{k_i} - L_i/k_i \rfloor$. And, we have finished the *i*th iteration of the loop.

Suppose that the loop in the second stage executed j iterations and p is the final item returned from the *j*th iteration which ranked s_j in S_j . By the way, we fix s_i , $2 \le i \le j$, the rank of p in S_1 is at most s_1 . Also, by the way, we fix s_i , $2 \le i \le j$, p is ranked at least $s_{j-1} - L_{j-1}/\sqrt{k_{j-1}}$ in S_{j-1} , at least

$$B_{j-2} - L_{j-2}/\sqrt{k_{j-2}} - L_{j-1}/\sqrt{k_{j-1}/k_{j-2}}$$

in S_{j-2}, \ldots , at least

$$s_1 - L_1 / \sqrt{k_1} - L_2 / \sqrt{k_2 / k_1} - \dots$$
$$- L_{j-1} / \sqrt{k_{j-1} / (k_1 k_2 \cdots k_{j-2})} = s_1 - \sum_{i=1}^j L_i / \sqrt{k_i / \prod_{m=1}^{i-1} k_m}$$

in S_1 . Because $L_{i+1} = L_i / \sqrt{k_i}$, we have

$$\kappa_{i+1} = N/L_{i+1} = N\sqrt{k_i}/L_i = k_i^{3/2}$$

Thus, *p* is ranked at least $s_1 - 2L_1/\sqrt{k_1}$ in S_1 . Also, by the way, we fix s_1 , *p* is ranked at most *s* in *S* and at least

$$\begin{aligned} &\left(s_1 - 2L_1/\sqrt{k_1}\right) k^{\sqrt{\log\log N/2}} \\ &\geq \left(s/k^{\sqrt{\log\log N/2}} - L/k^{\sqrt{\log\log N/2}} - 2L_1/\sqrt{k_1}\right) k^{\sqrt{\log\log N/2}} \\ &= s - L/k^{\sqrt{\log\log N/2}} - 2L_1k^{\sqrt{\log\log N/2}}/\sqrt{k_1} \end{aligned}$$

in S. Because $L_1 = L/k^{\sqrt{\log \log N/2}}$, we have

$$k_1 = k^{1 + \sqrt{\log \log N}/2}.$$

Thus p is ranked at least $s - L/(2k\sqrt{\log \log N/4})$.

After we find q (which is symmetrical to p) which is ranked at least s and at most $s + L/(2k\sqrt{\log \log N/4})$, we can use p and q to eliminate most of the data items in S so that there will be at most $L/k\sqrt{\log \log N/4}$ data items left from which we form set T. Suppose the rank of p in S is s'. By setting a 1 for data items less than p and setting a 0 for other data items, and by using summation on binary bits in Thus, we are able to reduce a selection problem of size L with Lk processors to the selection problem of size $L/k^{\sqrt{\log \log N}/4}$ in $O(\log \log N)$ time. We call such a reduction a phase of our algorithm. Initially, the processor advantage is a constant c, i.e., we could use one processor to simulate c processors. After i phases of reduction, the number of data items left will be no more than $N/c^{\left(\sqrt{\log \log N}/4\right)^i}$. Thus, the total number of phases needed to reduce the number of data items to constant is $O(\log \log N/\log \log \log N)$. Because we use $O(\log \log N)$ time for each phase, the time complexity of our algorithm is $O((\log \log N)^2/\log \log \log N)$.

Theorem 1. The selection problem for N data items can be solved on an N processor LARPBS in $O((\log \log N)^2 / \log \log \log N)$ time.

5 CONCLUSIONS

We have demonstrated a fast deterministic selection algorithm on the LARPBS model. Because certain operations such as compression and sorting a smaller set of data can be done in constant time on the LARPBS model, we are able to take advantage of them and to make our selection algorithm faster than any existing PRAM selection algorithm in the literature. We believe many other algorithms can also take advantage of the high communication bandwidth on the LARPBS model. Our selection algorithm may also implicitly improve the results for many other algorithms on the LARPBS and related models. We expect to see more results in this area published in the future.

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REFERENCES

- [1] S. Chaudhuri, T. Hagerup, and R. Raman, *Computer Science*, Springer-Verlag, pp. 352-361, 1993.
- R.J. Cole, "An Optimally Efficient Selection Algorithm," *Information Processing Letters 26*, pp. 295-299, 1987/1988.
 B. Cong, "Mapping of ANNs on Linear Array with a Reconfigur-
- [3] B. Cong, "Mapping of ANNs on Linear Array with a Reconfigurable Pipelined Bus System," Proc. 1997 Int'l Conf. Parallel and Distributed Processing Techniques and Applications, pp. 522-529, 1997.
- [4] P.W. Dowd, "Wavelength Division Multiple Access Channel Hypercube Processor Interconnection," *IEEE Trans. Computers*, vol. 41, no. 10, pp. 1223-1241, Oct. 1992.
- [5] M. Hamdi, C. Qiao, Y. Pan, and J. Tong, "Communication-Efficient Sorting Algorithms on Reconfigurable Array of Processors with Slotted Optical Buses," J. Parallel and Distributed Computing, vol. 57, no. 2, pp. 166-187, May 1999.

- [6] Z. Guo, R. Melhem, R. Hall, D. Chiarulli, and S. Levitan, "Pipelined Communication in Optically Interconnected Arrays," *J. Parallel and Distributed Computing*, vol. 12, no. 3, pp. 269-282, 1991.
- [7] Z. Guo, "Sorting on Array Processors with Pipelined Buses," Proc. 1992 Int'l Conf. Parallel Processing, pp. 289-292, 1992.
- [8] Z. Guo, "Optically Interconnected Processor Arrays with Switching Capacity," J. Parallel and Distributed Computing, vol. 23, pp. 314-329, 1994.
- [9] H. Kimm, "Inversion Number Algorithm on a Linear Array with Reconfigurable Pipelined Bus System," Proc. 1996 Int'l Conf. Parallel and Distributed Processing Techniques and Applications, pp. 1398-1408, 1996.
- [10] S. Levitan, D. Chiarulli, and R. Melhem, "Coincident Pulse Techniques for Multiprocessor Interconnection Structures," *Applied Optics*, vol. 29, no. 14, pp. 2024-2039, 1990.
- *plied Optics*, vol. 29, no. 14, pp. 2024-2039, 1990.
 [11] K. Li, Y. Pan, and S.-Q. Zheng, "Fast and Processor Efficient Parallel Matrix Multiplication Algorithms on a Linear Array with Reconfigurable Pipelined Bus System," *IEEE Trans. Parallel and Distributed Systems*, vol. 9, no. 8, pp. 705-720, Aug. 1998.
- [12] K. Li, Y. Pan, and S.-Q. Zheng, "Parallel Matrix Computations Using a Reconfigurable Pipelined Optical Bus," J. Parallel and Distributed Computing, vol. 59, no. 1, pp. 13-30, Oct. 1999.
- [13] K. Li, Y. Pan, and S.-Q. Zheng, "Efficient Deterministic and Probabilistic Simulations of PRAMs on Linear Arrays with Reconfigurable Pipelined Bus Systems," *The J. Supercomputing*, vol. 15, no. 2, pp. 163-181, Feb. 2000.
- [14] Y. Li, Y. Pan, and S.-Q. Zheng, "Pipelined Time-Division Multiplexing Optical Bus with Conditional Delays," *Optical Eng.*, vol. 36, no. 9, pp. 2417-2424, Sept. 1997.
- [15] A. Louri, "Three-Dimensional Optical Architecture and Data-Parallel Algorithms for Massively Parallel Computing," IEEE Micro, vol. 11, no. 2, Apr. 1991.
- [16] R. Melhem, D. Chiarulli, and S. Levitan, "Space Multiplexing of Waveguides in Optically Interconnected Multiprocessor Systems," *The Computer J.*, vol. 32, no. 4, pp. 362-369, 1989.
- [17] Y. Pan, "Hough Transform on Arrays with an Optical Bus," Proc. Fifth ISMM Int'l Conf. Parallel and Distributed Computing and Systems, pp. 161-166, 1992.
- [18] Y. Pan, "Order Statistics on Optically Interconnected Multiprocessor Systems," Proc. First Int'l Workshop Massively Parallel Processing Using Optical Interconnections, pp. 162-169, 1994.
- [19] Y. Pan and M. Hamdi, "Quicksort on a Linear Array with a Reconfigurable Pipelined Bus System," Proc. IEEE Int'l Symp. Parallel Architectures, Algorithms, and Networks, pp. 313-319, 1996.
- [20] Y. Pan and K. Li, "Linear Array with a Reconfigurable Pipelined Bus System—Concepts and Applications," *Information Sciences*, vol. 106, no. 3/4, pp. 237-258, May 1998.
- [21] Y. Pan, "Basic Data Movement Operations on the LARPBS Model," *Parallel Computing Using Optical Interconnections*, K. Li, Y. Pan, and S.Q. Zheng, eds., Boston: Kluwer Academic Publishers, 1998.
- [22] Y. Pan, K. Li, and S.-Q. Zheng, "Fast Nearest Neighbor Algorithms on a Linear Array with a Reconfigurable Pipelined Bus System," *Parallel Algorithms and Applications*, vol. 13, pp. 1-25, 1998.
- [23] S. Pavel and S.G. Akl, "On the Power of Arrays with Optical Pipelined Buses," *Proc.* 1996 Int'I Conf. Parallel and Distributed Processing Techniques and Applications, pp. 1443-1454, 1996.
 [24] S. Pavel, "Computation and Communication Aspects of Arrays
- 24] S. Pavel, "Computation and Communication Aspects of Arrays with Optical Pipelined Buses," PhD Dissertation, Dept. of Computing and Information Science, Queen's Univ., Canada, Oct. 1996.
- [25] S. Pavel and S.G. Akl, "Matrix Operations Using Arrays with Reconfigurable Optical Buses, *Parallel Algorithms and Applications*, vol. 11, pp. 223-242, 1996.
- [26] S. Pavel and S.G. Akl, "Integer Sorting and Routing in Arrays with Reconfigurable Optical Bus," Proc. 1996 Int'l Conf. Parallel Processing, vol. III, pp. 90-94, Aug. 1996.
- [27] C. Qiao, R. Melhem, D. Chiarulli, and S. Levitan, "Optical Multicasting in Linear Arrays," *Int'l J. Optical Computing*, vol. 2, no. 1, pp. 31-48, 1991.
- [28] C. Qiao and R. Melhem, "Time-Division Optical Communications in Multiprocessor Arrays," *IEEE Trans. Computers*, vol. 42, no. 5, pp. 577-590, May 1993.
- [29] C. Qiao, "Efficient Matrix Operations in a Reconfigurable Array with Spanning Optical Buses," Proc. Fifth IEEE Symp. Frontiers of Massively Parallel Computations, pp. 273-280, 1995.

- [30] S. Rajasekaran and S. Sahni, "Sorting, Selection and Routing on the Arrays with Reconfigurable Optical Buses," *IEEE Trans. Parallel* and Distributed Systems, vol. 8, no. 11, pp. 1123-1132 Nov. 1997.
- [31] S. Sahni, "Models and Algorithms for Optical and Optoelectronic Parallel Computers," *Proc. Fourth IEEE Int'l Symp. Parallel Architectures, Algorithms, and Networks,* pp. 2-7, 1999.
- [32] H. Shen, "Improved Universal k-Selection in Hypercubes," *Parallel Computing*, vol. 18, no. 2, pp. 177-184, 1992.
 [33] J.L. Trahan, A.G. Bourgeois, Y. Pan, and R. Vaidyanathan, "An
- [33] J.L. Trahan, A.G. Bourgeois, Y. Pan, and R. Vaidyanathan, "An Optimal and Scalable Algorithm for Permutation Routing on Reconfigurable Linear Arrays with Optically Pipelined Buses," J. Parallel and Distributed Computing, vol. 60, no. 9, pp. 1125-1136, Sept. 2000.



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