# Analysis of a Half-Controlled Single-Phase Bridge Circuit with Free-Wheeling Diode (II)

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This paper is presented on the operation of the phase controlled single-phase bridge circuit with a free-wheeling diode, which has capacitance in parallel with the inductive load. Such circuit configuration is frequently met with, for instance, at the time when the more smoothness of d-c voltage is required, the higher reliability of SCR's firing is taken into account or load windings include some capacitance which is not negligible. The capacitance influences not only largely upon SCR's rush current but also significantly upon control characteristics of the circuit such as wave forms, pulsating ratio and power factor etc. Here, the various effects of the capacitance on the circuit performance are discussed.

## § 1. Introduction

Because of the features such as small size, small forward voltage drop, short turn-off time and large amplification, silicon controlled rectifiers (SCRs) have been used in many application fields, particularly in rectifier fields. Essentially by varying the firing angles of the SCRs, it is possible to control load voltage smoothly over the range between maximum available and zero. The operation, however, is much different from that of the conventional silicon rectifier diodes, which is larger pulsating ratio and reduced power factor etc.

A number of circuit configurations may be used for rectifier connections, among which bridge connections are particularly familiar1 and have two different ways, that is; all rectifying devices are phase controlled (all SCRs bridge connection) and half the rectifying devices are made rectifier diodes, the others remaining SCRs (half-controlled bridge circuit). All SCRs bridge connection system is very useful when it is used as electric car rectifiers because of its possibility of regenerative braking. When the regenerative characteristic, however, is not necessary, it is usual for a free-wheeling diode to be connected in parallel with the load, which results in a small ripple voltage and reduced reactive power. A half-controlled bridge circuit is frequently used because this circuit operates as the same as that of the all SCRs bridge connection with a free-wheeling diode. What's more, it is extremely economical, since halves

of SCRs are replaced with rectifier diodes. On the half-controlled bridge circuit, however, when the phase retard becomes larger than 90 degrees, so-called "half-waving" effect can take place2, which results in losing control characteristic (this does not occur in the single-phase doubler connection). In order to avoid this phenomenon, it is a conventional procedure to connect a free-wheeling diode in parallel with the load. Since this procedure leads the operation of a c side equal to having resistive load, all SCRs bridge connections and half-controlled bridge connections are regarded to operate under the same manner. Upon the above reason, analysis of the circuit operation of a halfcontrolled single-phase bridge circuit with a free-wheeling diode, which has the inductive load, has been the subject of the previous paper<sup>3</sup> in which the source impedance is taken negligible or only resistive.

It is very useful to take the source impedance pure resistive but in practice the transformer leakage reactance or stray reactance of the lead wire can sometimes become large to some degree which is not negligible. Further more, it frequently happens that the capacitance exists in parallel with the inductive load such as a smoothing condenser, a compensating condenser for the SCR's firing or the stray capacitance of the inductive load. In such case, the circuit operation becomes much different from that of the inductive load only. Resonance circuits are composed of between the capacitance and the source impedance or the load circuit. Conse-

quently, control characteristics may come sometimes to lose the smoo hness and also the wave form can come to get out of shape. Analysis of such circuit configuration which in future will be used more and more, has been presented in some references<sup>4</sup> but restricted to only "one sequence" of the operation modes (see Table 1), and not referred to the others. References on the SCR's circuit configuration have not yet been seen.

In this paper, the steady-state operation of a half-controlled single-phase bridge circuit with a free-wheeling diode which has capacitance in parallel with the inductive load, has been treated analytically. Here, the following characteristics have been discussed; control characteristics as functions of firing angle, pulsating ratio of d-c voltage and current, power factor, L-R-C resonance and the behavior of the counter emfload.

## § 2. Circuit Analysis

The circuit configuration, shown in Fig. 1, is the common cathode. Capacitance is connected

in parallel with an inductive load. In order to simplify the analysis, it is assumed that

- 1) the forward voltage drops of SCRs and rectifier diodes are negligible,
- 2) the leakage currents of SCRs and rectifier ciodes are negligible,
- 3) the turn-on and turn-off time of SCRs is negligible,
- 4) the commutating phenomena can take place only within the free-wheeling clode  $D_f$  and load branch, and
- 5) the wave form of the supply voltage is sinusoidal.

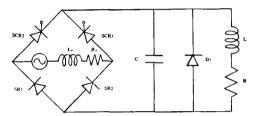
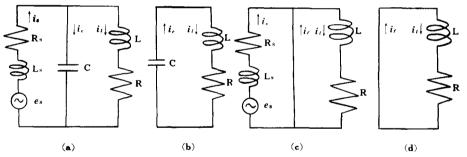


Fig. 1. Phase controlled single-phase bridge circuit with free-wheeling diode, which has capacitance in parallel with the inductive load.



Flg. 2. Equivalent circuits according to the circuit operation.

The circuit operation of the a-c side can be divided into four operation modes, whose equivalent circuits are shown in Fig. 2 (a), (b), (c) and (d). The mode (a) corresponds to the so-called "rectifying period" when the energy flow is from the a-c side to the d-c side. In the mode (b), the a-c current is blocked but the load voltage is not zero. The mode (c) is the so-called "commutating period" and occurs just after one SCR has turned to conducting. The mode (d) expresses the period when the fracture expressions corresponding with the operation modes are, in the mode (a)

$$E_m \sin(\omega t + \theta) = R_s i_s + L_s \frac{di_s}{dt} + \frac{1}{C} \int i_c dt \quad (1)$$

$$-\frac{1}{C}\int i_c dt + Ri_L + L\frac{di_L}{dt} = 0$$
 (2)

$$i_C + i_L = 0 \tag{3}$$

in the mode (b)

$$-\frac{1}{C}\int i_c dt + L\frac{di_L}{dt} + Ri_L = 0$$
 (4)

$$i_C = -i_L \tag{5}$$

in the mode (c)

$$E_m \sin(\omega t + \theta) = R_s i_s + L_s \frac{di_s}{dt}$$
 (6)

$$Ri_L + L \frac{di_L}{dt} = 0 (7)$$

$$i_s + i_f = i_L \tag{8}$$

	Table 1.	Classification of Se	equences. (a), (b), (c	) and (d) refers to l	Fig. 2. ①, ②, ③ as	nd ④ designate the c	lassification of sequ	ences.
classi- fication	operation modes sequence of operation modes	(c)	(a)	(b)	(a)	(b)	(c)	(d)
	sequence		<del></del>	<b>→</b> ○				
1	boundary condition	wt:	$\begin{aligned} \omega t_1(=0) & \omega t = 0 \\ ib = 0 & ib = 0 \\ ic = iL & -ic = iL \\ = ILt_1 & = ILt_1 \\ = ILt_0 \end{aligned}$	$ \begin{array}{cccc} \bullet t_1 & & \omega t : \\ -i_c \approx i_L & -i_c \approx i_L \\ & = ILt_2 & = ILt_2 \\ & = ILt_0 \end{array} $	$=\omega t_{\rm s}(=\pi)$			
2	sequence	<del>-&gt;</del>	<del>&gt;</del> O	<del>-&gt;</del> O				<del>&gt;</del> O
	of = boundary condition	ib=0 $if=0$	$i_c = 0 \qquad i_b = 0$ $i_b = i_L \qquad -i_c = i_L$ $= I_{Lt_1} \qquad = I_{Lt_2}$		+ ω <b>t</b> 3		wi :	$\begin{aligned} \omega t_0 &= \omega t_1 \\ if &= iL & if &= iL \\ &= ILt_1 &= ILt_0 \end{aligned}$
3	sequence	<del>\</del>	<del></del>				<del>&gt;&gt;</del>	<del></del>
	ωf = boundary condition	ib=0 $if=0$		- mf <sub>2</sub>		1	$iL = ILt_2$ $ib = 0$ $if = iL$ $= ILt_3$	$ \begin{array}{ccc} \omega t_0 & \omega t \\ if = iL & if = iL \\ = ILt_0 & = ILt_0 \end{array} $
	sequence	<del>\</del>	<b>→</b>	<del>-&gt;</del>	<del></del>	<b>&gt;</b>		<del></del>
4	of= boundary condition	ib=0 $if=0$	1 : 1	$-i_c=i_L$ $-i_c=i_L$	$ib=0$ $ib=0$ $ib=0$ $-ic=iL$ $=ILt_1$ $=ILt_2$			$if = iL \qquad if = iL$ $= I_{Lt_5} \qquad = I_{Lt_0}$

in the mode (d)

$$Ri_{L} + L \frac{di_{L}}{dt} = 0 (9)$$

$$i_t = i_L \tag{10}$$

where  $E_m$  is the peak value of the source voltage,  $\theta$  is the firing angle,  $i_s$  is the a-c current,  $i_L$  is the load d-c current,  $i_C$  is the capacitance current,  $i_f$  is the current through the freewheeling diode  $D_f$ .

The sequences of the operation modes in a half cycle, that is, from the instant when one SCR is fired until the other will be fired, is classified, as shown in Table 1. In the operation of the classification ①, the d-c voltage does not come to zero in a half cycle and the free-wheeling diode does not conduct all the way. In the a-c side, the resonant current flows through  $L_s$ ,  $R_s$  and C.

However, in the classification  $② \sim ④$ , since  $D_{\mathcal{T}}$  conducts always once in a half cycle, commutating periods are in existence just after one SCR has turned to conducting. The d-c voltage is zero when  $D_{\mathcal{T}}$  is conducting. Therefore, these classifications appear when capacitance is not very large.

Classification ② expresses the operation that added to ① with both the commutating period and the  $D_f$ -conducting period. For the purpose of comprehension, the wave forms of each branch of the classification ② are shown in Fig. 3.

Classification ③ expresses the operation that the a-c current flows like that of an inductive load, that is,  $i_s$  keeps flowing even after the source voltage drops to zero in the mode (a).

Classification 4 is, so to speak, the transition period operation, which suddenly appears as the firing angle  $\theta$  increases in the state of the classification 2. If  $\theta$  increases further more, this will shift to the classification 3. The a-c current flows discontinuously in a half cycle. Theoretically, discontinuous a-c currents may appear more than 2 times in a half cycle. This operation depends largely upon the SCR's grid input wave form. If one pulse input is added to the gates of SCRs, this phenomenon does not occur. Fig. 4 shows the wave forms of the classification 4; the a-c current, the capacitance current and the d-c voltage wave forms.

# § 3. Control characteristics

Control characteristics

The control characteristics of the d-c voltage,

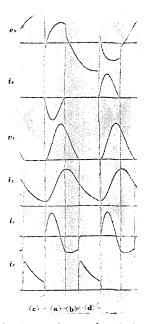


Fig. 3. Wave forms of each branch, corresponding to the classification ② (cf. Table 1).

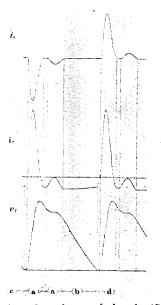
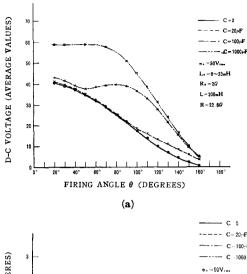
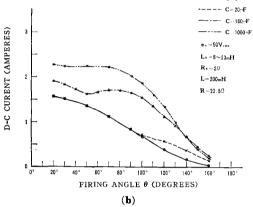


Fig. 4. Wave forms of the classification (4) (cf. Table 1).

th dc current and the acc current as functions of the firing angles are shown in Fig. 5 (a), (b) and (c). Where, values of capacitance were changed while the other circuit constants kept unchanged. From the inspection, it can be seen that

1) the output voltage and current increase





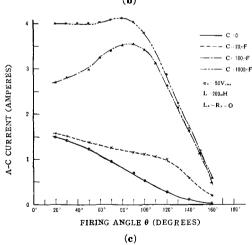


Fig. 5. Control characteristics.
(a); d-c voltage (average), (b); d-c current (average) and (c); a-c current (rms).

#### when capacitance is present.

2) the control characteristic curves are less smooth than that of the case that capacitance is not present. This reduction of smoothness originates in the transfer of the sequences of operation modes, as firing angles vary.

- 3) as capacitance will be large, the output grows large all over firing angles.
- 4) reasons of the increased output are: the load impedance becomes small when capacitance is present; in the a-c side, the presence of capacitance results more or less in the resonant current and so its peak value increases. It should be noted that if capacitance is connected for smoothing the d-c voltage, it is only the source impedance that suppresses the peak value of the capacitance charging current.

## Control limitation

- 1) The controlled range will become somewhat limited when capacitance is present in the operation of the classification (1).
- 2) The d-c voltage is positive all the way in classification ①. When SCRs are not biased forward, they can not be fired. In the operation of the classification ①, the d-c voltage has some value. So if the a-c voltage is not larger than the d-c voltage, SCRs does not conduct in the succeeding half cycle.

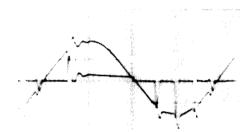


Photo 1. Wave forms of the a-c voltage and current. In the former half cycle, SCR's grid input is rectangular. In the latter half cycle, four pulses are added.

#### Input waveform

Although the presence of capacitance makes SCRs conduction initiate securely, the a-c current can flow oscillatory. This phenomenon results in the discontinuous a-c current. This operation, however, depends largely upon the SCR's grid input wave form. Photo 1 shows the bridge input a-c voltage and the a-c current wave forms. In the former half cycle, the rectangular grid input is provided to the SCR. In the latter, the pulse input is. In this photo, however, the pulses which were oscillated by the UJT pulse generator were added, and consequently four pulses appeared. Depending on input wave forms, the a-c current and the d-c

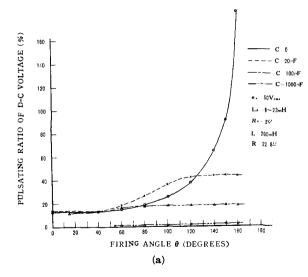
output characteristics vary. Thus, in order to keep smoothness of the control characteristics, it is desirable that the grid input wave form is rectangular.

## § 4. Pulsation and Oscillation

Pulsating ratio

Fig. 6 shows pulsating ratio of the d-c voltage and the d-c current. Capacitance influences upon the pulsating ratio as following.

- 1) The pulsating ratio becomes smaller due to capacitance.
- 2) The larger capacitance, the smaller pulsating ratio.
- 3) In Fig, 6 it is also shown that the pulsating ratio of the d-c voltage grows larger under a certain condition than that of no capacitance. This results from the a-c oscillating current.



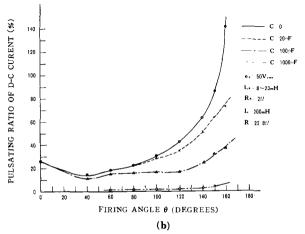


Fig. 6. Pulsating ratio.(a); d-c voltage and (b); d-c current.

Smoothing inductance

If capacitance is used with inductance, to smooth the d-c voltage, the bridge a-c voltage and the load voltage becomes different in an instantaneous value. When the bridge d-c voltage reduces to zero, the load voltage is not necessarily zero, which means that a new operation mode takes place. So the smoothing inductance can not be taken as part of the source impedance. All SCRs bridge connection and the half-controlled bridge connection operate in different ways, because the function of the freewheeling diode differs from that of no smoothing inductance. Thus, analysis of the circuit operation must be done respectively.

Fig. 7 shows the pulsating ratios of the d-c currents when the smoothing inductance was put into both d-c side and a-c side. It it clear

that the operation modes and also their sequences are different.

#### § 5. Power Factor

When SCRs are used to control the output voltage, the load current will lag the source voltage, and consequently the power factor will reduce more and more as the firing angle increases. Photo 2 shows the phase relation between the source voltage and current. Presence of capacitance will generally reduce the period of current conduction It can be seen from Fig. 8 that this results in the reduction of the power factor. From the inspection, when capacitance is 1,000  $\mu$ F, the reduction of the power factor varies smoothly as the firing angle increases. This is from the fact that the sequence of the operation modes is only of the classification (1) all the way. It is noted that in this case, under 70 degrees of the firing angle, the control function is lost

## § 6. Counter emf

When counter emf load is connected, the current can flow through SCRs only when the a-c source voltage is greater than counter emf voltage. The range of the possible firing of SCRs will be limited more narrow than that of being without counter emf voltage. The output current flows through the energy subtracted from the source voltage by

counter emf and diminishes to some degree, but the output voltage can grow larger (see photo 3 (a) and (b)). Further more, when the counter emf exceeds the voltage due to the energy trapped in the inductance, the d-c current will flow back from the d-c load to capacitance reversely and consequently will capacitance be charged. These phenomena are particular ones resulting from the presence of capacitance.

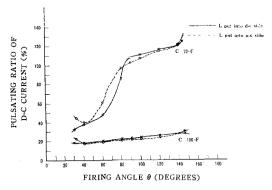


Fig. 7. Pulsating ratio when a smoothing inductance is added. solid line; when the inductance is added to the d-c side (as smoothing inductance). dotted line; when the inductance is added to the a-c side.

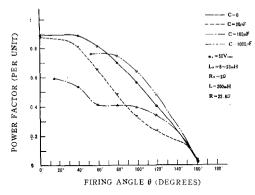


Fig. 8. Power factor, as functions of firing angle. Parameter is capacitance.

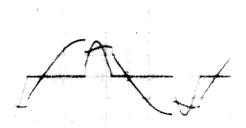


Photo 2. Wave forms of the bridge a-c voltage and the a-c current.

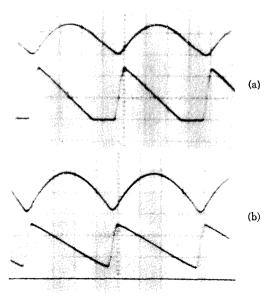


Photo 3. Wave forms of the d-c voltage and current.

(a); without counter emf and (b); added counter emf. In each case, the upper trace is the d-c current and the lower trace is the d-c voltage.

## § 7. Conclusion

The circuit operation of the single-phase bridge connection with capacitance in parallel with the inductive load can be divided into four modes. The sequence of the operation modes can express all the performance. The presence of capacitance will result in the following,

- 1) the d-c voltage and current increases,
- 2) the control characteristics are not smooth.
- 3) pulsating ratio becomes small,
- 4) the power factor will not be always improved, and
- 5) the controlled range will be limited.

The authors are not in a position to tell the design details on SCRs rectifier connections, but it seems that it cannot be always expected to result in good contribution that capacitance is put into d-c side on purpose.

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