# HYBRID INTEGRATED PLATFORM OF PDMS MICROFLUIDICS AND SILICA CAPILLARY FOR EFFECTIVE CE AND ESI-MS COUPLING

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#### **ABSTRACT**

We present an effective hybrid integration of PDMS microfluidic devices and fused silica capillaries. These hybrid microfluidic Integrated PDMS and Silica Capillary (iPSC) modules exhibit a novel architecture and method for leakage free CE sample injection requiring only a single high voltage source. Use of the iPSC devices is based on a modular approach which allows the capillary to be reused over 1,000 times whilst replacing the fluidics below it for different experiments. Integrating fused silica capillaries with PDMS microfluidics allows the direct application of a wide variety of well established conventional CE protocols for complex analyte separations and ESI-MS coupling, allowing users to focus on the sample analysis rather than the development of new separation protocols. The iPSC fabrication method is simple (3 steps) and quick (7 min).

## **KEYWORDS**

CE, Microfluidics, ESI-MS, Silica Capillary, Integration.

#### INTRODUCTION

Microfluidic systems provide a powerful platform for bioanalytical assays. By embedding micro-channels in a chip the sample preparation procedures and the assay formats can be miniaturized. The often mentioned advantages of the miniaturized assays include small reagent and cell volumes, quick reaction times, low cost, as well as the potential for high-level process parallelization, automation and integration. PDMS casting [1] is a convenient and widely accepted way of constructing such microfluidic systems. PDMS casting constitutes a simple and rapid fabrication technique that can replicate with high fidelity micron scale features, optical transparency (down to 280 nm), non toxicity, compatibility to biological samples [2], chemical inertness, reversible and irreversible bonding to itself or other materials such as silicon, glass, SiO2, glassy carbon and quartz. For bio assays, such as PCR, cell culture, electrophoresis, to be successfully implemented on PDMS microfluidic devices, adequate surface properties are required. This is an especially critical issue when trying to perform capillary electrophoresis (CE) within a PDMS

microfluidic device. The highly hydrophobic surface of unmodified PDMS channels usually suffers from analyte adsorption and unstable electroosmotic flow (EOF). These surface coating issues generally reduce the reproducibility and efficiency of separations and thus hinder the use of CE within PDMS devices.

On the other hand electrophoresis using fused silica capillaries has several advantages over PDMS microchannels. Since its emergence [3] the conventional CE has passed through a rapid developmental phase and a wide body of knowledge and separation protocols are available [4]. Furthermore, various kinds of surface treated fused silica capillaries are commercially available for specialized applications. Monolithically integrating fused silica capillaries with PDMS microfluidics would create a powerful combination that harnesses the advantages of standard fused silica CE with those of highly integrated PDMS microfluidics.

Various strategies have been proposed [5] to connect capillaries with microfluidic devices in substrates such as silicon [6], glass [7] and plastic [8]. Few methods have been shown to work in PDMS. In one approach a specially machined casing and mould with inserted wires and fused silica capillaries is used during the casting process to create capillary connection ports [9] while in another approach holes are punched and capillaries directly inserted into the PDMS device [10]. Although these techniques have been successful in creating fluidic interconnects, the monolithic integration of silica capillaries with PDMS microfluidic devices has not been demonstrated.

In this article, we present a new, simple (3 step) and quick (7 min) method for integrating PDMS microfluidic devices with fused silica capillaries. These integrated PDMS silica capillary (iPSC) modules exhibit a novel architecture and method for leakage free CE sample injection using a single high voltage source. The fabrication of the iPSC devices is based on a modular approach which allows the capillary layer to be reused over 1000 times whilst replacing the fluidics layer for different experiments. Furthermore, due to the silica capillary integration, the device can be easily coupled to ESI-MS with zero dead volume and requiring no non-standard custom made devices or parts.

#### FABRICATION OF IPSC

The PDMS module incorporating the silica capillary was fabricated by spin coating a mixture of curing agent and the prepolymer (1:10 w/w) on a Si wafer at 500 rpm for 10 s, to obtain ~400 µm thick layer (Fig. 1A). A fused silica capillary of desired length was obtained and a detection window was made by incinerating the polyimide coating on the capillary. Then water was filled into the capillary through capillary action, just by dipping one end of the capillary in a drop of water. The water filled capillary was placed on the PDMS coated Si wafer and allowed to sink into the PDMS, and the wafer was placed on a hot plate set to 75 °C for 5 minutes. The water inside the capillary prevented the PDMS from entering into the capillary. Due to the expansion of water at elevated temperatures, the contained water formed small bubbles at the openings on either side of the capillary (Fig. 1C) ensuring that the capillary is not blocked by PDMS during the curing process, thus allowing to route fluids directly into the capillary core. Holes were punched with a 1-mm OD flat-tip needle in the PDMS layer to access the capillary inlet and outlet. If required one end of the capillary can be easily cut out of the surrounding PDMS since there is no irreversible bonding between the capillary and the surrounding PDMS. The iPSC module is then used by mounting it on to a fluidics chip and placing the iPSC inlet port over the fluidics chip sample outlet.

## **CE OPERATION**

The process of sample injection, and separation is shown in Fig 2. (steps 1-5). In our operation method, sample leakage is prevented by cleanly removing the remaining part of the sample from the inlet access port with the continuous flow of buffer from the PDMS channel running in the fluidic chip as depicted in Fig. 2

- Step 1. The iPSC CE module is mounted onto the fluidic chip by placing the iPSC inlet over the fluidics chip sample outlet. Then the capillary is filled with buffer.
- Step 2. Sample is introduced in the inlet port.
- Step 3. Voltage is applied across the inlet and the outlet, and sample is introduced into the capillary by electroosmotic flow.
- Step 4. Plenty of buffer is flown into the port for washing and discharged to waste directly from the injection port. Fluidic chips were fabricated using standard soft photolithography[1] (other fabrication methods are also compatible with the plug and play module).
- Step 5. High voltage is applied for separation and the separated analytes are detected by fluorescence monitoring under a microscope.

Platinum electrodes were used with a high voltage source HVS448 (LabSmith, Livermore, CA, USA). Electropherograms were obtained by using an inverted

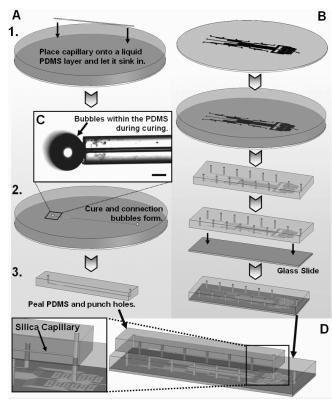


Fig. 1 (A) 3 step fabrication process of an iPMSC module. (B) Standard soft-lithography replication process for manufacturing complex microfluidic devices for sample treatment and preparation (C) Micrograph showing the bubble formed at the capillary edge after PDMS curing, scale bar 150 µm. (D) Mounting the iPMSC module onto the fluidic device.

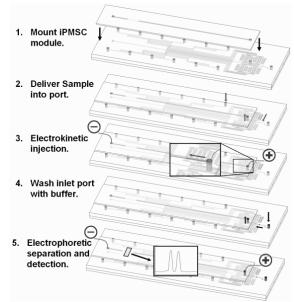


Fig. 2 Device mounting and operation (Step 2-3) Precise electrokinetic injection of the sample is done by applying a voltage on the electrodes (Step 4-5) Using hydrostatic flow the sample port is washed and then the sample plug is CE separated.

fluorescent microscope (Olympus IX81) focused on the capillary window with a 10X objective. The capillary window was exposed to 492 nm excitation light (excitation filter BP492/18 and a xenon light source CellR MT20, Olympus) for 80 ms and the 530 nm fluorescent light was sampled through a filtercube (U-MF2, Olympus) with a CDD sensor (Hamamatsu C4742-80-12AG). The image sampling rate was 4 Hz. After the CE separation the images were analysed with the CellR software and the fluorescent kinetics plotted. A new capillary was flushed with 1 M NaOH for 10 min by connecting the outlet to a vacuum line and placing a 50  $\mu$ L drop of 1 M NaOH at the capillary inlet followed by 5 min flushing with water, and then 5 min with 0.1-M NaOH. Finally the separation buffer was loaded.

## **RESULTS: ON CHIP CE**

To demonstrate the reliability of the iPSC CE module we separated a mixture of two analytes FL and DCF using the conditions described previously [11] (Fig. 3). A peak migration time with a standard deviation of 3.9% (n = 3) and a peak area with a standard deviation of 4.0% (n = 3) were obtained. These relative standard deviations can be further lowered by surface regeneration between each run, e.g. by flushing the capillary with 0.1 M NaOH. As seen in Fig. 3 the flat baseline shows that there was no sample leakage or adsorption.

To demonstrate the module's ability to separate complex analytes by using conventional CE protocols we separated green fluorescent protein (GFP) iso-forms (Fig. 4). The separation protocol was adopted from a previously described (conventional CE) method [12]. GFP was purified off chip from *E-Coli* lysate. A single iPSC CE module was used to perform more than 100 separation experiments. To achieve this, the capillary was flushed with 0.1 M NaOH for surface regeneration.

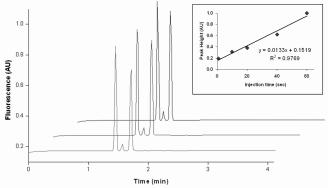


Fig. 3 Reproducible separation of FL and DCF. Inset shows the relation between the peak height and the injection time.

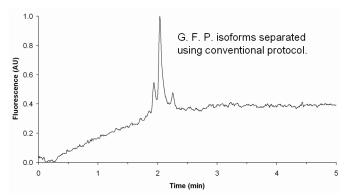


Fig. 4 Separation of green fluorescent protein (GFP) isoforms.

# DISCUSSION

The fabrication of the iPSC module can be done within 7 minutes in a conventional lab without micro-fabrication facilities and requires only simple widely available equipment and materials. During the fabrication of the iPSC module uncured PMDS is prevented from entering the capillary by loading it with water which expands and evaporates during the PDMS curing process. This is advantageous since water droplets and vapour bubbles are formed at the edges of the capillary (Fig. 1F) which, after punching the holes, efficiently route fluids from the fluidics layer directly into the capillary core. The bubble size or formation are not critical factors since the punching process after curing eventually defines the connection channel at high precision. For special applications commercially available pre-coated silica capillaries can also be integrated. Finally the plug-and-play iPSC module has to be mounted onto the fluidic chip with minimal alignment complications. Since reversible bonding is sufficient a single capillary may be reused more than a 1000 times which allows for the fluidic chip to be replaced in each new experiment.

Since the capillary can be made to extend out of the main monolithic chip area other detection techniques such as electrical contactless detection[13] or UV detection can be efficiently and easily coupled by using commercially available detector modules and sliding the extended capillary into the sensor module. Furthermore using standard protocols this capillary extension can be easily coupled to ESI-MS with zero dead volume. Finally the integration of fused silica capillaries with PDMS microfluidics allows the direct application of a wide variety of well established conventional CE protocols for complex analyte separations and ESI-MS coupling, allowing users to focus on the sample analysis rather than the development of new separation protocols.

## **CONCLUSIONS**

In this work we have shown a novel method for integrating the advantages of microfluidics with conventional CE into a PDMS microdevice. In addition,

the on chip CE operation within a monolithic PDMS microdevice method solves important problems such as surface characteristics for low adsorption and stable EOF and leakage free and reproducible injection. The electrode and voltage source requirements were reduced with respect to the popular double-T format CE chips.

The modular approach allows the iPSC module to be reused over 1000 times whilst replacing the fluidic chip for different experiments. Additionally, by extending the capillary out of the chip, these devices can be easily coupled with ESI-MS with zero dead volume and minimal fabrication complexity and equipment requirement. This enables the fabrication of monolithic sample-in-answer-out systems with mass spectrometric accuracy. Finally, since conventional CE capillaries are used, this allows the direct application of the wide variety of well established existing conventional CE protocols for complex analyte separations and electroselectrospray ionization mass spectrometry (ESI-MS) coupling, permitting users to focus on the sample analysis rather than the development of new separation protocols.

## **ACKNOWLEDGEMENTS**

The authors would like to thank Dr. Niamh Gilmartin for providing the green fluorescent protein. This work was supported by the Science Foundation Ireland under Grant No. 05/CE3/B754.

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